

Reading Quiz 5

1. How many bits are needed to properly store the sum of two 32-bit numbers?

33 bits, assuming we want to keep track of the carry bit.

2. Solve the following binary addition problems; provide your answer in base-2 and base-10.

$\begin{array}{r} 1000\ 1010 \\ + 1110\ 1001 \\ \hline 101110011 \\ \\ 371 \end{array}$	$\begin{array}{r} 1010\ 1010 \\ + 0101\ 0101 \\ \hline 011111111 \\ \\ 255 \end{array}$	$\begin{array}{r} 1111\ 1111 \\ + 1000\ 0001 \\ \hline 110000000 \\ \\ 384 \end{array}$	$\begin{array}{r} 0001\ 1000 \\ + 1011\ 1101 \\ \hline 011010101 \\ \\ 213 \end{array}$
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3. In your own words, explain why a half-adder is called a half-adder.

Because it can only do half of the problem and while it is fine for adding single bit binary numbers, anything beyond that requires more the addition of 3 bits which a half adder can't do.

4. What are the three steps for subtracting two numbers without borrowing? (assume minuend > subtrahend)

First subtract the subtrahend from a string of 9's the same length as the subtrahend in number of digits to get the 9's complement. Add the 9's complement of the subtrahend to the minuend. Add 1 and subtract 1 followed by the relevant number of zeros to be the same number of digits (e.g. 1000 for 4 digits).

5. Distill a simple formula for subtracting without borrowing (hint: pages 144/145). Solve the following base-10 subtraction problem using that formula, showing your work: 2014 - 1976.

$$\text{minuend} + (9999 - \text{subtrahend}) + 1 - 10000$$

$$(2014) + (9999 - 1976) + 1 - 10000$$

$$= (2014) + 8023 + 1 - 10000$$

$$= 38$$

6. What is a simple way to get the one's complement of any binary number? (hint: doesn't require math)

Flip all the bits.

7. Solve the following binary subtraction problems, showing the use of one's complements. Show each of the three steps of your work.

$\begin{array}{r} 1010\ 1010 \\ - 0101\ 0101 \\ \hline 001010101 \end{array}$	$\begin{array}{r} 1111\ 1111 \\ - 1000\ 0001 \\ \hline 001111110 \end{array}$	$\begin{array}{r} 0000\ 1000 \\ - 0000\ 0100 \\ \hline 000000100 \end{array}$	$\begin{array}{r} 1000\ 1010 \\ - 1110\ 1001 \\ \hline 101011110 \end{array}$	$\begin{array}{r} 0001\ 1000 \\ - 1011\ 1101 \\ \hline 110100100 \end{array}$
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STEP 1:

1111 1111 - 0101 0101 ----- 1010 1010	1111 1111 - 1000 0001 ----- 0111 1110	1111 1111 - 0000 0100 ----- 1111 1011	1111 1111 - 1110 1001 ----- 0001 0110	1111 1111 - 1011 1101 ----- 0100 0010
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STEP 2:

1010 1010 + 1010 1010 ----- 101010100	1111 1111 + 0111 1110 ----- 101111101	0000 1000 + 1111 1011 ----- 100000011	1000 1010 + 0001 0110 ----- 010100000	0001 1000 + 0100 0010 ----- 001011010
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STEP 3:

101010100 + 1 ----- 101010101	101111101 + 1 ----- 101111110	100000011 + 1 ----- 100000100	010100000 + 1 ----- 010100001	001011010 + 1 ----- 001011011
101010101 - 100000000 ----- 001010101	101111110 - 100000000 ----- 001111110	100000100 - 100000000 ----- 000000100	111111111 - 010100001 ----- 101011110	111111111 - 001011011 ----- 110100100

8. Using the adder diagram at the bottom of page 150, convince yourself that it works with the following problems. Note that this machine does not properly display negative numbers (p151 ¶3). Show your work by writing the numbers as they change through the circuitry, and show the use of the SUB switch/bit.

0000 1000 + 0000 0100	1111 1111 + 1000 0001	0000 1000 - 0000 0100
0000 0100 - 0000 1000		

9. The three steps in #2 above can be distilled into a simple rule when using ten's or two's complement to subtract two numbers (aka adding a negative number). "To subtract two decimal numbers, simply _____ the _____'s complement of the _____ to the _____." Hence, $65 - 138 \Rightarrow 65 + (-138) \Rightarrow 65 + \underline{\hspace{1cm}} = \underline{\hspace{1cm}}$ (or -).

"To subtract two decimal numbers, simply **add** the **10's's** complement of the **subtrahend** to the **minuend**." Hence, $65 - 138 \Rightarrow 65 + (-138) \Rightarrow 65 + (999-138+1) = 927$ (or -73).

10. What's the easy way to figure out the two's complement of a binary number?

Keep the rightmost 1 and all trailing zeros and flip the rest of the bits.