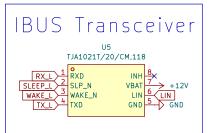
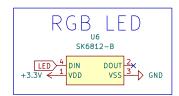
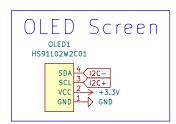


Notes: Strapping pins 8,9 ENABLE is pulled high in the module







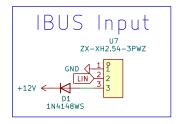
LD1117S33TR\_S0T223

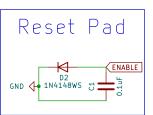
 $+5V \stackrel{3}{\longleftrightarrow} VI \stackrel{V0}{\longleftrightarrow} V0 \stackrel{2}{\longleftrightarrow} +3.3V$ 

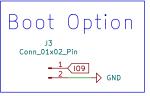
GND

Voltage Regulator

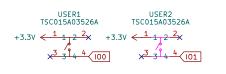
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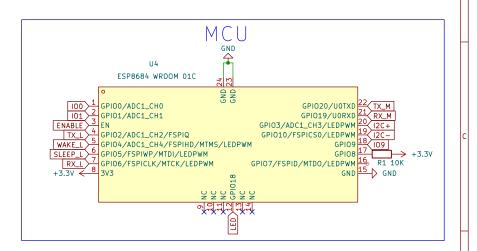






## User Input





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