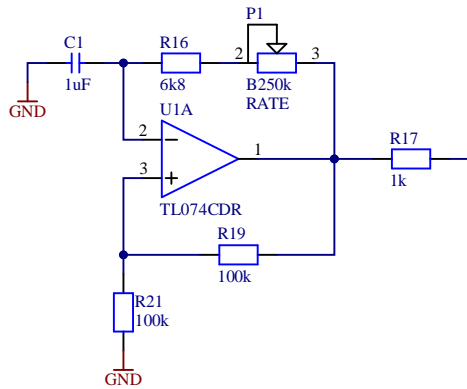
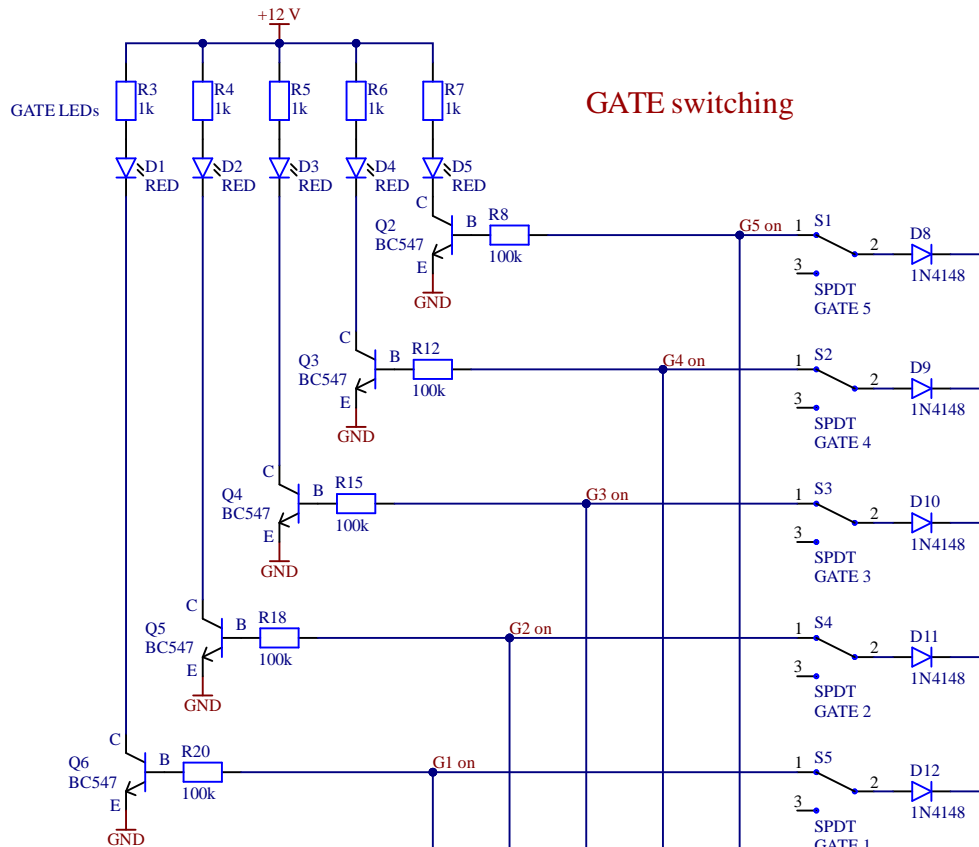


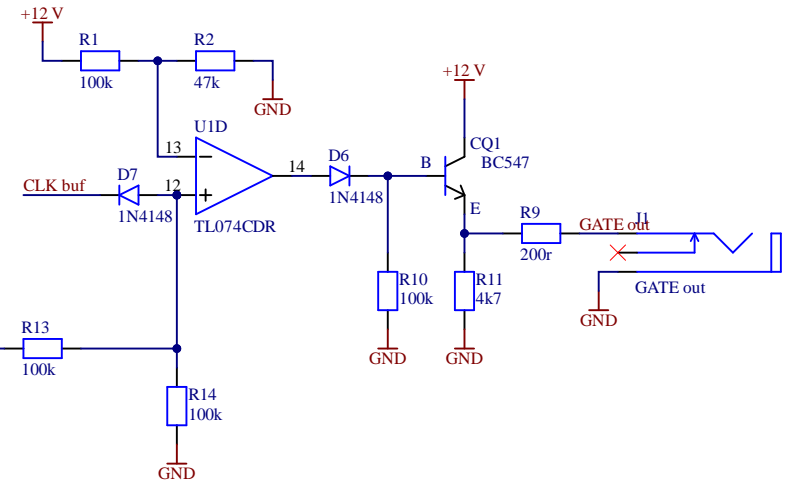
Internal CLK gen



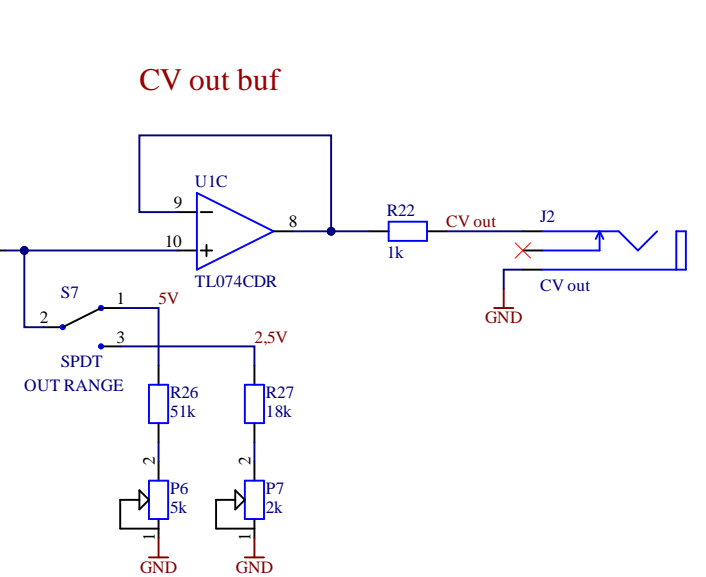
GATE switching



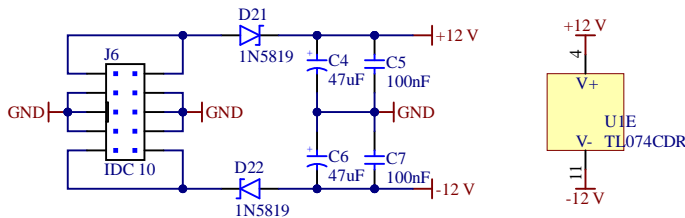
GATE out buf



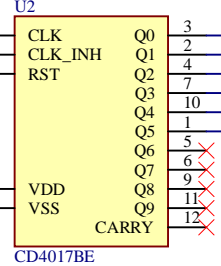
CV out buf



Power section



Counter



PROJECT:

5_step_seq.PrjPcb

SCHEMATIC:

Sequencer_board

DRAWN BY:

Jakub Orzechowski

REV: 1.0

DATE: 19.08.2025

FILENAME:

5_step_seq.SchDoc

SHEET:

1 out of 1