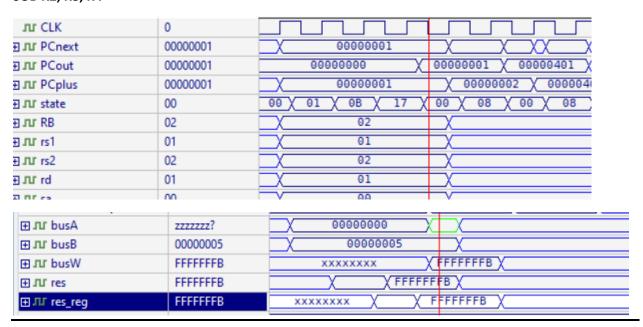
ADD R2, R3, R4:

Signal name	Value	8 16 24 32						
JUT CLK	1							
⊕	00000001	X 00000001						
⊞ лг PCout	00000001	00000000						
⊞ .rr PCplus	00000001	00000001						
∄	17	00 X 01 X 09 X 17						
⊞ .rr RB	00	xx X 00						
⊕ лл rs1	02	xx X 02						
⊞ лг rs2	00	xx X 00						
⊕ лг rd	03	xx X 93						
⊕ лг sa	ZZ							
⊞ JJ Function	01	xx X 01						
лг RegWr	1							
лг JalSig	0							
■ ⊕ Jur PCPlusTemp	00000002	0000001 X	0000					
⊞.TJ busA	00000002	X 00000002 X						
⊕ лг busB	00000000	χ 00000000 χ						
⊕ лг busW	00000002	xxxxxxxx X 00000	9002					
⊕ ЛГ res	00000002	xxxxxxx	X					
	00000002	xxxxxxx	02					

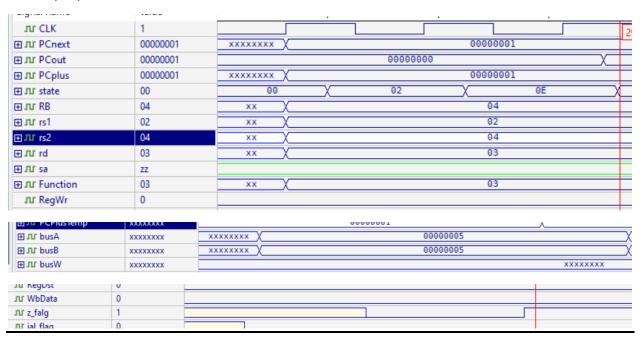
SUB R2, R3, R4



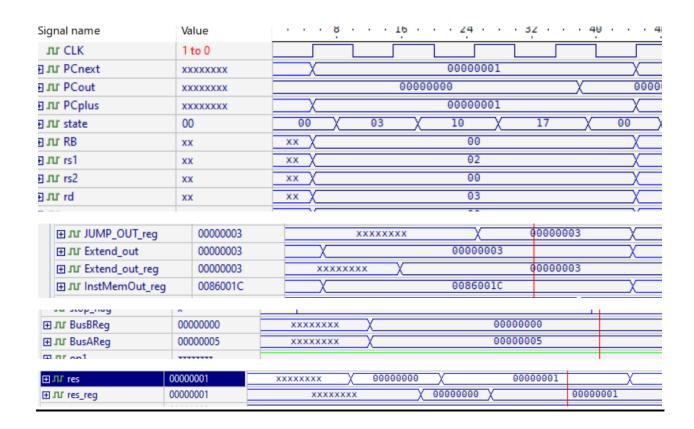
AND R2, R3, R4

Signal name	value	7 77 77 77	
JU CLK	1		39 5
⊞ 	00000001	0000001	
⊞ лг PCout	00000001	0000000	X
⊞ лг PCplus	00000001	0000001	
	00	00 X 01 X 0A X 17	\supset
⊞ лư RB	00	xx X 00	
⊕ лư rs1	02	xx X 02	
⊞ лг rs2	00	xx X 00	
⊞ лт rd	03	xx X 03	
⊞ JU sa	ZZ		
⊞ J Function	00	xx X 00	
лг RegWr	1		
лг JalSig	0		
шта гололопр	COCCOCC		
⊞ лг busA	00000002	X 00000002	=
⊞ лг busB	00000000	00000000	=
⊞ лг busW	00000000	xxxxxxxx	6
⊞ лт res	00000000	xxxxxxx X 00000000	
⊞ JI J res_reg	00000000	xxxxxxx	00000000

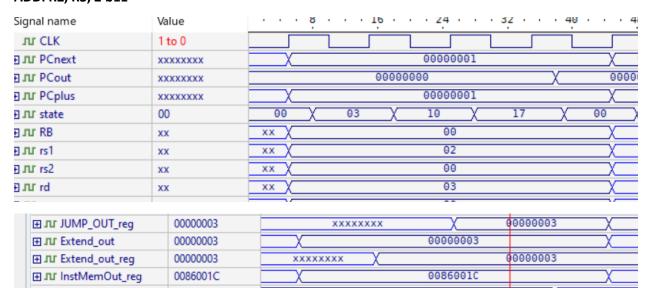
CMP R2, R3, R4



ANDI R2, R3, 2'b11

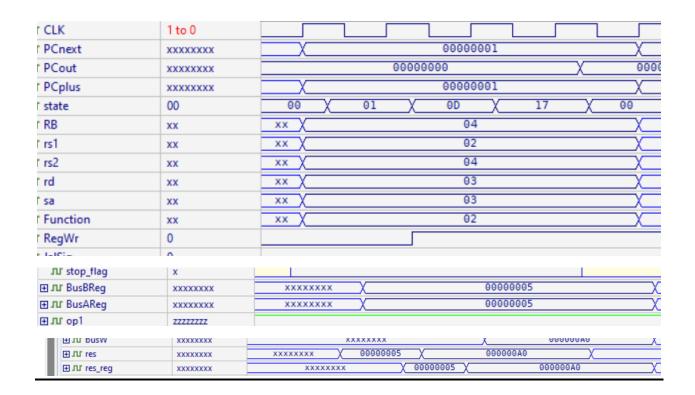


ADDI R2, R3, 2'b11

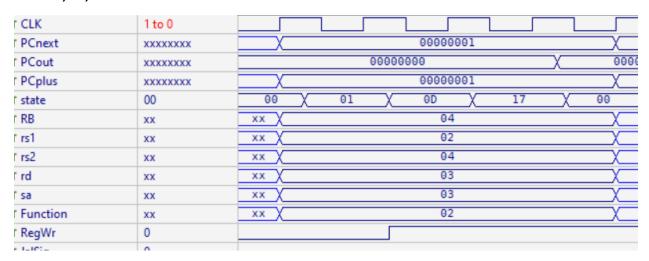


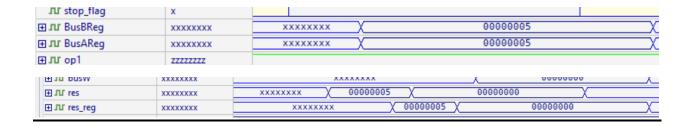
⊕ ЛГ res	xxxxxxxx	xxxxxxxx	00000000	X	00000008	X
⊞ лг res_reg	xxxxxxx	xxxxxxx	()	00000000	00000008	X
	00000000	-				0000000

SLLV R2, R3, R4.



SLRV R2, R3, R4.





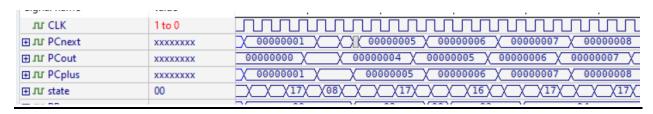
SLL R2, R3, 3.

_								
⊕ лг busW	xxxxxxx	XXXXXXX			X	00000028	X	
⊞ ЛГ res	xxxxxxx	xxxxxxxx	00000005		00000028			
⊞ лг res_reg	xxxxxxxx	xxxxxxx	X	00000005 X		00000028		X

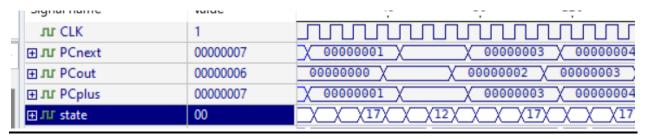
SLR R2, R3, 3.



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BEQ



Jal

