

## Part 1 - Theory

*Chat GPT is allowed. Preparation time:  $\sim 10$  sec.*

Why is BEAMS the best ?

## Part 2 - Project

### Question 1 - Moore's law

What would you change to make this a fonctionning RISC-V superscalar full-pipeline multi-core processor ?

You only have 69 lines

```
1 library ieee;  
2 use ieee.std_logic_1164.ALL;  
3  
4 entity myEntity is Port (  
5     d_in : in std_logic;  
6     d_out : in std_logic  
7 ); end myEntity;  
8  
9 architecture arch of myEntity is  
10  
11 begin  
12  
13     d_in <= d_out;  
14  
15 end arch;
```



