

Revision 3: Corrected ISP header orientation and changed references to be more consistent  
Revision 2: Increased width of power traces and added power plane on top copper  
Revision 1: Initial Design

**Designed by Ryan Davis**

Sheet:

File: NumitronClock.kicad\_pcb

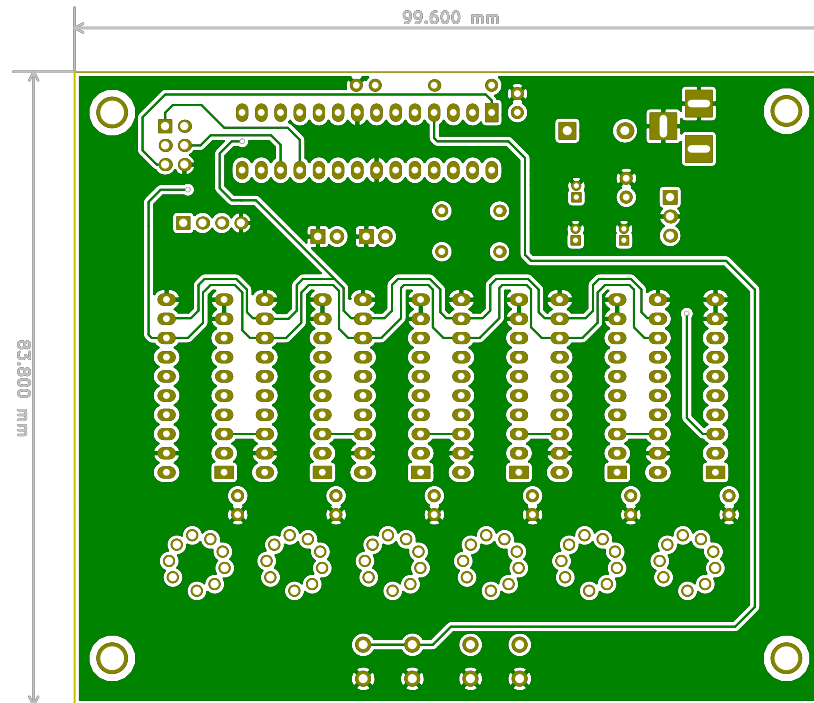
**Title: Numitron Clock PCB**

Size: USLetter Date: 2017-05-09

KiCad E.D.A. kicad 4.0.5

**Rev: 3**

Id: 1/1



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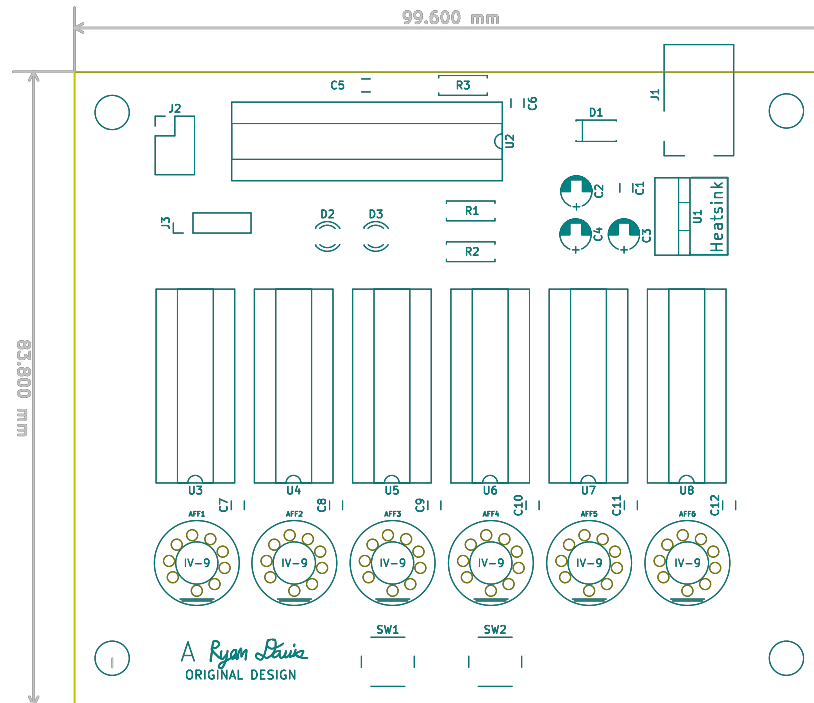
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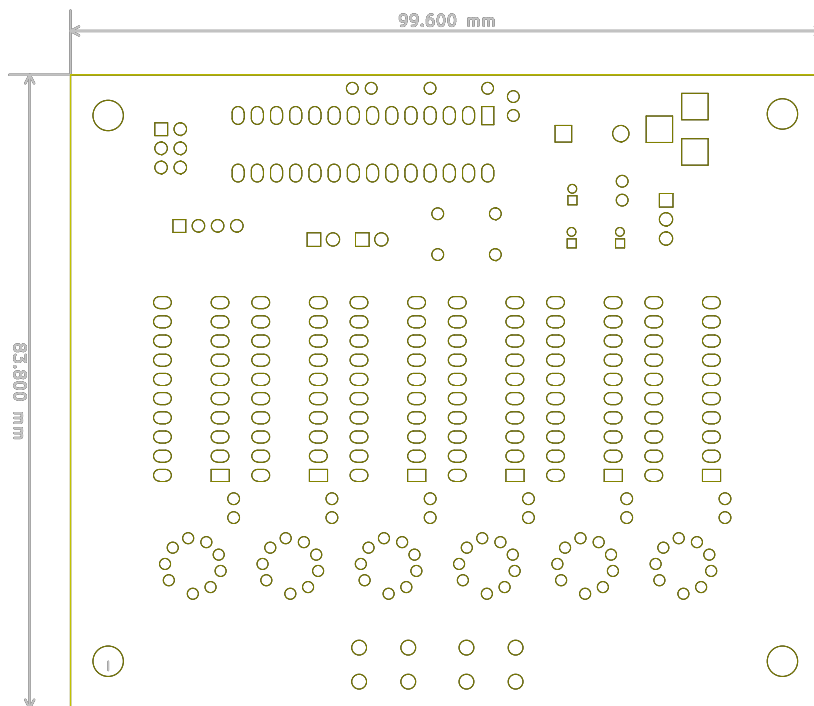
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