

Op-Amp Characteristics

(a) Voltage Follower

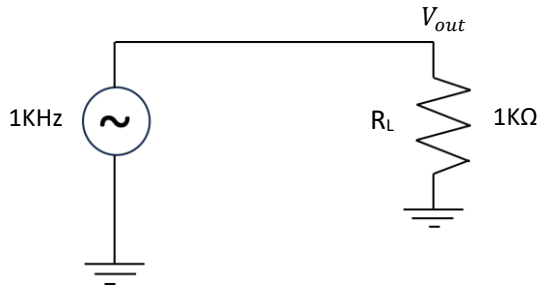


Figure 1(a)

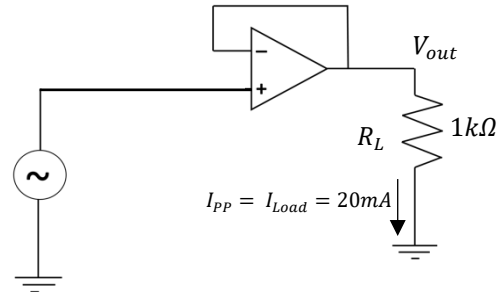


Figure 1(b)

1. Apply a 1 kHz sinusoidal signal to the 1k load shown in Figure 1(a)
Note: Make sure the signal generator output amplitude is at its minimum.
2. Now increase the signal generator output amplitude until you get 20 mA peak-to-peak current through the load resistor R_L .
Hint: Measure the ac-voltage across R_L , instead of measuring the current through it. This will allow you to find the peak-to-peak current through the load resistor without the aid of an ammeter. You can use multi-meter for this purpose, remember that it shows you the RMS value of the voltage when put it into the ac mode.
3. From the data you obtained from step 2, find the peak value of the current drawn from the signal source for step 2.
4. Now connect the circuit shown in Figure 1(b).
Note: Make sure you that you have adjusted the signal generator amplitude to its minimum before you connect the circuit.
5. Using a 1 kHz sinusoidal signal, repeat step2.

(b) Input offset Voltage

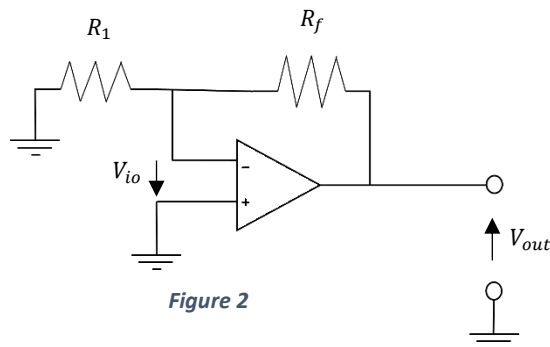


Figure 2

1. Derive an expression for V_{out} in terms of V_{io} , R_f and R_1 .
2. Connect the circuit by taking $R_f = 100k\Omega$ and $R_1 = 100\Omega$. (In case of unavailability use suitable replacements).
3. Measure the output voltage with respect to the ground. Hence calculate the input Offset Voltage of the Op-Amp.

(c) Frequency/Phase Response

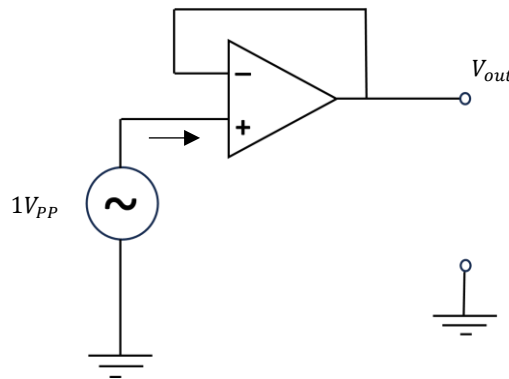


Figure 3

1. Connect the unity gain buffer circuit as shown in Figure 3.
2. Apply a 1V peak to peak sinusoidal signal to the non-inverting terminal.
3. Measure the input signal amplitude and the output signal amplitude for the following frequencies. Also measure the phase difference between Input and output signals. (20Hz 50Hz 100Hz 200Hz 500Hz 1kHz 5kHz 10kHz 50kHz 100kHz 200kHz 500kHz)
Note: You have to check and make sure that the amplitude of the signal generator output is at 1V peak to peak for each and every frequency mentioned above.
4. Then obtain the frequency response of the Op-Amp by plotting the **Gain Response** and the **Phase Response** on log-scaled plots.

(d) Input Bias Current and Input Offset Current

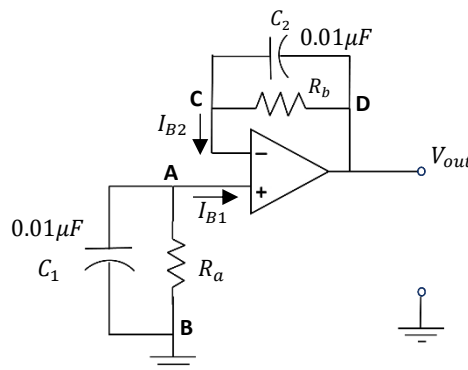


Figure 4

1. Connect the circuit as shown in Figure 4.
Note: Choose $R_a = R_b > 10M\Omega$. Normally the input bias current I_B , is in the range 100nA. So higher values of R_a and R_b will ensure that $I_B R_b > V_{io}$ so that the voltages created by bias currents are much greater than V_{io} .
2. What is the value of V_{out} when the terminals A and B are short circuited? Measure the value of V_{out} for this condition and calculate the value of I_{B2} .
3. What is the value of V_{out} when the terminals C and D are short circuited? Measure the value of V_{out} for this condition and calculate the value of I_{B1} .
4. From the values of I_{B1} and I_{B2} , calculate the value of input bias current I_B .
5. Find an expression for the input offset current in terms of I_{B1} and I_{B2} and find the input offset current of the Op-Amp used.

(e) Slew Rate

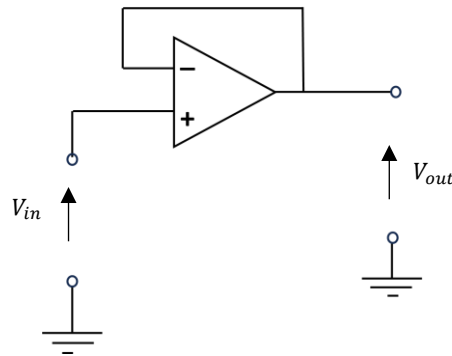


Figure 5

1. Connect the unity gain buffer circuit as shown in Figure 5.
2. Apply a 3V peak to peak sinusoidal signal to the non-inverting terminal.
3. Observe the output waveforms for the following frequencies.
20Hz 50Hz 100Hz 200Hz 500Hz 1kHz 5kHz 10kHz 50kHz 100kHz 200kHz 500kHz.
Explain in your own words what you have observed.
Hint: Consider changes of waveform shape, amplitude etc.
4. Now apply 6V peak to peak square wave signal to the non-inverting terminal.
5. Observe the output waveforms for the following frequencies.
20Hz 50Hz 100Hz 200Hz 500Hz 1kHz 5kHz 10kHz 50kHz 100kHz 200kHz 500kHz
Note: You have to check and make sure that the amplitude of the signal generator output is at 6V peak-to-peak for each and every frequency mentioned above.
 - (a) At the rising edge of the input waveform, find the time it takes for the output signal to reach its peak value.
 - (b) At the falling edge of the input waveform, find the time it takes for the output signal to reach its lowest value.
 - (c) Then calculate the temporal rate of change of the closed loop amplifier output voltage for each of above frequencies for both rising edge and falling edge cases.
 - (d) Then find the maximum rate of change of the output voltage, hence find the slew rate of the device.
Hint: It is the usual practice to specify the slower of the two rates (rising edge case or falling edge case) as the slewing rate of the device.
6. Comment on the waveform shapes you observed at the output for higher frequencies.

Discussion

1. Refer to the UA741 Op-Amp and find typical parameter values and responses and compare them with the values and responses you obtained in the practical.
2. Consider the sections (a) to (e) in the practical. List down practical situations where you need to consider the characteristics discussed in those sections.
3. What is frequency response of an amplifier? Explain briefly. Is there a difference between the terms 'frequency response' and the 'frequency spectrum'? If so, explain briefly.
4. What is the difference between the high frequency response and low frequency response of an amplifier?
5. What is meant by CMRR and PSRR values of an Op-Amp?