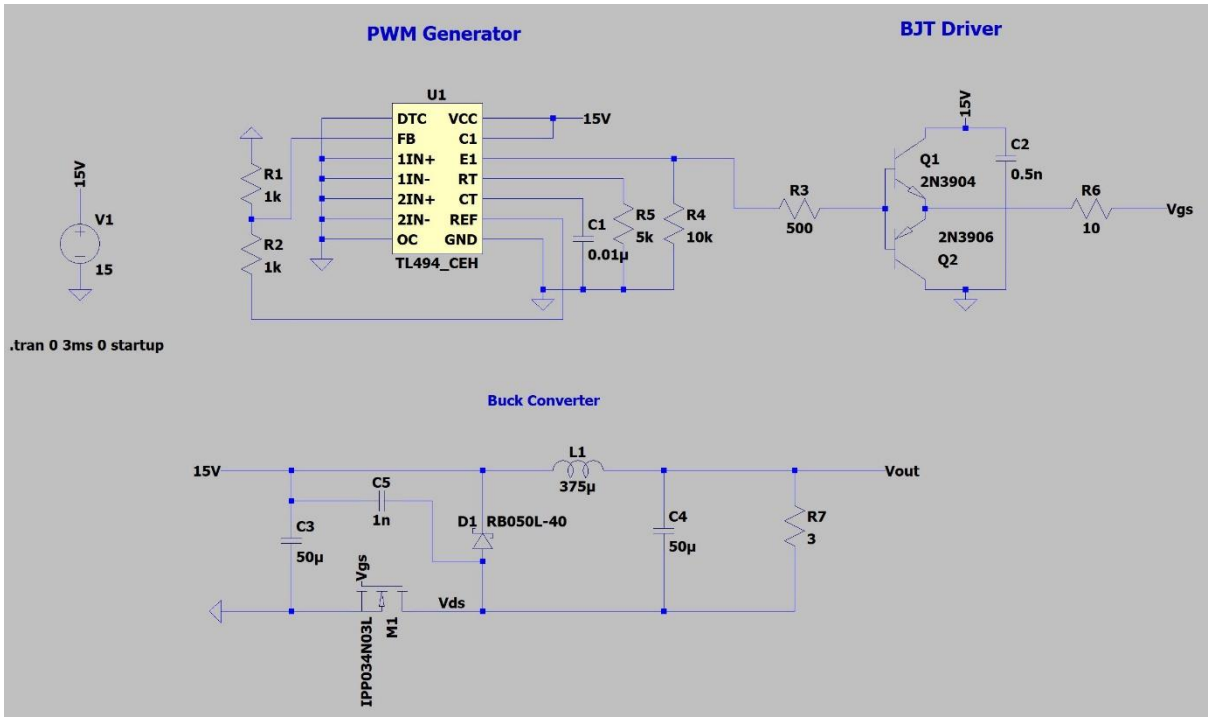
LTSpice Buck Converter Simulation

ENEL373 Power Electronics

Adam Finlayson

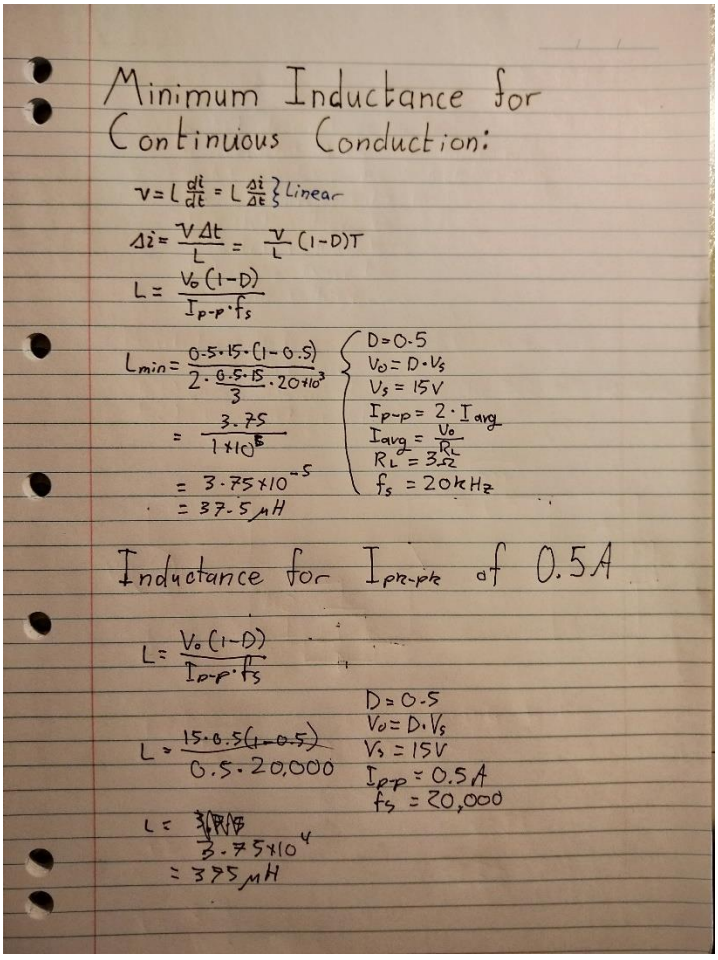
Simulated Circuit Diagram



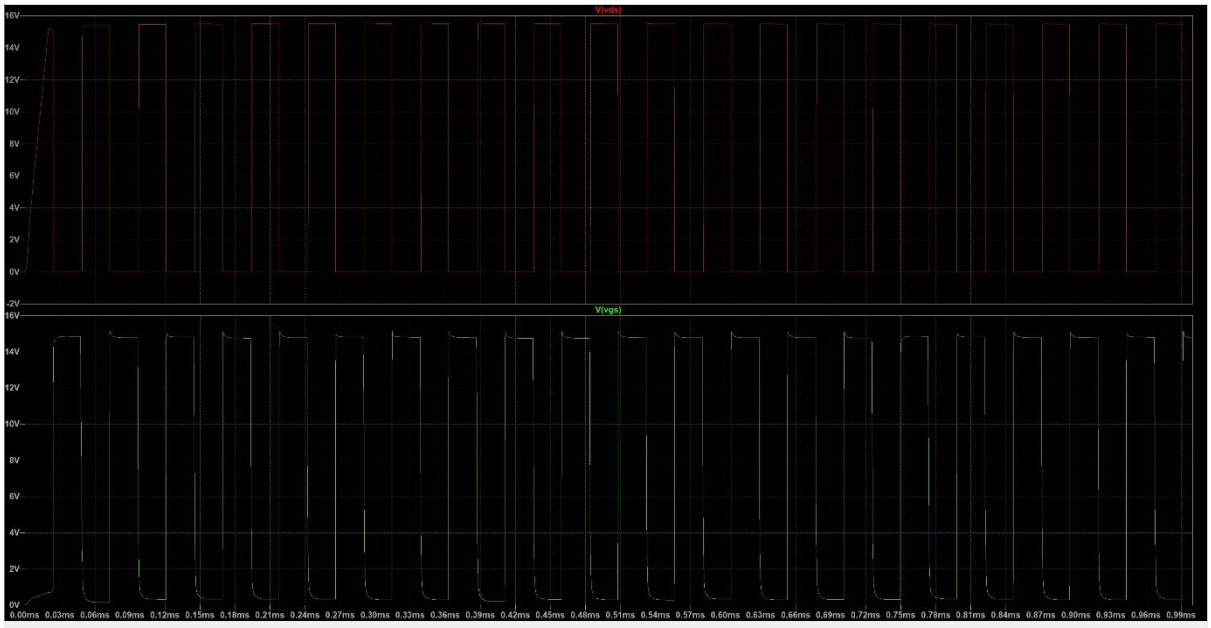
Analytical Solutions for Inductor Sizing

Left: Absolute Minimum L for Continuous Conduction

Right: L for I_{p-p} of 0.5A



MOSFET Vgs and Vds



Output Voltage