Ousama Jamal Eddin

CE - Student

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Motivated Student pursuing a CE degree. Passionate and punctual individual focused on teamwork and quality paired with outstanding communication and presentation abilities. Experienced in managing various projects and utilizing appropriate resources to meet strict deadlines.

Education

Student at FenerBahce University

- I am a second-year Computer Engineering Student at Fenerbahce University, As a dedicated student, my journey in the world of programming and computers has been nothing short of exhilarating. From the first lines of code I wrote to the countless hours spent delving into the intricacies of algorithms, my love for this field has only deepened with each passing day. Being a student isn't just about acquiring knowledge; it's about embracing the excitement of discovery. My love for programming stems from an insatiable curiosity, driving me to continuously learn and explore new concepts, languages, and technologies.

High School
Diploma

- Al-Thagher Highschool
  Jeddah, Saudi Arabia

2020-2021

FenerBahce University

- Second Year Computer Engineering Student
  Istanbul, Turkey

2022

Skills

- Computer Experience
- Coding
- Time Management
- Problem-solving
- Team Support
- Flexible Schedule

Programming Skills

- Proficient: **C++, C,**
- Intermediate: **Python, Verilog**
- Basic: **Java**

Other:

- **Verilog and Verification skills**
- **OOP (C++ and Python)**

Language

- **Arabic:** Native
- **English:** Proficient
- **Turkish:** B1 Level

Courses

- The Bits and Bytes of Computer Networking by Google
- Technical Support Fundamentals by Google
- MATLAB Onramp Course by Mathworks Training Services

Projects

- **CPU Implementation Using Verilog:**

I collaborated with a dynamic group on a comprehensive project where we collectively designed and implemented a fully operational CPU using Verilog. Our work involved delving into the intricacies of vital components such as the Program Counter (PC), Instruction Register (IR), Memory Address Register (MAR), Memory Data Register (MDR), Control Unit (C-Unit), Accumulator (ACC), and Arithmetic Logic Unit (ALU). The project showcased our proficiency in hardware description language and digital design.

CPU Implementation Project