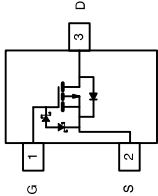


## P-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω) Max.	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)
- 30	0.045 at V <sub>GS</sub> = -10 V	- 4.8	10.6 nC
	0.053 at V <sub>GS</sub> = -4.5 V	- 4.4	
	0.080 at V <sub>GS</sub> = -2.5 V	- 3.6	

TO-236  
(SOT-23)



Top View

Si2371EDS (E6)\*

\*Marking Code

### Ordering Information:

Si2371EDS-T1-GE3 (Lead (Pb)-free and Halogen-free)

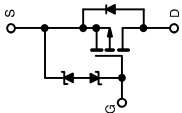


- FEATURES**
- TrenchFET<sup>®</sup> Power MOSFET
  - 100 % R<sub>g</sub> Tested
  - Built-in ESD Protection
  - Typical ESD Performance 3000 V
  - Material categorization:  
For definitions of compliance please see  
[www.vishay.com/doc/99912](http://www.vishay.com/doc/99912)

**RoHS  
COMPLIANT**  
HALOGEN  
**FREE**

### APPLICATIONS

- Power Management for Portable and Consumer
- Load Switches
- OVP (Over Voltage Protection) Switch



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C, unless otherwise noted)			
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	- 30	V
Gate-Source Voltage	V <sub>GS</sub>	± 12	
Continuous Drain Current (T <sub>J</sub> = 150 °C)	I <sub>D</sub>	- 4.8	A
		- 3.8	
		- 3.7 <sup>b,c</sup>	
		- 2.9 <sup>b,c</sup>	
Pulsed Drain Current (t = 300 μs)	I <sub>DM</sub>	- 20	
Continuous Source-Drain Diode Current	I <sub>S</sub>	- 1.4	
		- 1 <sup>b,c</sup>	
		1.7	
Maximum Power Dissipation	P <sub>D</sub>	1.1	W
		1 <sup>b,c</sup>	
		0.8 <sup>b,c</sup>	
		- 55 to 150	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C
Soldering Recommendations (Peak Temperature) <sup>d,e</sup>		260	

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Typical	Unit
Maximum Junction-to-Ambient <sup>b, d</sup>	R <sub>θJA</sub>	100	°C/W
Maximum Junction-to-Foot (Drain)	R <sub>θJF</sub>	60	

Notes:

- T<sub>C</sub> = 25 °C.
- Surface mounted on 1" x 1" FR4 board.
- t = 5 s.
- Maximum under steady state conditions is 175 °C/W.

SPECIFICATIONS (T <sub>J</sub> = 25 °C, unless otherwise noted)					
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.
Static					
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = - 250 μA	- 30		V
V <sub>DS</sub> Temperature Coefficient	ΔV <sub>DS</sub> /T <sub>J</sub>	I <sub>D</sub> = -250 μA		- 24	mV/°C
V <sub>GS(th)</sub> Temperature Coefficient	ΔV <sub>GS(th)</sub> /T <sub>J</sub>			2.2	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = - 250 μA	- 0.6		- 1.5
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 12 V			± 10
		V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 4.5 V			± 1
		V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V			- 1
		V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 10
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> ≤ - 5 V, V <sub>GS</sub> = - 10 V	- 15		A
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 3.7 A		0.037	0.045
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 2 A		0.044	0.053
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 2 A		0.066	0.080
Dynamic <sup>b</sup>					
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 3.7 A		22.8	35
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 3.7 A		10.6	16
Gate-Drain Charge	Q <sub>gd</sub>			1.7	
Gate Resistance	R <sub>g</sub>	f = 1 MHz		2.6	
Turn-On Delay Time	t <sub>d(on)</sub>		2.2	11	22
Rise Time	t <sub>r</sub>	V <sub>DD</sub> = - 15 V, R <sub>L</sub> = 5.2 Ω I <sub>D</sub> ≅ - 2.9 A, V <sub>GEN</sub> = - 4.5 V, R <sub>g</sub> = 1 Ω		28	42
Turn-Off Delay Time	t <sub>d(off)</sub>			65	98
Fall Time	t <sub>f</sub>			47	71
Turn-On Delay Time	t <sub>d(on)</sub>			62	93
Rise Time	t <sub>r</sub>	V <sub>DD</sub> = - 15 V, R <sub>L</sub> = 5.2 Ω I <sub>D</sub> ≅ - 2.9 A, V <sub>GEN</sub> = - 10 V, R <sub>g</sub> = 1 Ω		7	14
Turn-Off Delay Time	t <sub>d(off)</sub>			8	16
Fall Time	t <sub>f</sub>			52	78
Drain-Source Body Diode Characteristics					
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C		- 1.4	A
Pulse Diode Forward Current	I <sub>SM</sub>			- 20	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 2.9 A, V <sub>GS</sub> = 0 V		- 0.8	- 1.2
Body Diode Reverse Recovery Time	t <sub>rr</sub>			13	20
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			6	12
Reverse Recovery Fall Time	t <sub>a</sub>	I <sub>F</sub> = - 2.9 A, dI/dt = 100 A/μs, T <sub>J</sub> = 25 °C		9	
Reverse Recovery Rise Time	t <sub>b</sub>			4	

Notes:

- Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.
- Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.