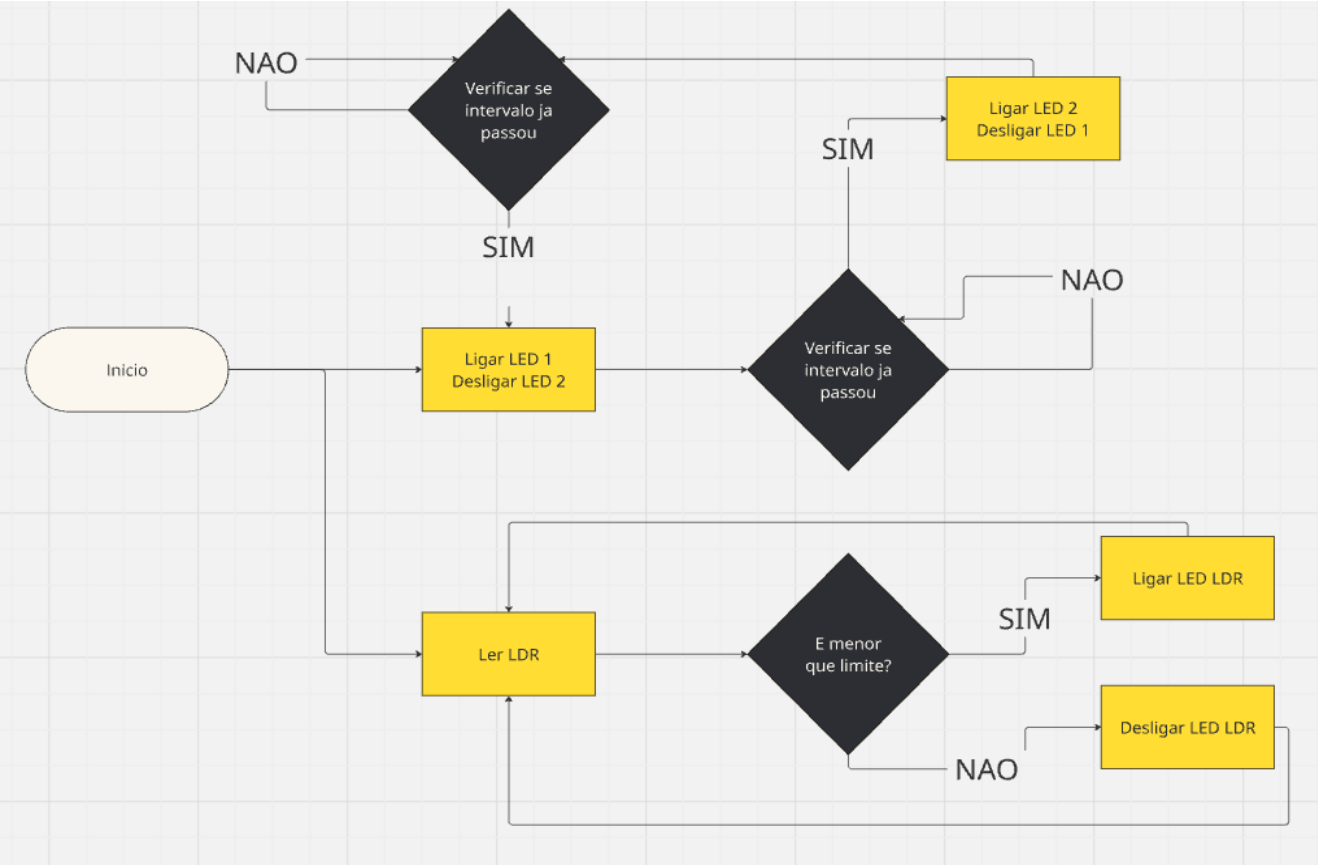
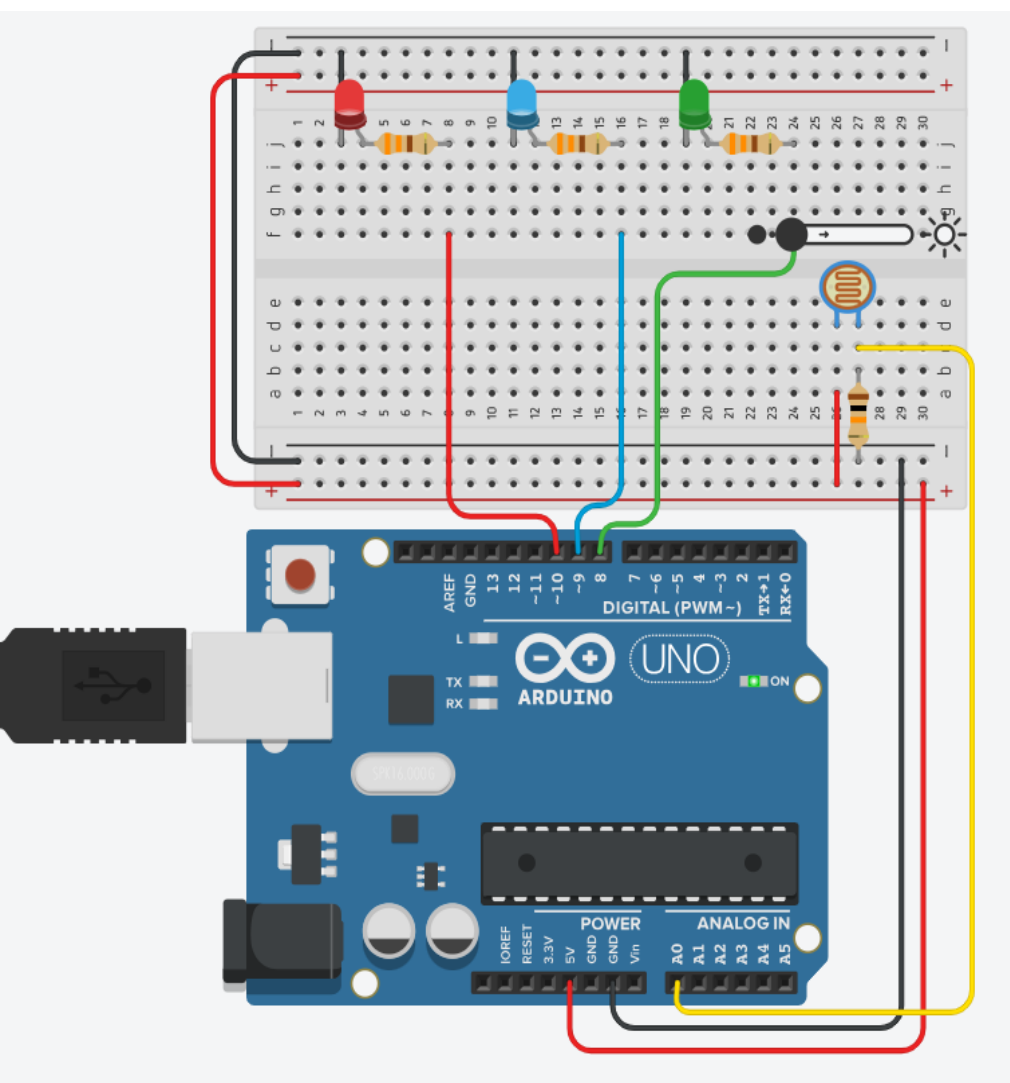
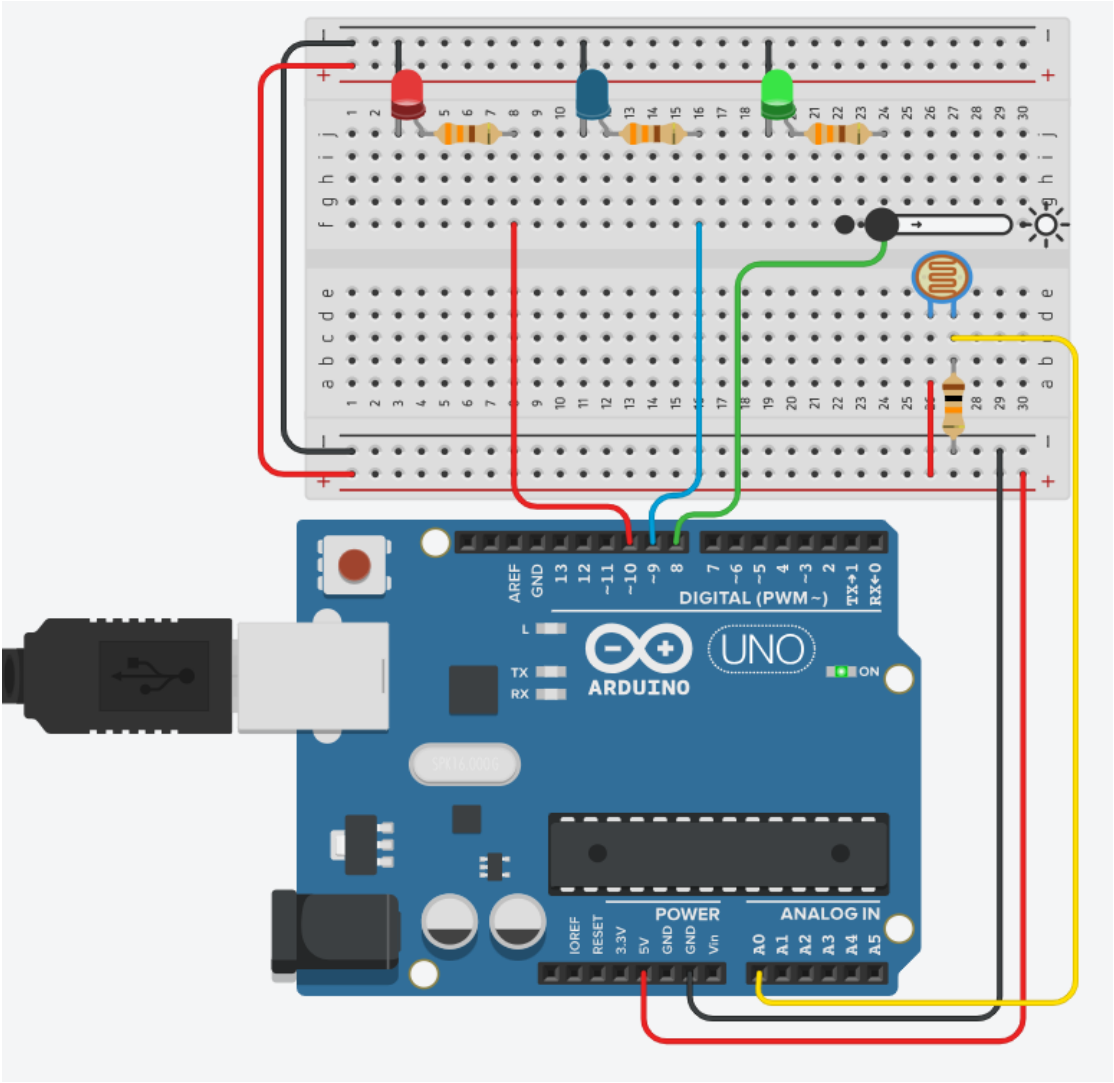


Circuitos sincronos e assincronos

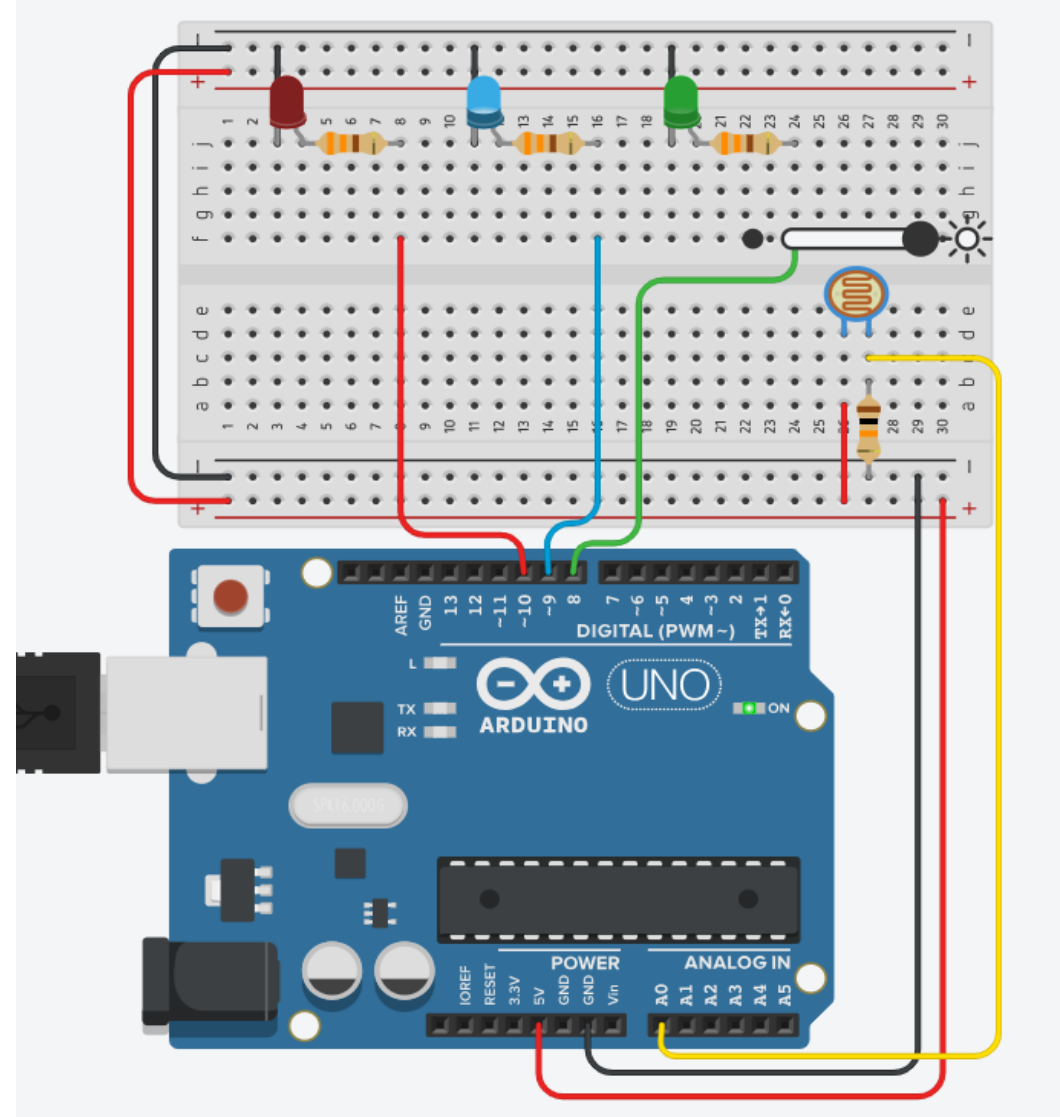
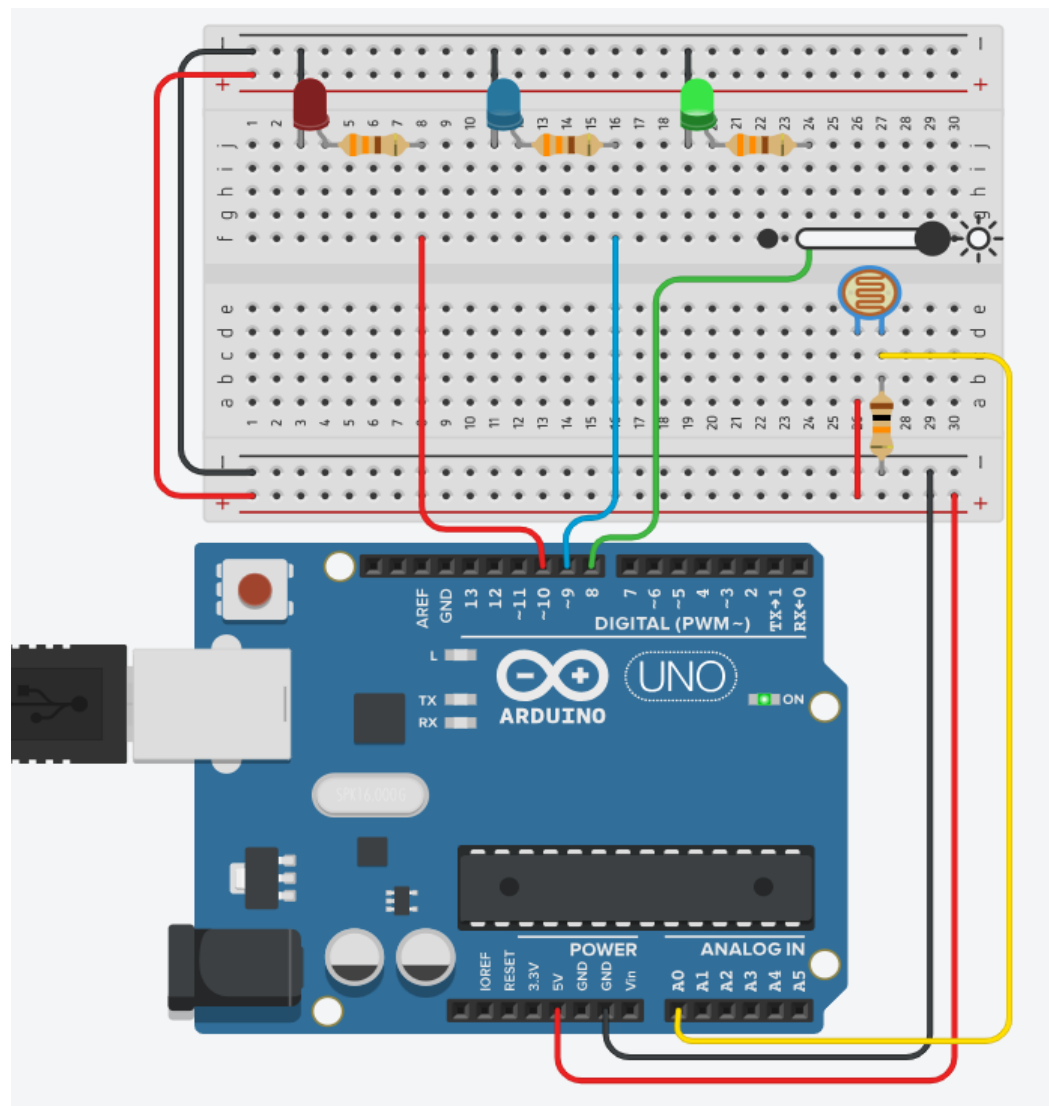
```
if-millis-com-leds-e-ldr.ino
1  const int led1 = 8;
2  const int led2 = 9;
3  const int ledLDR = 10;
4  const int pinoLDR = A0;
5  const int limite = 300; // Ajuste conforme necessário
6
7  unsigned long tempoAnterior = 0;
8  const int intervalo = 500; // 500ms entre piscadas
9  bool estadoLed = false;
10
11 void setup() {
12   pinMode(led1, OUTPUT);
13   pinMode(led2, OUTPUT);
14   pinMode(ledLDR, OUTPUT);
15   Serial.begin(9600);
16 }
17
18 void loop() {
19   // Controle dos LEDs piscando alternadamente
20   if (millis() - tempoAnterior >= intervalo) {
21     tempoAnterior = millis();
22     estadoLed = !estadoLed;
23
24     digitalWrite(led1, estadoLed);
25     digitalWrite(led2, !estadoLed);
26   }
27
28   // Controle do LED via LDR
29   int leituraLDR = analogRead(pinoLDR);
30   Serial.println(leituraLDR); // Monitorar no Serial Monitor
31
32   if (leituraLDR < limite) {
33     digitalWrite(ledLDR, HIGH); // Acende no escuro
34   } else {
35     digitalWrite(ledLDR, LOW); // Apaga na claridade
36   }
37 }
38
```




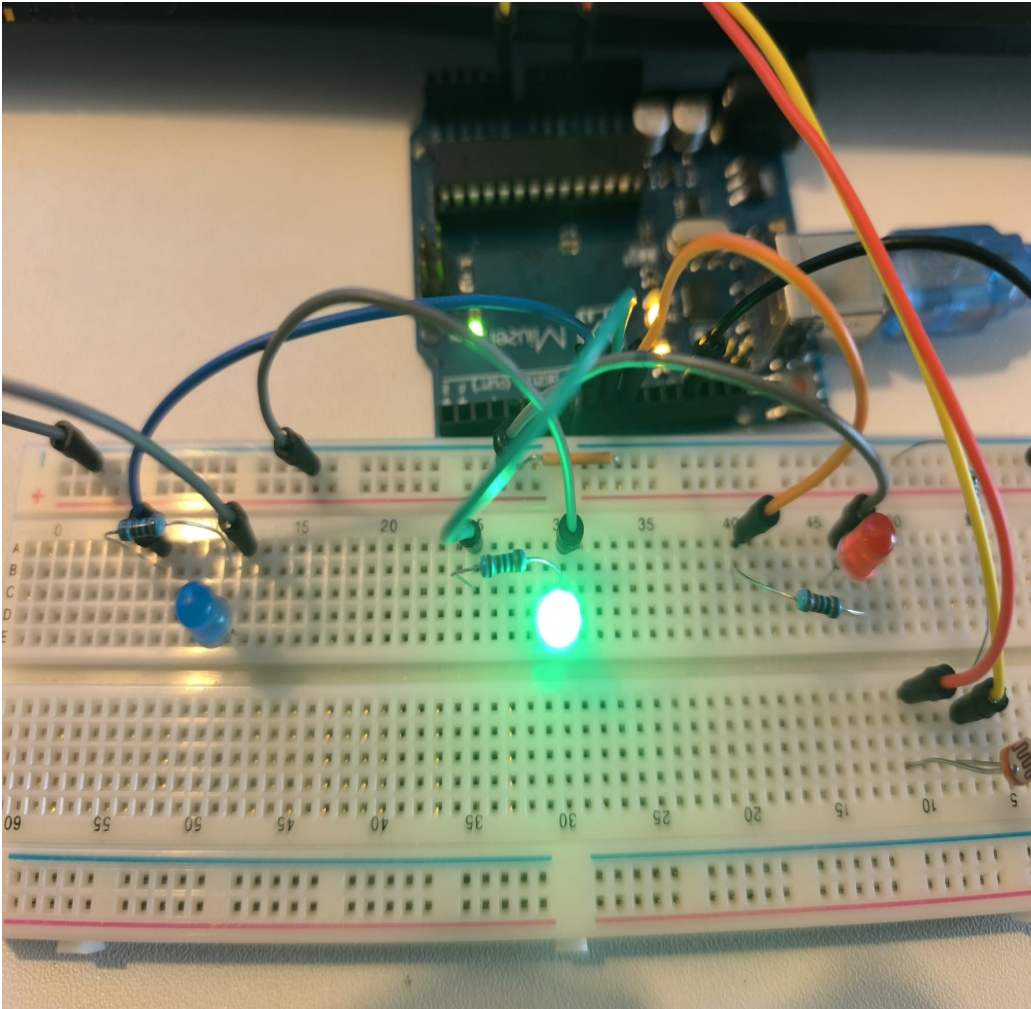
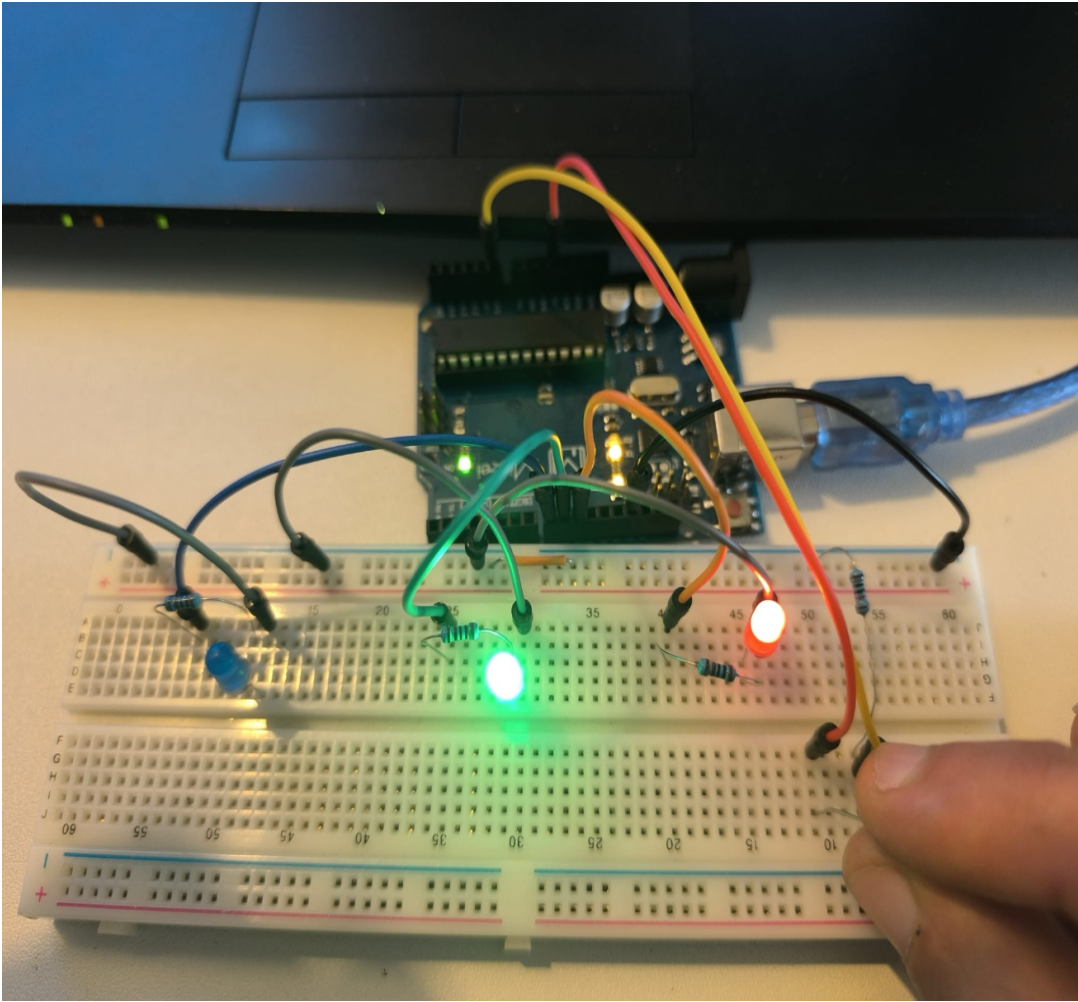
Schematic	Circuitos sincronos e assincronos			Create at	2025-04-02
				Update at	2025-04-02
				Page	P2
Drawn	Diogo Otero	UFCD 6026			
Reviewed	Diogo Otero				
		Version	Size	Page 1 Total 6	
		V1.0	A4	EasyEDA.com	




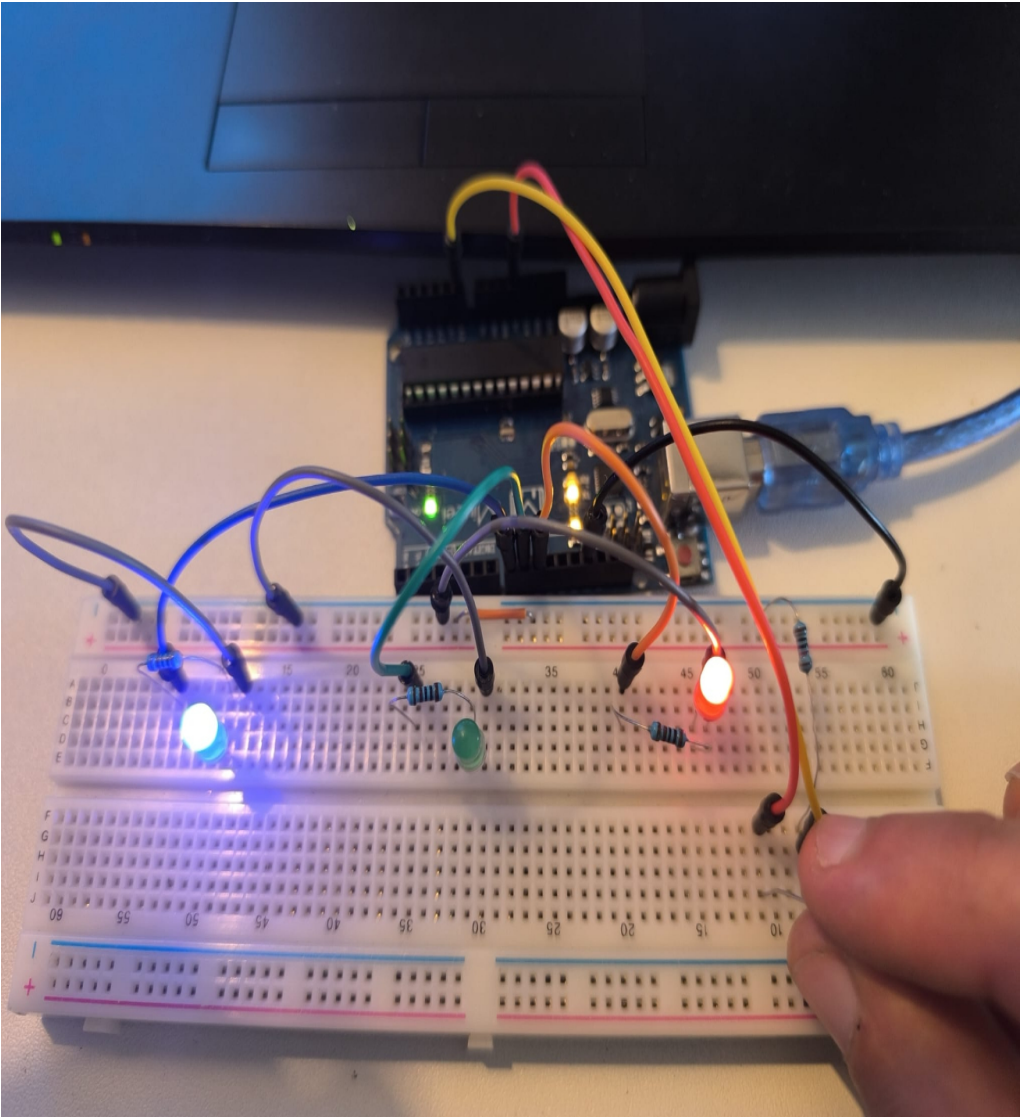
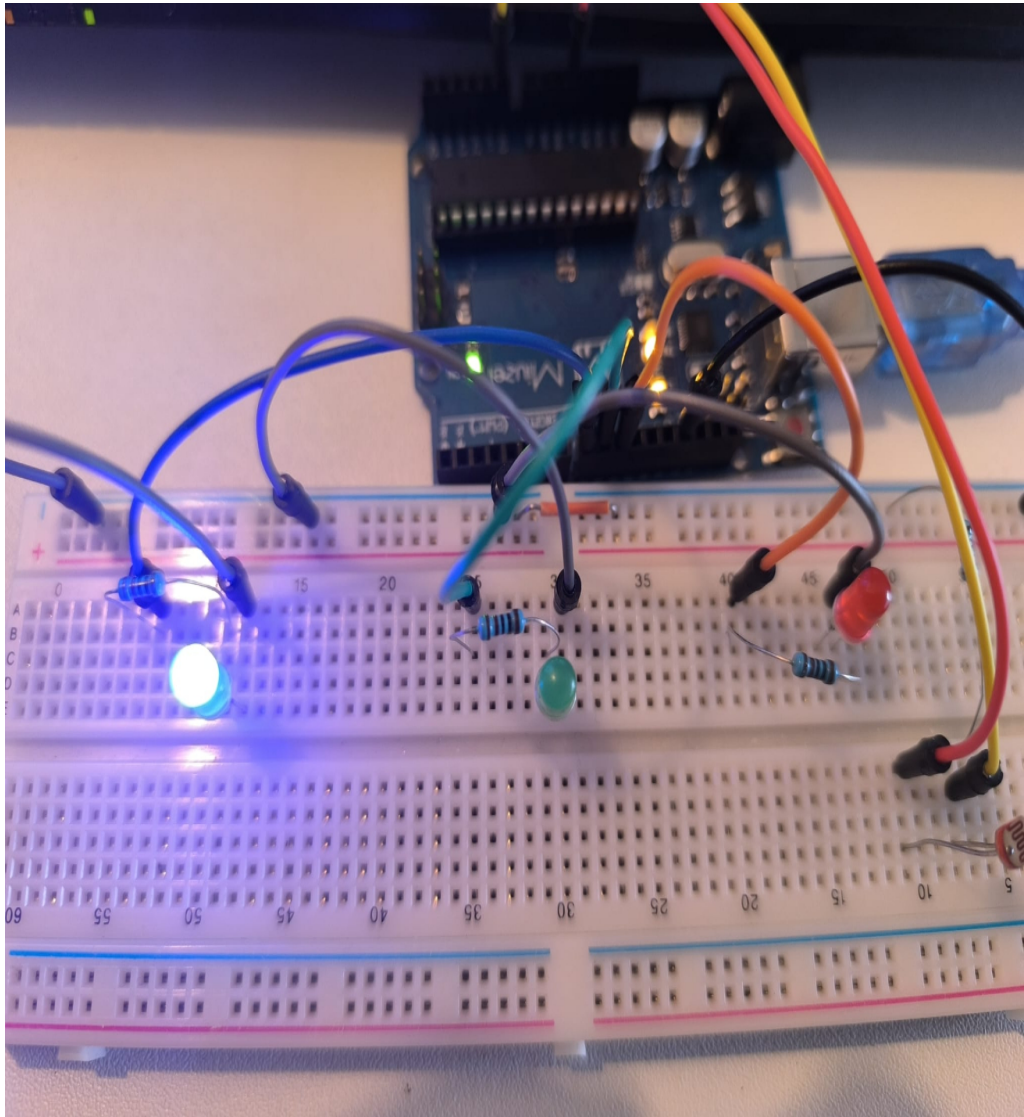
Schematic	Circuitos sincronos e assincronos			Create at	2025-04-02
				Update at	2025-04-02
				Page	P3
Drawn	Diogo Otero	UFCD 6026			
Reviewed	Diogo Otero				
		Version	Size	Page 2 Total 6	
		V1.0	A4	EasyEDA.com	




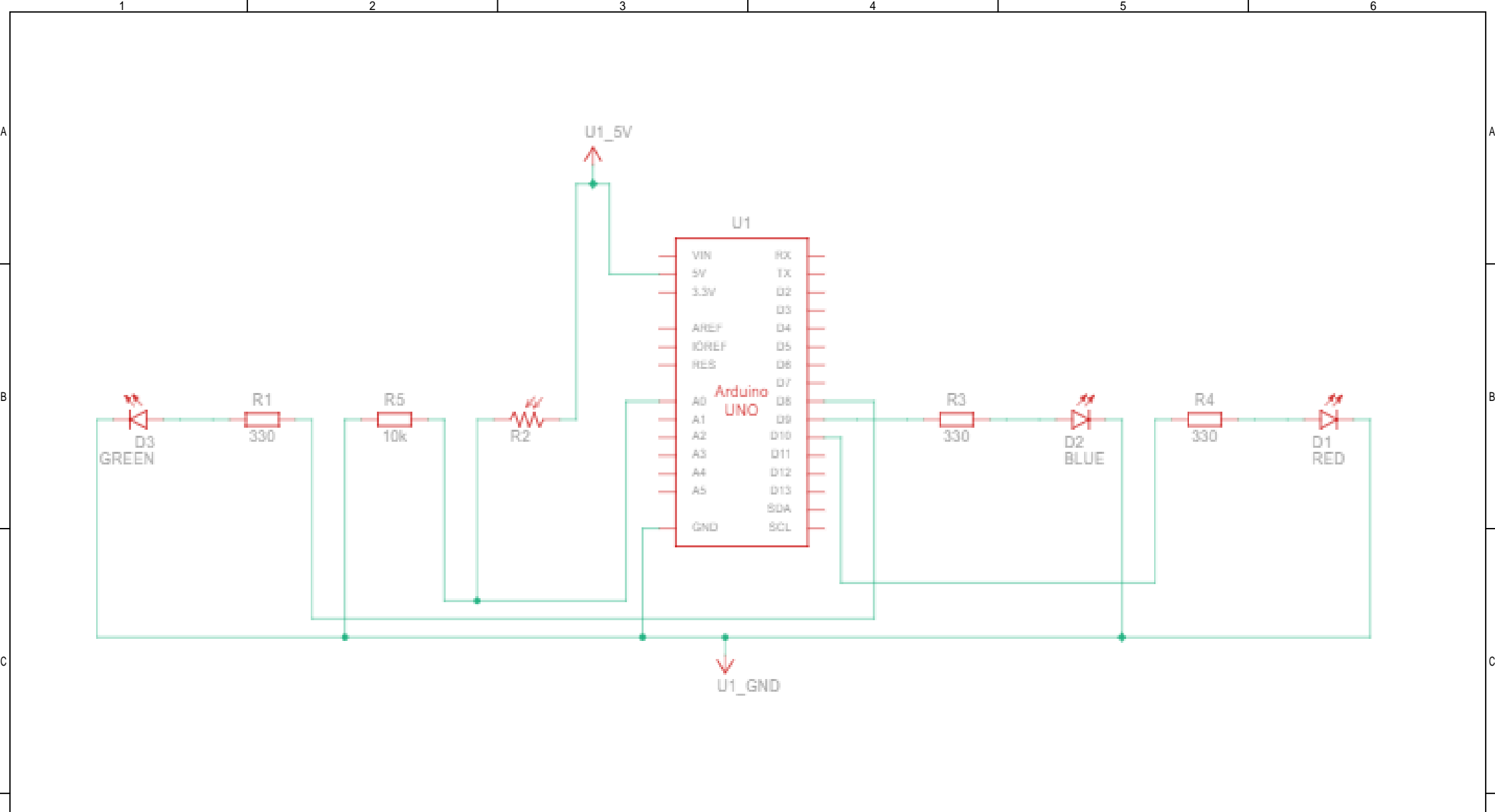
Schematic	Circuitos sincronos e assincronos			Create at	2025-04-02
				Update at	2025-04-02
				Page	P4
Drawn	Diogo Otero	UFCD 6026			
Reviewed	Diogo Otero				
		Version	Size	Page 3 Total 6	
		V1.0	A4	EasyEDA.com	



Schematic	Circuitos sincronos e assincronos			Create at	2025-04-02
				Update at	2025-04-02
				Page	P5
Drawn	Diogo Otero	UFCD 6026			
Reviewed	Diogo Otero				
		Version	Size	Page 4 Total 6	
		V1.0	A4	EasyEDA.com	



Schematic	Circuitos sincronos e assincronos			Create at	2025-04-02
				Update at	2025-04-02
				Page	P6
Drawn	Diogo Otero	UFCD 6026			
Reviewed	Diogo Otero				
		Version	Size	Page 5 Total 6	
		V1.0	A4	EasyEDA.com	



Schematic	Circuitos sincronos e assincronos		Create at	2025-04-02
			Update at	2025-04-02
			Page	P7
Drawn	Diogo Otero	UFCD 6026		
Reviewed	Diogo Otero			
		Version	Size	Page 6 Total 6
		V1.0	A4	EasyEDA.com