12 23 12 AV (b) Howard wants to demonstrate his engineering skills, but Sheldon sets a challenging constraint for him. Howard is tasked with designing a 4-bit Arithmetic Logic Unit (ALU), and the constraint is that he cannot use any parallel adder. Instead, he is only allowed to use parallel subtractors and logic gates for the ALU design. The ALU should have three selection variables, denoted as S0, S1, and S2. These selection variables control the ALU to perform various operations on two 4-bit inputs, Labelled as A and B. Your task is to help Howard design the 4-bit Arithmetic Logic Unit (ALU) so that it can do the following operations:

(14)

-B=B+1

$S_2$	$S_1$	$S_0$	Operation	
0	0 0		A - B	
0	0	1	A + B	
0	1	0	-A -B - 2	
0	1	1	B - A	
1	0	0	A XOR B	
1 1		0	A AND B	

$S_1$	$S_{\delta}$	X;	y i	Βī		OP
0	<u>Q</u>	A	B	Ö	A-B	A -B
Q.		A	B'	1	A - B - 1 $= A - (-B - 1) - 1$ $= A + B + 1 - 1$ $= A + B$	A+B
	0	A'	B	<u>1</u>	= A/-8-1 $= -A-1-B-1$ $= -A-B-2$	-A-B-2
1 [		A/	B	$\mathcal{O}$	$\beta'-\beta'$	~ ^

$$A/B/OA-B/OB-A$$

$$= (-A-i)-(-B-i)$$

$$= B-A$$

$$\mathcal{H}_{i} = S_{1} \oplus A$$

$$\mathcal{H}_{i} = S_{0} \oplus B$$

$$\mathcal{H}_{i} = S_{1} \oplus S_{0}$$

noω,

$$S_2$$
  $S_1$   $S_2$   $S_3$   $S_4$   $S_5$   $S_4$   $S_5$   $S_4$   $S_5$   $S_6$   $S_6$ 

ABB

21 05/2/2

$$S_0, \quad B_i = \overline{S_2(S_1 \oplus S_0)}$$

5<sub>2</sub> S<sub>1</sub> S<sub>0</sub>

x; di A B

output > ADB=AB+AB

need AB

$$(B+K) \oplus \overline{A} = A3$$

 $= \frac{1}{3} = \frac{$ 

ABK + AB+KA = AB

$$k = A$$

$$finally$$

$$x_{i} = S_{i} \oplus A$$

$$y_{i} = S_{o} \oplus B + S_{2} S_{i} S_{o} A'$$

$$B_i = S_2(S_0 \oplus S_1)$$