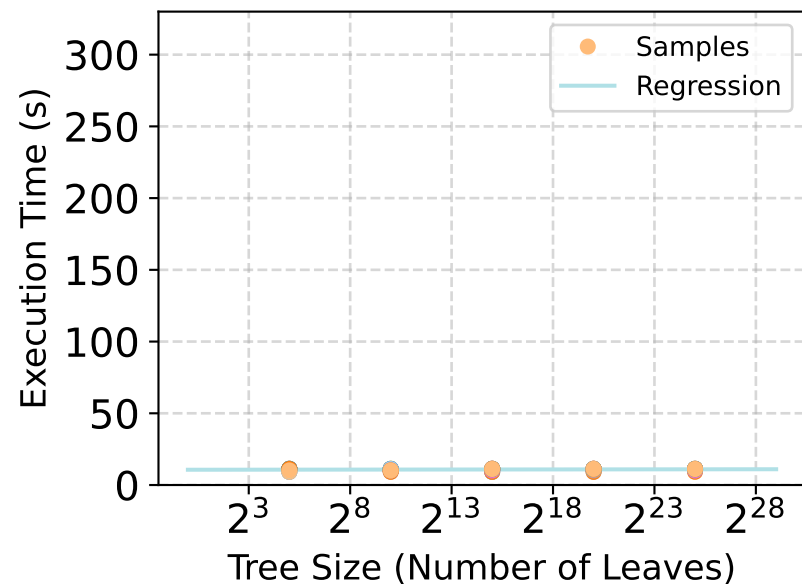
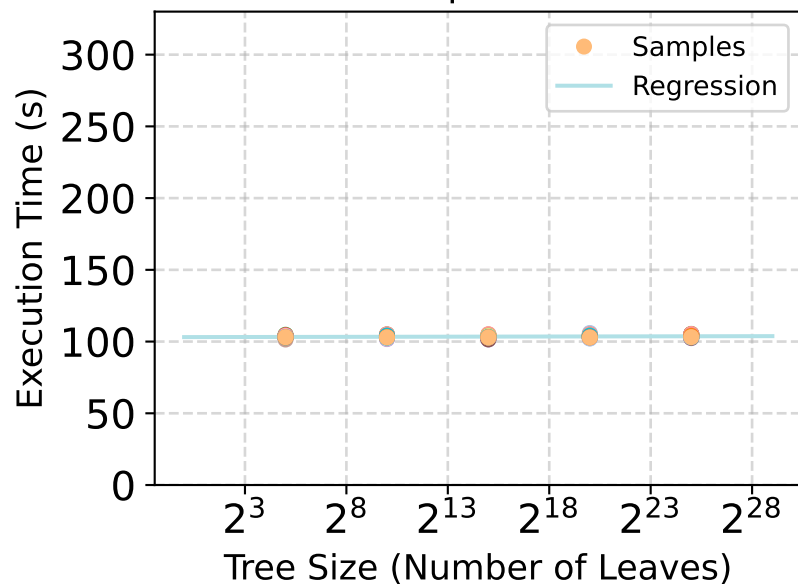


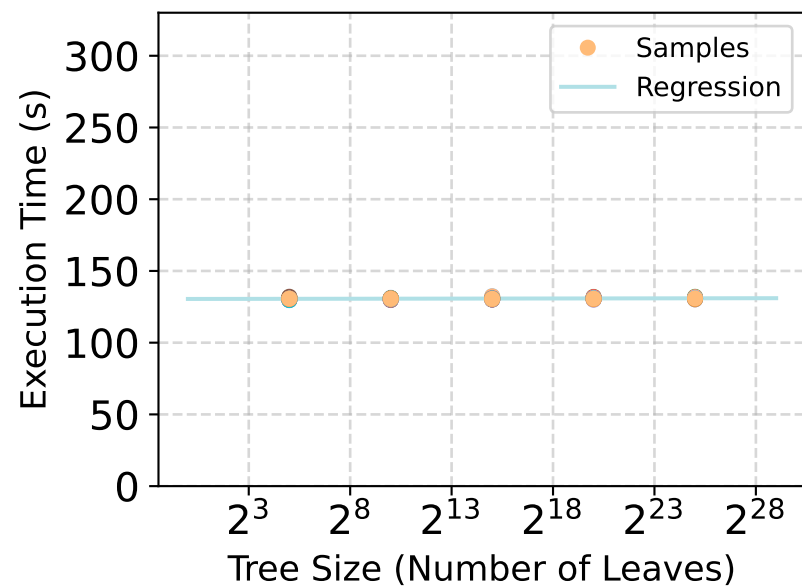
Circuit Generation



Setup Phase



Proof Generation



Proof Verification

