
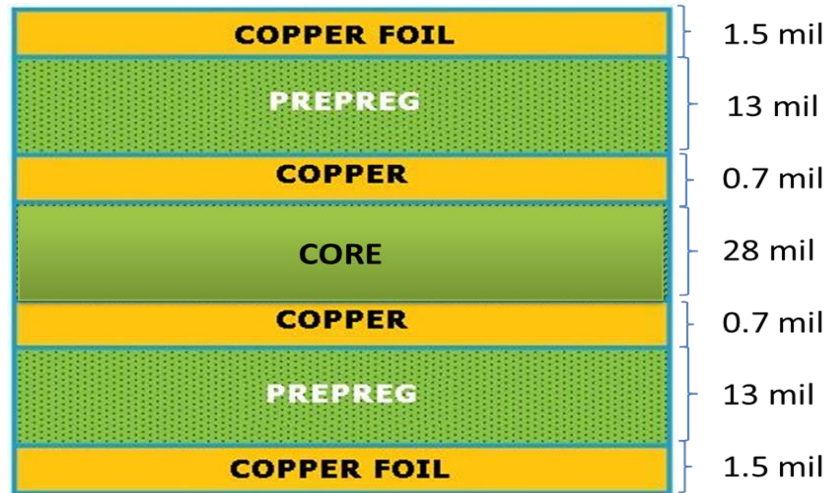


Revision History		
Level	Date	Description
Rev A	12-Jul-2019	Initial Design

Catena 4618 PCB Layout Fabrication Drawing

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Date : 12-Jul-2019	Drawing number : 234001206	Rev - A



Stackup:

Thickness	Thickness (mm)	Layer	Tolerance	Tolerance (mm)
1 mil	.025	Solder Mask	±0.25mil	±0.01
1.5 mil	.04	1 oz copper	+0.5mil	±0.02
13 mil	.33	FR4 Prepreg		
0.7 mil	.02	0.5 oz copper		
28 mil	.7	FR4 Core		
0.7 mil	.02	0.5 oz copper		
13 mil	.33	FR4 Prepreg		
1.5 mil	.04	1 oz copper	+0.5mil	±0.02
1 mil	.025	Solder Mask	±0.25mil	±0.01

PCB Substrate dielectric constant of 3.95 @ 1GHz

Note : All the Measurements in mils

4-Layer PCB Stackup Model

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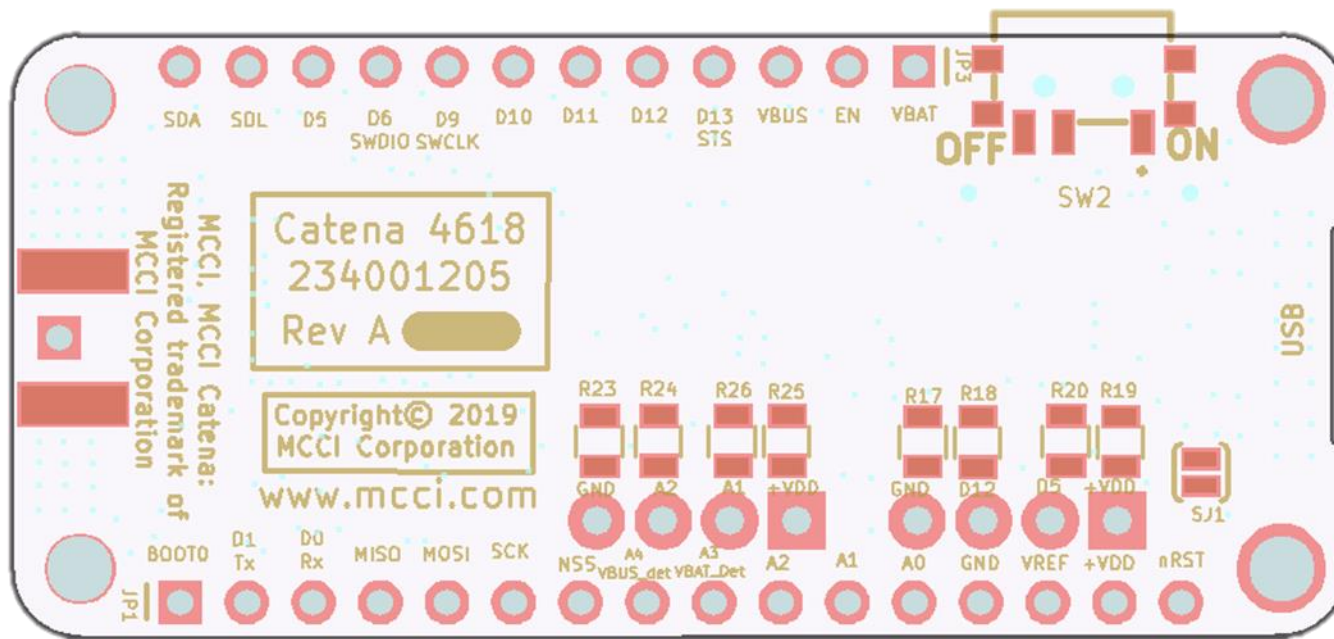
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Components Assembly Bottom

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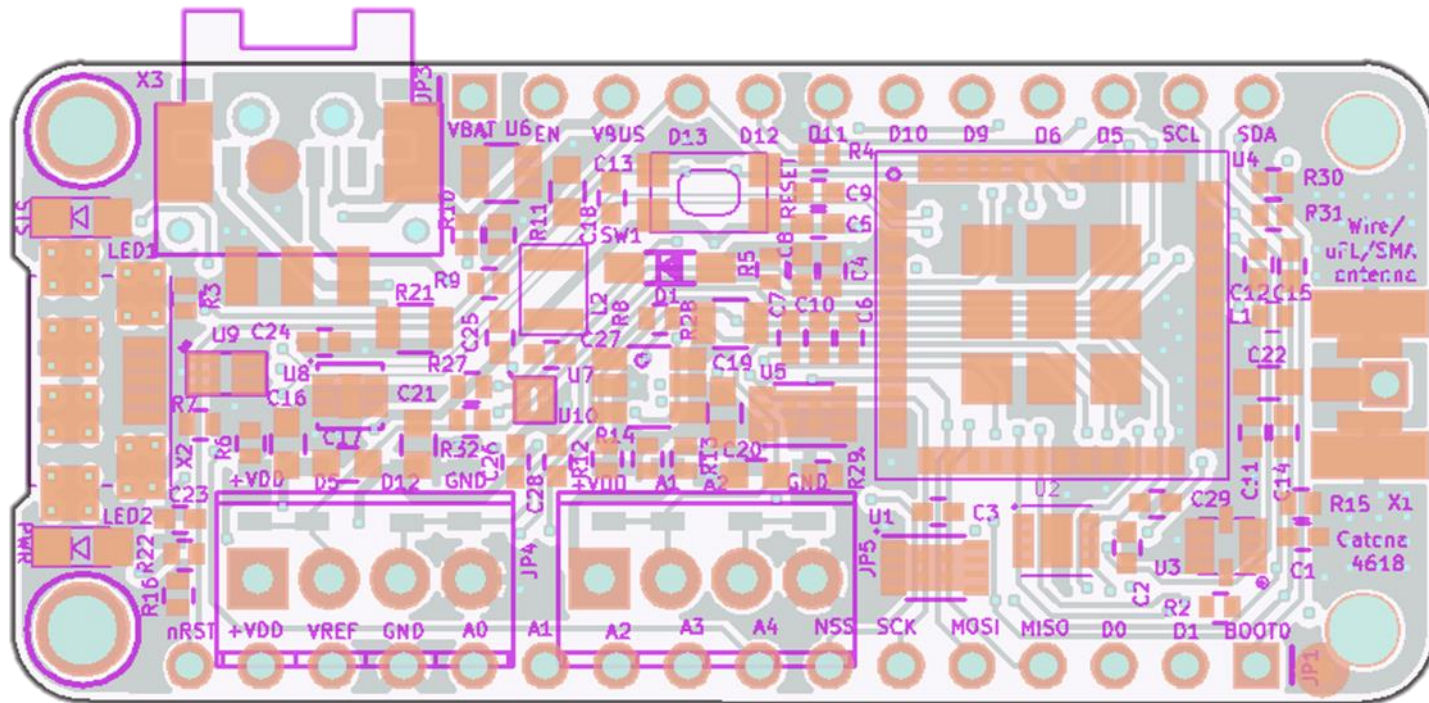
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Top Layer

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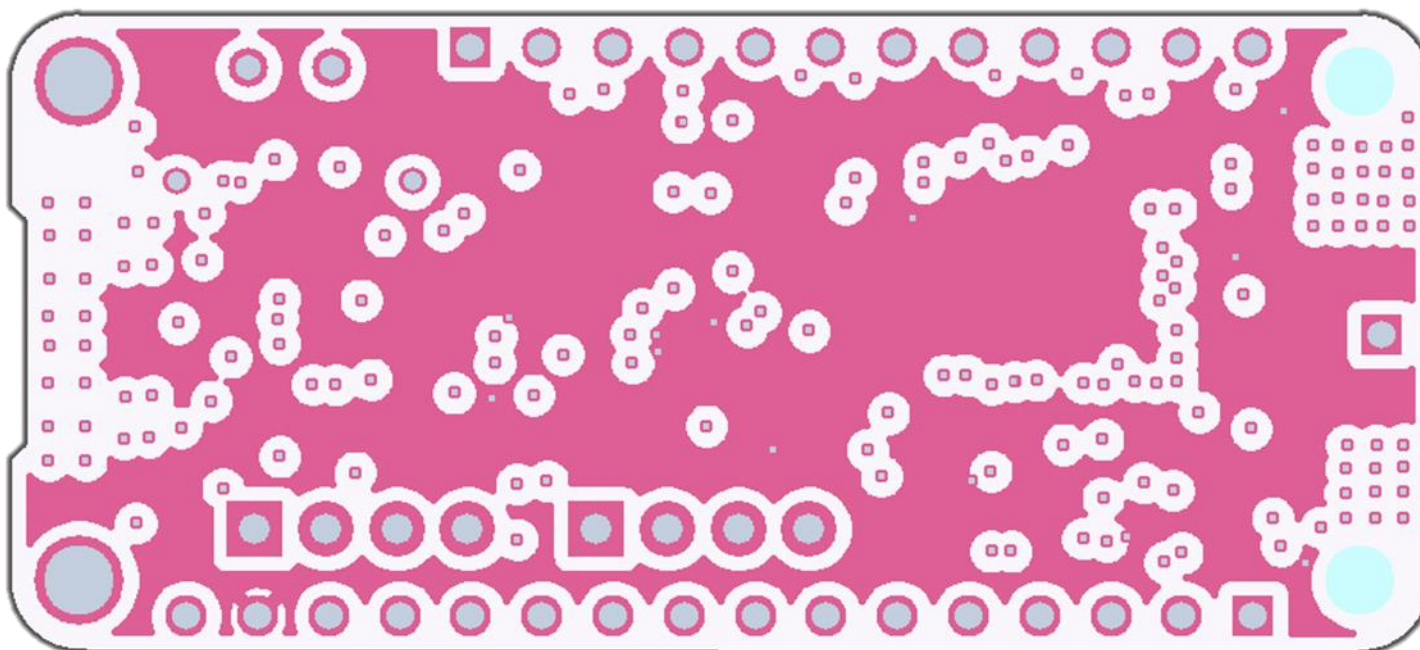
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Ground Layer

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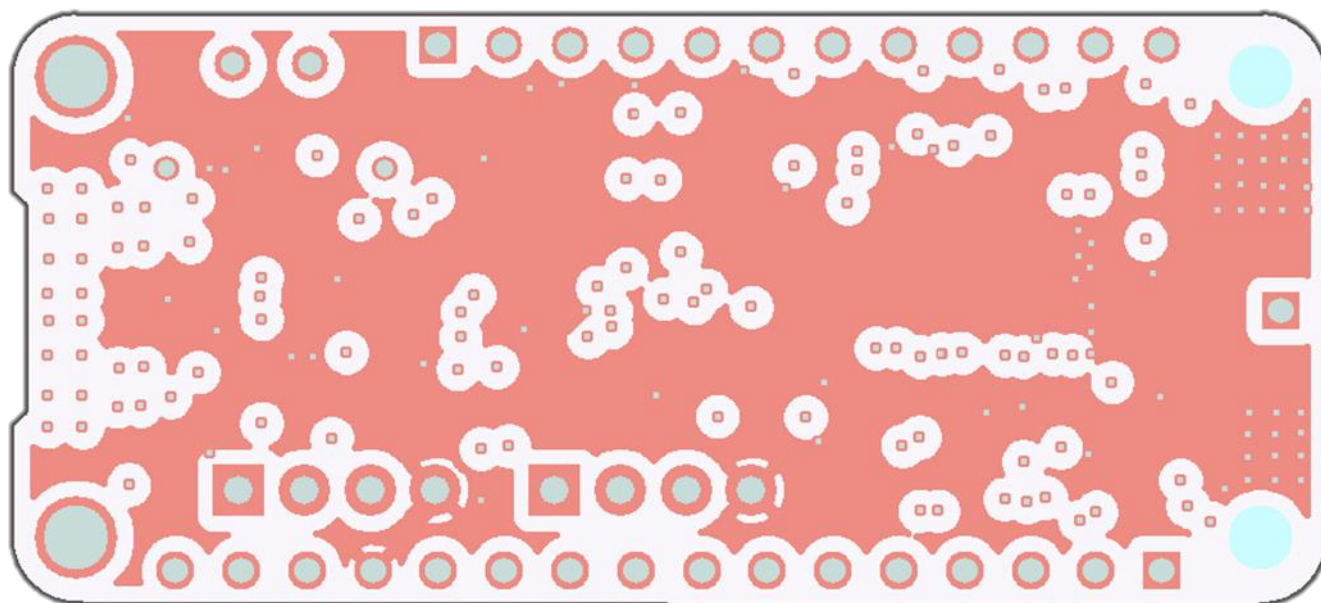
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Power Layer

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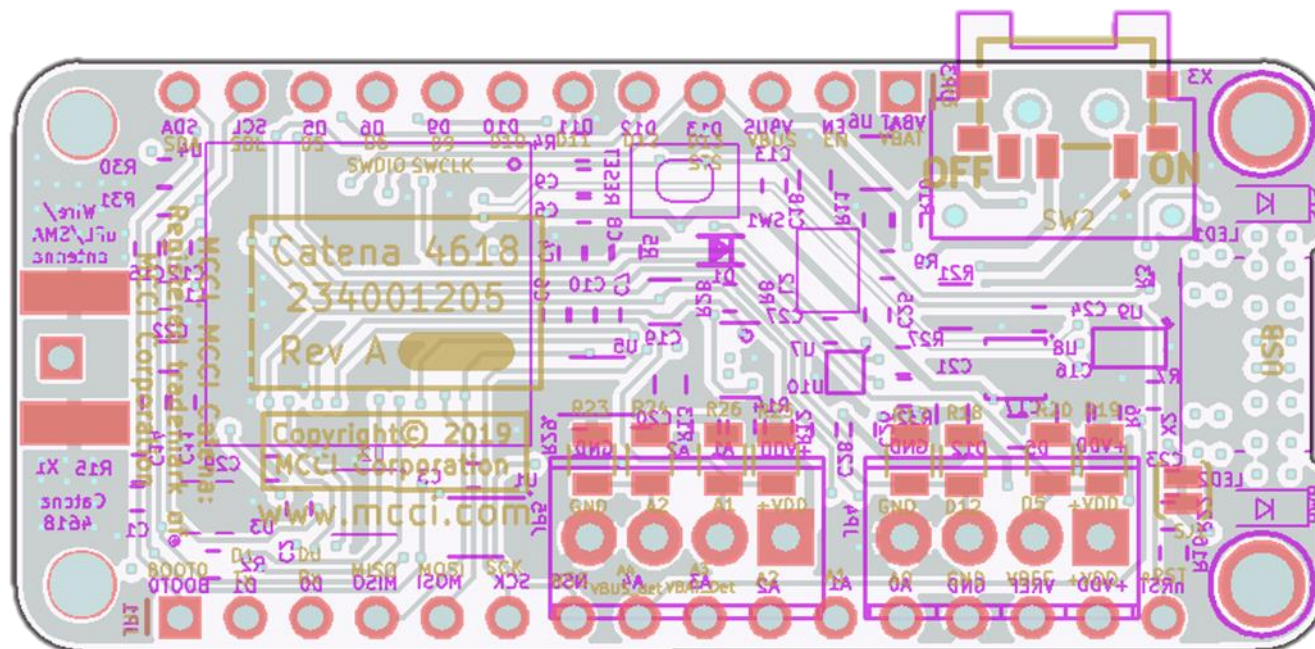
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Bottom Layer

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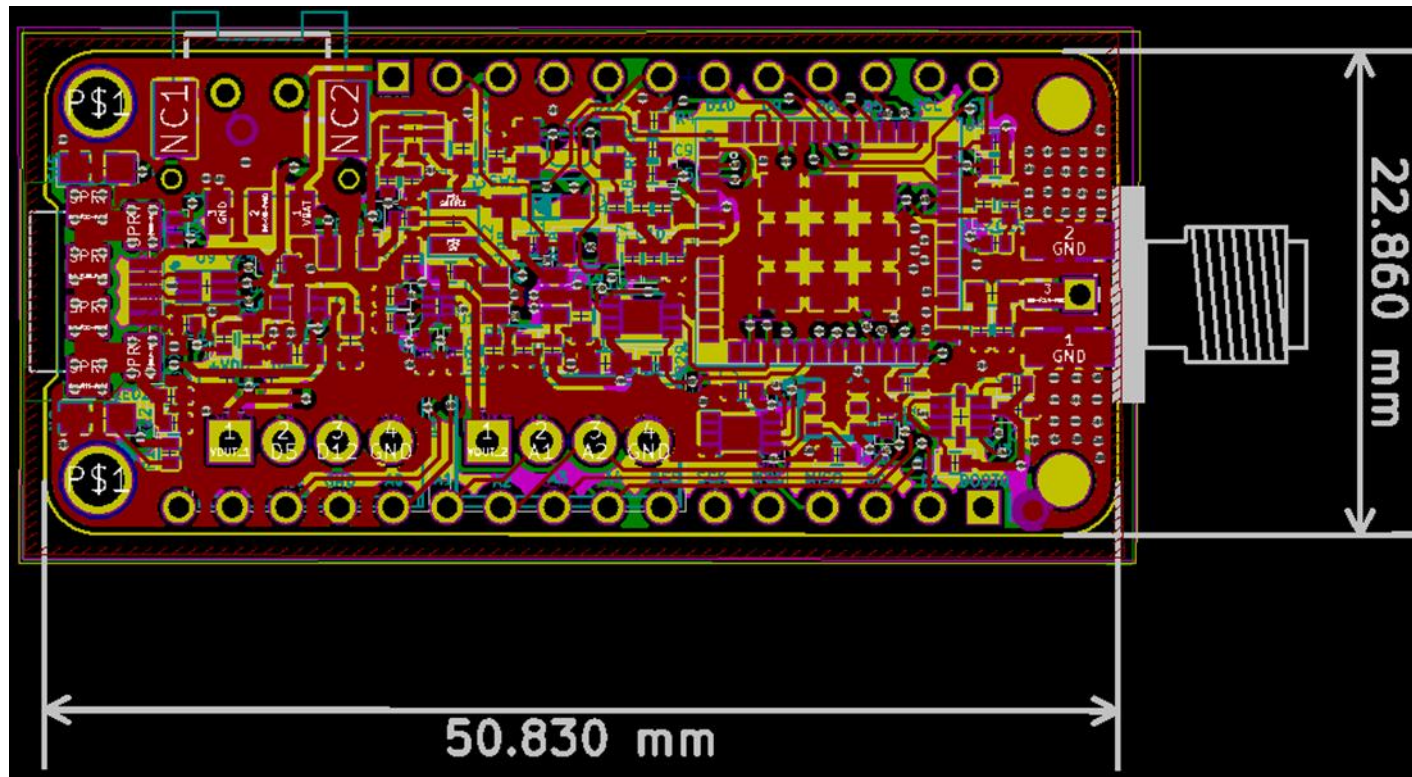
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Board Dimension

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