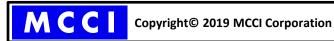
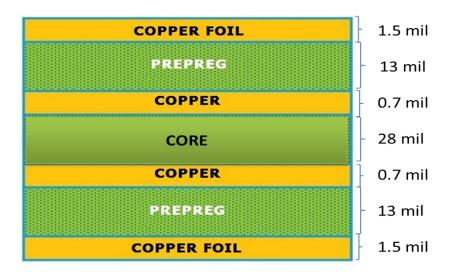
Revision History				
Level	Date Description			
Rev A	27-Nov-2019	Initial Design		

Catena 4430 Shield PCB Layout Fabrication Drawing



Title: Catena 4430 Shield PCB Layout Fabrication Drawing

Date: 27-Nov-2019 Drawing number: 234001201



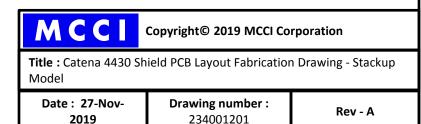
Stackup:

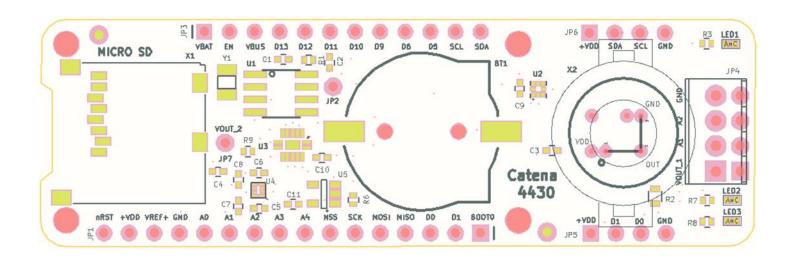
Thickness	Thickness (mm)	Layer	Tolerance	Tolerance (mm)
1 mil	.025	Solder Mask	±0.25mil	±0.01
1.5 mil	.04	1 oz copper	+0.5mil	±0.02
13 mil	.33	FR4 Prepreg		
0.7 mil	.02	0.5 oz copper		
28 mil	.7	FR4 Core		
0.7 mil	.02	0.5 oz copper		
13 mil	.33	FR4 Prepreg		
1.5 mil	.04	1 oz copper	+0.5mil	±0.02
1 mil	.025	Solder Mask	±0.25mil	±0.01

PCB Substrate dielectric constant of 3.95 @ 1GHz

4-Layer PCB Stackup Model

Note: All the Measurements in mils



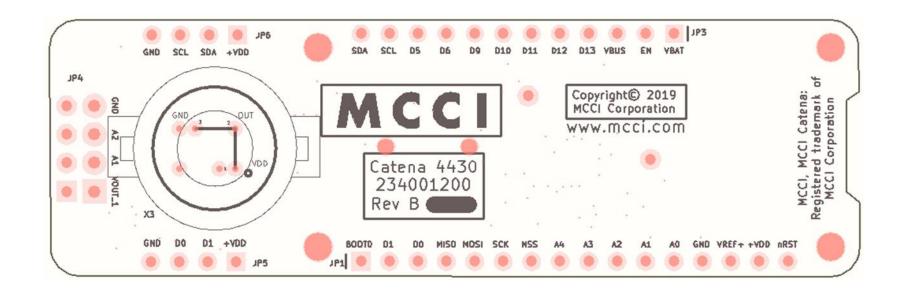


Components Assembly Top



Title : Catena 4430 Shield PCB Layout Fabrication Drawing - Assembly Top

Date: 27-Nov- 2019 Drawing number:234001201

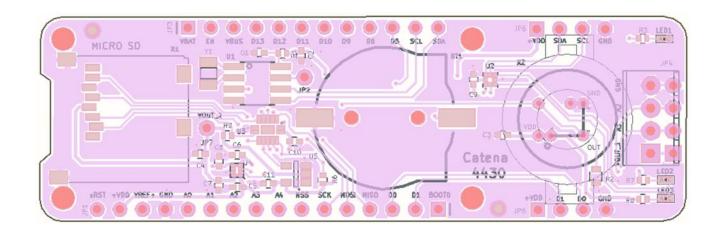


Components Assembly Bottom



Title : Catena 4430 Shield PCB Layout Fabrication Drawing - Assembly Bottom

Date: 27-Nov-2019 Drawing number: 234001201

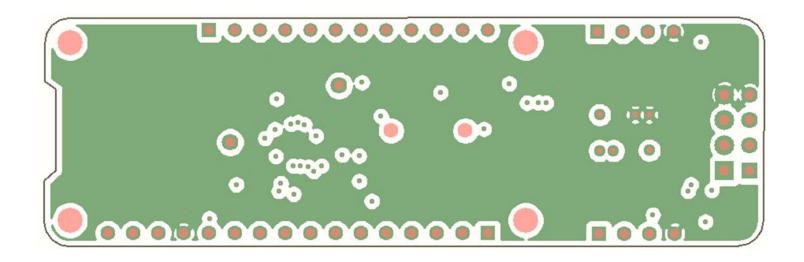


Top Layer

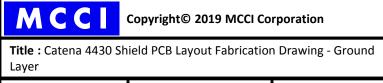


Title: Catena 4430 Shield PCB Layout Fabrication Drawing - Top Layer

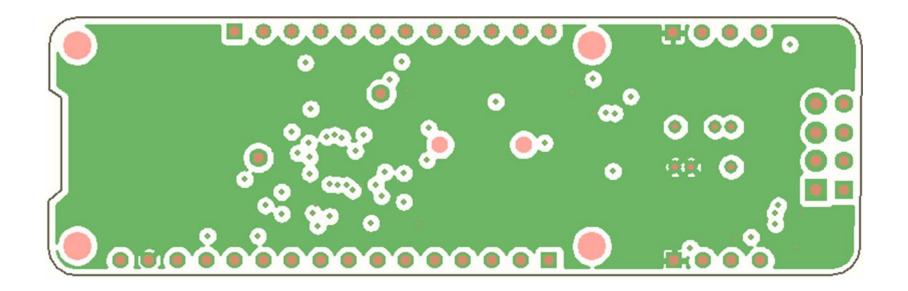
Date: 27-Nov-2019 Drawing number: 234001201



Ground Layer



Date: 27-Nov- 2019Drawing number: 234001201

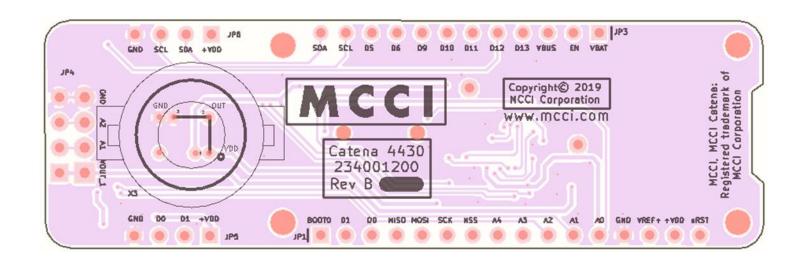


<u>Power Layer</u>

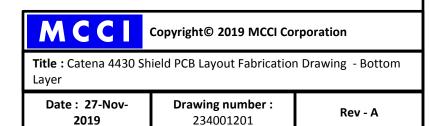


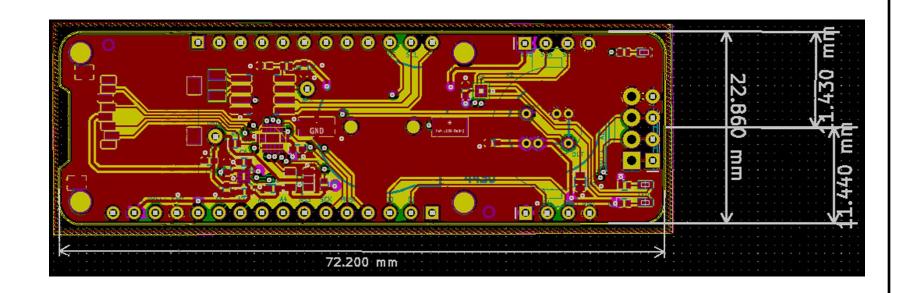
Title : Catena 4430 Shield PCB Layout Fabrication Drawing - Power Layer

Date: 27-Nov-2019 Drawing number: 234001201



Bottom Layer





Board Dimension

