Revision History				
Level	Date	Description		
Rev A	13-Jul-2018	Initial Design		

Catena 4611 PCB Layout Fabrication Drawing

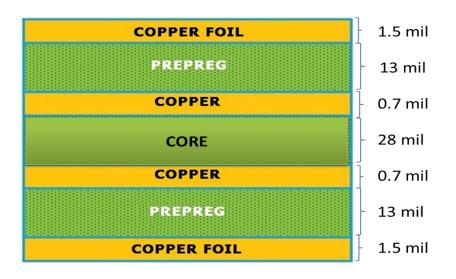


Title: Catena 4611 PCB Layout Fabrication Drawing

Date: 13-Jul-2018

Drawing number: 234001103

Rev - A

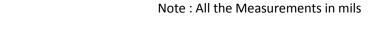


Stackup:

Thickness	Thickness (mm)	Layer	Tolerance	Tolerance (mm)
1 mil	.025	Solder Mask	±0.25mil	±0.01
1.5 mil	.04	1 oz copper	+0.5mil	±0.02
13 mil	.33	FR4 Prepreg		
0.7 mil	.02	0.5 oz copper		
28 mil	.7	FR4 Core		
0.7 mil	.02	0.5 oz copper		
13 mil	.33	FR4 Prepreg		
1.5 mil	.04	1 oz copper	+0.5mil	±0.02
1 mil	.025	Solder Mask	±0.25mil	±0.01

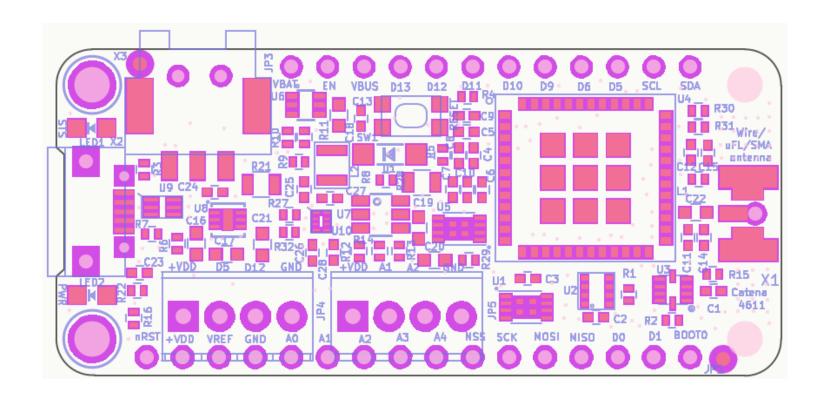
PCB Substrate dielectric constant of 3.95 @ 1GHz

4-Layer PCB Stackup Model





Rev - A



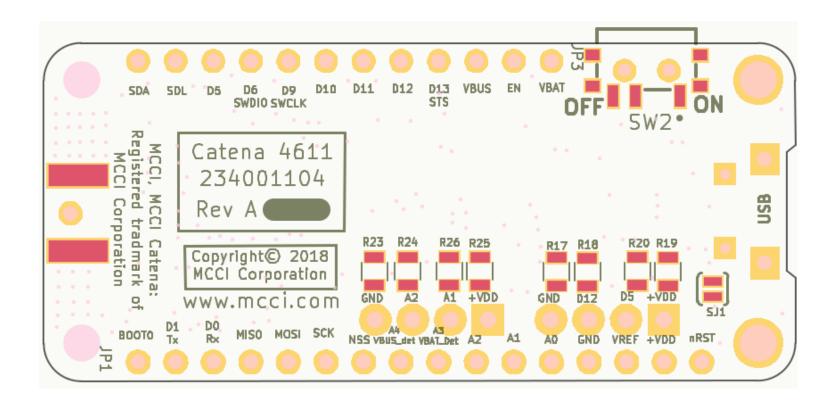
Components Assembly Top



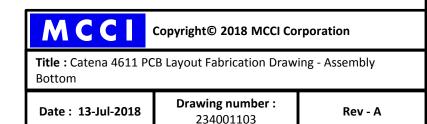
Date: 13-Jul-2018

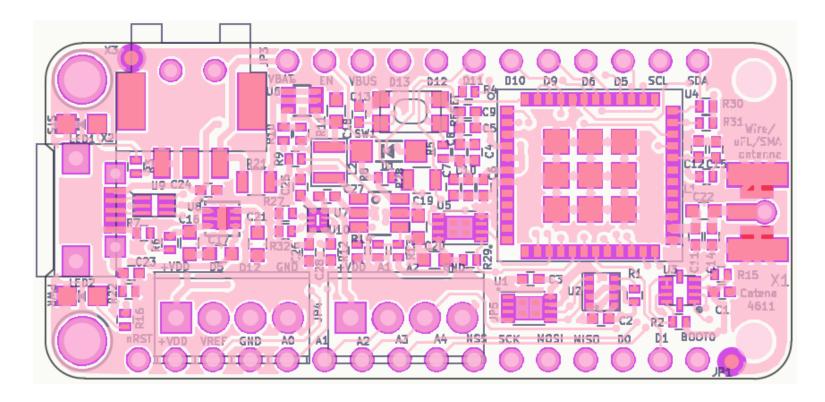
Drawing number: 234001103

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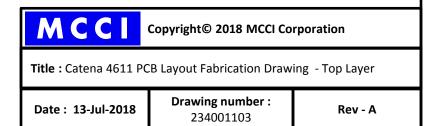


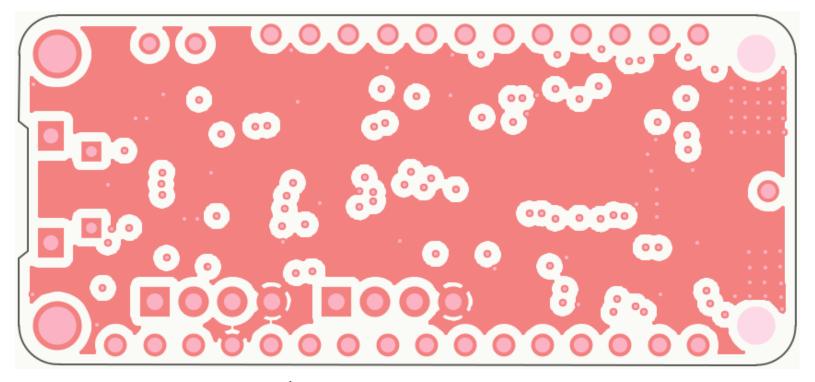
Components Assembly Bottom





Top Layer

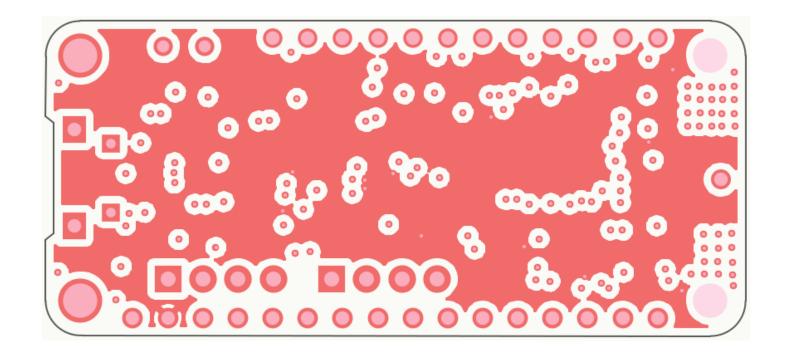




Ground Layer



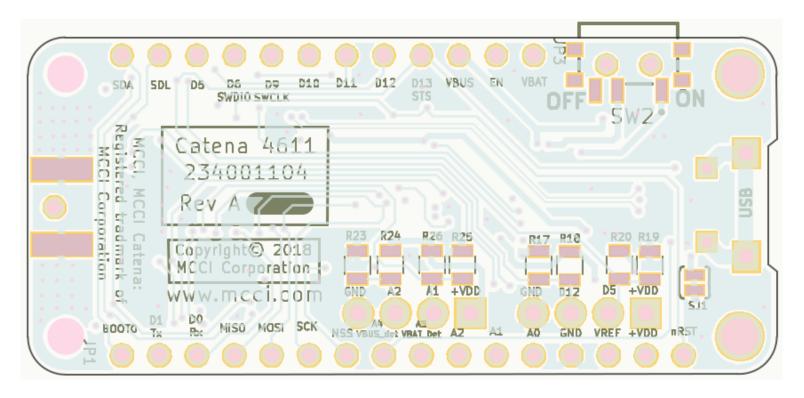
Title: Catena 4611 PCB Layout Fabrication Drawing - Ground Layer



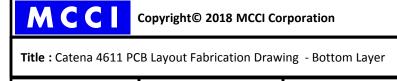
Power Layer



Title: Catena 4611 PCB Layout Fabrication Drawing - Power Layer



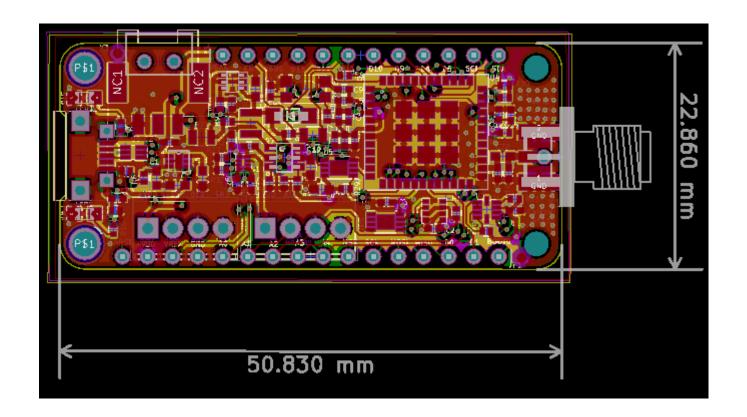
Bottom Layer



Date: 13-Jul-2018

Drawing number: 234001103

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Board Dimension

