# **Ousama Jamal Eddin**

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## **COMPUTER ENGINEERING STUDENT**

Motivated Student pursuing a CE degree. Passionate and punctual individual focused on teamwork and quality paired with outstanding communication and presentation abilities. Experienced in managing various projects and utilizing appropriate resources to meet strict deadlines.

**EXPERIENCE**July 2024 - Augsut 2024

During my internship at AvionChip Teknoloji, I worked on implementing an RPN calculator using Verilog and Vivado. I focused on designing and optimizing stack operations, as well as implementing basic arithmetic functions like addition and multiplication. I handled serial data input and ensured efficient data processing for accurate calculation execution. This experience provided hands-on knowledge of FPGA development and digital circuit design, while enhancing my skills in Verilog programming and the Vivado toolset.

## **EDUCATION**

## Student at FenerBahce University

As a third-year Computer Engineering student, my academic journey has fueled my passion for programming and technology. I've explored critical areas such as Logical System Design, Data Structures, Algorithms, Object-Oriented Programming (Python), and Software Engineering. My coursework in Computer Architecture, Al, Machine Learning, and Database Systems has enhanced my understanding of both software and hardware integration. This comprehensive foundation continues to drive my curiosity to learn, innovate, and apply new technologies to real-world challenges.

High School 2020 - 2021

2022 - Present

Diploma at Al-Thagher Highschool Jeddah, Saudi Arabia

FenerBahce University

Third-year Computer Engineering Student Istanbul, Turkey

## **KEY COMPETENCIES**

Coding Team Support
Touch typing Problem-solving
Flexible Schedule Time Management

## **PROGRAMMING SKILLS**

Proficient: Python, .DARTIntermediate: Verilog, C++, CBasic: Java, C#, php, html, css

**OTHER** 

Verilog and Verification skills DataBase (MySQL)

Android App Dev (Flutter)

Debugging Firebase

## LANGUAGE SKILLS

**Arabic:** Native **English:** Proficient

Turkish: B1 Level (Intermediate)

## **PROJECTS**

## **CPU Implementation Using Verilog**

I collaborated with a dynamic group on a comprehensive project where we collectively designed and implemented a fully operational CPU using Verilog. Our work involved delving into the intricacies of vital components such as the Program Counter (PC), Instruction Register (IR), Memory Address Register (MAR), Memory Data Register (MDR), Control Unit (C-Unit), Accumulator (ACC), and Arithmetic Logic Unit (ALU). The project showcased our proficiency in hardware description language and digital design.

#### **OTHERS**

<u>Expense Tracker</u>	<u>Shopping List</u>	<u>MealsApp</u>	<u>ChatApp</u>
<u>(Flutter app)</u>	<u>(Flutter app)</u>	<u>(Flutter App)</u>	<u>(Flutter App)</u>
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<u>Favorite Places</u>	<u>FlappyBird_clone</u>	<u>TreasureHunt</u>	<u>Hangman</u>

## COURSES

The Bits and Bytes of Computer Networking

**Technical Support Fundamentals** 

MATLAB Onramp Course by Mathworks Training services

Foundations of Cybersecurity

Flutter & Dart - The Complete Guide [2025 Edition]