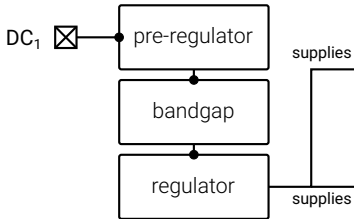
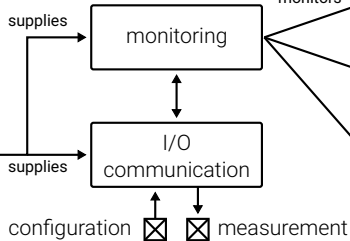


protected
primary regulator



on-chip
monitoring



primary regulator
under test

