

# TINLAB

Bread Enjoyers

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# Inhoudsopgave

# **1 SSS: System Subsystem Specification**

## **1.1 Scope**

## **1.2 System overview**

## **2 SSDD: System Subsystem Design Description**

### **2.1 System-wide design decisions**

### **2.2 System architectural design**

### **2.3 Sysyem components**

### **3 SDD: Software Design Description**

## **4 SRS: System Requirements Specification**

### **4.1 Requirements**

### **4.2 Required states and modes**

### **4.3 System capability requirements**

#### **4.3.1 (System capability)**

### **4.4 System external interface requirements**

### **4.5 System internal interface requirements**

### **4.6 System internal data requirements**

### **4.7 Adaptation requirements**

### **4.8 Safety requirements**

### **4.9 Security and privacy requirements**

### **4.10 System environment requirements**

### **4.11 System quality factors**

### **4.12 Design and construction constraints**

### **4.13 Other requirements**

## **5   IDD: Interface Design Description**

### **5.1   Interface design**

### **5.2   Interface identification and diagrams**

#### **5.2.1   Project unique identifier of interface**

## **6 STP: System Test Plan**

### **6.1 Scope**

### **6.2 Software test environment**

### **6.3 Software items**

### **6.4 Hardware and firmware items**

### **6.5 Other materials**

### **6.6 Test identification**

#### **6.6.1 General information**

#### **6.6.2 General test conditions**

#### **6.6.3 Test progression**

#### **6.6.4 Data recording, reduction and analysis**

### **6.7 Planned tests**

#### **6.7.1 (Project-unique identifier of a test)**



## **7 STR: System Test Report**

### **7.1 Overview of test results**

7.1.1 (Project-unique identifier of a test)

### **7.2 Test log**

## 8 Notes

## 9 Referenced documents

## 10 Appendix