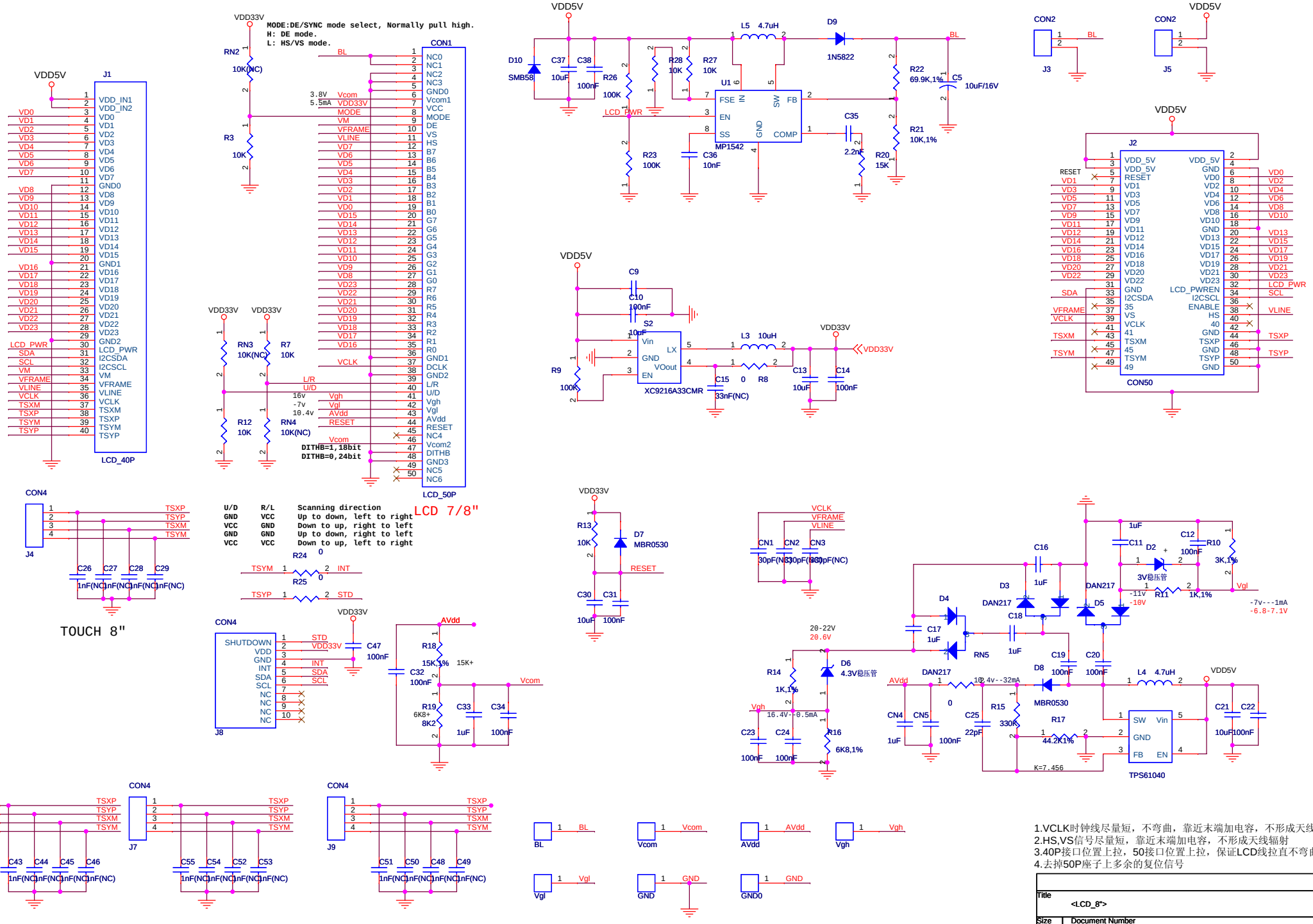
MODE:DE/SYNC mode select, Normally pull high.
H: DE mode.
L: HS/VS mode.
LCD_40P
LCD_50P
CON1
CON50
LCD 7/8"
TOUCH 8"
U/D R/L Scanning direction
GND VCC Up to down, left to right
VCC GND Down to up, right to left
GND GND Up to down, right to left
VCC VCC Down to up, left to right
DITHB=1,18bit
DITHB=0,24bit
MP1542
XC9216A33CMR
TPS61040
1.VCLK时钟线尽量短，不弯曲，靠近末端加电容，不形成天线辐射
2.HS,VS信号尽量短，靠近末端加电容，不形成天线辐射
3.40P接口位置上拉，50接口位置上拉，保证LCD线拉直不弯曲
4.去掉50P座子上多余的复位信号
Title
<LCD_8">
Size A3
Document Number
<1>
Rev 0.2
Date: Monday, April 23, 2012
Sheet 1 of 1