| treatment | reps_with_extinct_syms | reps_with_extinct_hosts |
|-----------------------|------------------------|-------------------------|
| CPU=16_MC=128_HRR=128 | 20 | 0 |
| CPU=16_MC=128_HRR=256 | 20 | 0 |
| CPU=16_MC=128_HRR=512 | 20 | 0 |
| CPU=16_MC=16_HRR=128 | 20 | 0 |
| CPU=16_MC=16_HRR=256 | 20 | 0 |
| CPU=16_MC=16_HRR=512 | 20 | 0 |
| CPU=16_MC=256_HRR=128 | 20 | 0 |
| CPU=16_MC=256_HRR=256 | 20 | 0 |
| CPU=16_MC=256_HRR=512 | 20 | 0 |
| CPU=16_MC=64_HRR=128 | 20 | 0 |
| CPU=16_MC=64_HRR=256 | 20 | 0 |
| CPU=16_MC=64_HRR=512 | 20 | 0 |
| CPU=32_MC=128_HRR=128 | 20 | 0 |
| CPU=32_MC=128_HRR=256 | 20 | 0 |
| CPU=32_MC=128_HRR=512 | 20 | 0 |
| CPU=32_MC=16_HRR=128 | 20 | 0 |
| CPU=32_MC=16_HRR=256 | 20 | 0 |
| CPU=32_MC=16_HRR=512 | 20 | 0 |
| CPU=32_MC=256_HRR=128 | 20 | 0 |
| CPU=32_MC=256_HRR=256 | 20 | 0 |
| CPU=32_MC=256_HRR=512 | 20 | 0 |
| CPU=32_MC=64_HRR=128 | 20 | 0 |
| CPU=32_MC=64_HRR=256 | 20 | 0 |
| CPU=32_MC=64_HRR=512 | 20 | 0 |
| CPU=4_MC=128_HRR=128 | 20 | 0 |
| CPU=4_MC=128_HRR=256 | 20 | 0 |
| CPU=4_MC=128_HRR=512 | 20 | 0 |
| CPU=4_MC=16_HRR=128 | 20 | 0 |
| CPU=4_MC=16_HRR=256 | 20 | 0 |
| CPU=4_MC=16_HRR=512 | 20 | 0 |
| CPU=4_MC=256_HRR=128 | 20 | 0 |
| CPU=4_MC=256_HRR=256 | 20 | 0 |
| CPU=4_MC=256_HRR=512 | 20 | 0 |
| CPU=4_MC=64_HRR=128 | 20 | 0 |
| CPU=4_MC=64_HRR=256 | 20 | 0 |
| CPU=4_MC=64_HRR=512 | 20 | 0 |
| CPU=8_MC=128_HRR=128 | 20 | 0 |
| CPU=8_MC=128_HRR=256 | 20 | 0 |
| CPU=8_MC=128_HRR=512 | 20 | 0 |
| CPU=8_MC=16_HRR=128 | 20 | 0 |
| CPU=8_MC=16_HRR=256 | 20 | 0 |
| CPU=8_MC=16_HRR=512 | 20 | 0 |
| CPU=8_MC=256_HRR=128 | 20 | 0 |
| CPU=8_MC=256_HRR=256 | 20 | 0 |
| CPU=8_MC=256_HRR=512 | 20 | 0 |
| CPU=8_MC=64_HRR=128 | 20 | 0 |
| | 20 | 0 |
| CPU=8_MC=64_HRR=256 | | |
| CPU=8_MC=64_HRR=512 | 20 | 0 |