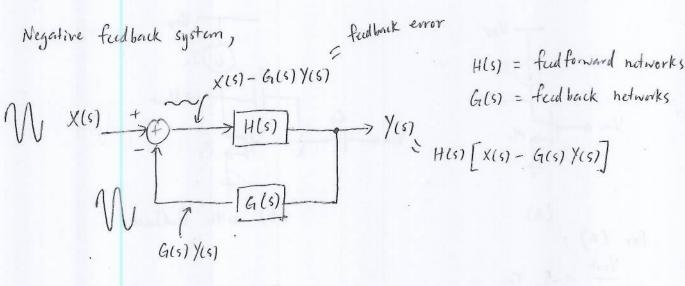
Chapter 7 - Feedback in Amplifiers

General Considerations



$$\frac{\gamma(s)}{\chi(s)} = \frac{\mu(s)}{(+6(s) + (s))}$$

$$\frac{Y(s)}{X(s)}$$
 = closed-loop transfer function
 $H(s)$ = open-loop transfer function (represents an amplifier)

More precise definition,

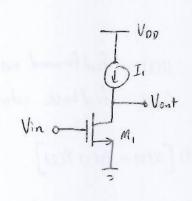
4(5) = frequency-independent quantity. (B -> feedback factor)

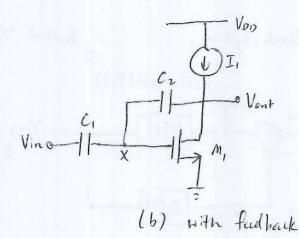
There are four elements in the feedback system:

- O feed forward amplifier
- (2) a means of sensing the output
- 3 feedback network
- (F) a means of generating the feedback error.

Properties of Feedback Circuits.

(1) Gain Desensitization





For (a):

-> poor definition of the gain. gm, and vo, very with process and temperature.

For 16):

$$\frac{V_{\text{out}}}{V_{\text{v}}} = -g_{m_1} r_{o_1}$$

Node analysis at point X:

$$\frac{V_{\text{out}}}{V_{\text{in}}} = \frac{1}{\left(1 + \frac{1}{g_{\text{mi}} r_{01}}\right) \frac{C_2}{C_1} + \frac{1}{g_{\text{mi}} r_{01}}}$$

If gmiro, is sufficiently large,

$$\frac{V_{\text{ont}}}{V_{\text{in}}} = -\frac{C_1}{C_2}$$

-> Gain can be controlled with much higher accuracy

-> If Co and Cr are made of same material, then process and temperature variations do not change C/Cz

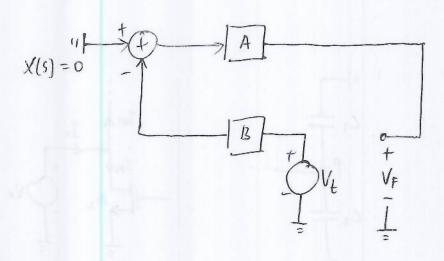
for more general cuse,

$$\frac{Y}{X} = \frac{A}{1 + BA}$$

$$\frac{1}{B} \left(\frac{1}{BA} \right)$$

If
$$BA \gg 1 \rightarrow \frac{Y}{X} \approx \frac{1}{B}$$

To compute loop gain,



$$V_t \beta(-1) A = V_F$$

$$\frac{V_F}{V_t} = -\beta A$$

For simple feedback circuit,

$$\begin{array}{c|c}
 & V_{0} \\
 & V_{t} \\
 & V_{f} \\
 &$$

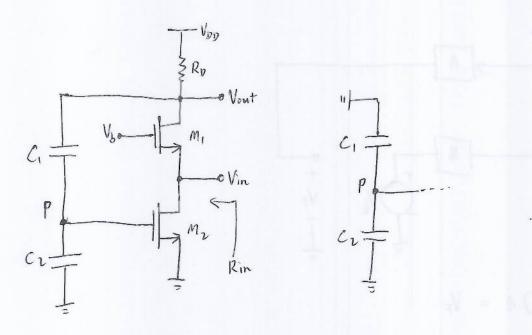
$$V_t = \frac{C_2}{C_1 + C_2} \left(-g_{m_1} r_{o_1} \right) = V_F$$

$$\frac{V_F}{V_t} = -\frac{C_2}{C_1 + C_2} g_{m_1} r_{01}$$

Four elements of feedback in this circuit?

- 1) Transistor M. and current source I, constitute the feedforward amplifier.
- (2) Capacitor Cz senses the output voltage and converts it to a current feedback signal, which is then added to the current produced by Vin through C1.

(2) Terminal Impedance Modification



common-gate with feedback

open-loop

calculation of input resistance.

(a) Consider
$$X \neq open-loop$$
 and $\lambda = 0$,
$$Rin, open = \frac{1}{g_{m_1} + g_{mb1}}$$

b) For closul-loop circuit,

$$V_{\text{out}} = \left(g_{\text{mi}} + g_{\text{mbi}}\right) V_{\text{x}} R_{\text{p}} - 0$$

$$V_{\text{p}} = \frac{C_{1}}{C_{1} + C_{2}} V_{\text{out}} - 0$$

4

$$V_{p} = \left(g_{m_{1}} + g_{mb_{1}}\right) V_{x} R_{0} \frac{C_{1}}{C_{1} + C_{2}}$$

Thus, small-signal drain current of Mz

Adding this current to the drain current of M :

$$I_{X} = I_{D_{1}} + I_{D_{2}}$$

$$= (g_{m_{1}} + g_{mb_{1}}) V_{X} + g_{m_{2}} (g_{m_{1}} + g_{mb_{1}}) \frac{c_{1}}{c_{1} + c_{2}} R_{D} V_{X}$$

$$= (g_{m_{1}} + g_{mb_{1}}) \left(1 + g_{m_{2}} R_{D} \frac{c_{1}}{c_{1} + c_{2}}\right) V_{X}$$

Therefore,

$$Rin, closed = \frac{V_x}{I_x}$$

$$= \frac{1}{g_{m1} + g_{mb1}} \cdot \frac{1}{1 + g_{m2}R_D} \cdot \frac{C_1}{C_1 + C_2}$$

$$= \frac{1}{g_{m1} + g_{mb1}} \cdot \frac{1}{1 + g_{m2}R_D} \cdot \frac{C_1}{C_1 + C_2}$$

$$= \frac{1}{g_{m1} + g_{m2}R_D} \cdot \frac{C_1}{C_1 + C_2}$$

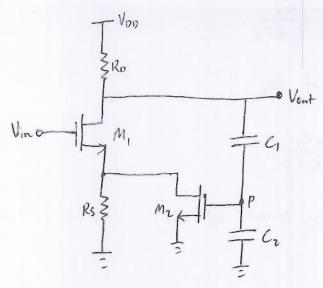
$$= \frac{1}{g_{m2} + g_{m2}R_D} \cdot \frac{C_1}{C_1 + C_2}$$

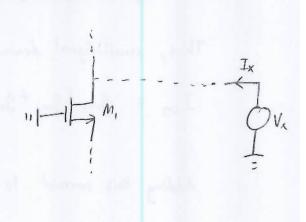
$$= \frac{1}{g_{m2} + g_{m2}R_D} \cdot \frac{C_1}{C_1 + C_2}$$

$$= \frac{1}{g_{m2} + g_{m2}R_D} \cdot \frac{C_1}{C_1 + C_2}$$

Four elements of fudback?

- (1) M, and Ro -> feed forward amplifiers
- (2) Co and Cz sense ontput
- 3 CI, Cz and Mz -> feedback network
- The substraction occurs in the current domain at the input terminal.





Cs stage with feedback

calculation of output resistance

$$I_{D_1} = V_X \frac{C_1}{c_1 + C_2} \frac{g_{m_2}}{g_{m_2}} \frac{R_S}{R_S + \frac{1}{g_{m_1} + g_{mb_1}}}$$

$$I_{D_2}$$
current divider rule

$$\frac{V_X}{I_X} = \frac{R_0}{1 + \frac{g_{mz}R_s(g_{mi} + g_{mbi})R_0}{(g_{mi} + g_{mbi})R_s + 1}} \frac{C_1}{c_1 + c_2}$$

- M, Rs and Ro constitute a common-source stage and c
- C1, C2 and M2 sense the output voltage
- Returning a current equal to [C1/(C1+C2)] Vont gmz to the source of M1.

(3) Bandwidth Modification.

Suppose the feedforward amplifier has a one-pole transfer function:

$$A(s) = \frac{A_0}{1 + \frac{s}{w_0}}$$

where Ao = low-frequency gain

Wo = 3-dB bandwidth

What is the transfer function of the closed-loop system?

$$\frac{A_0}{1+\frac{S}{N_0}} = \frac{A_0}{1+\frac{S}{N_0}}$$

$$1+\frac{A_0}{N_0}$$

$$= \frac{A_0}{1 + BA_0 + \frac{S}{W_0}}$$

closed-loop gain at low frequency

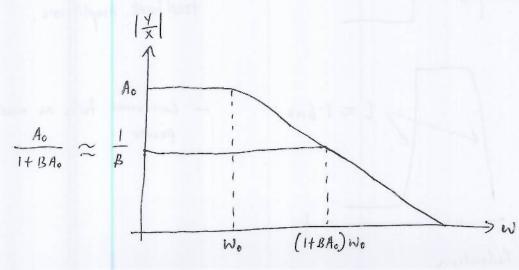
Ao

1+BAo

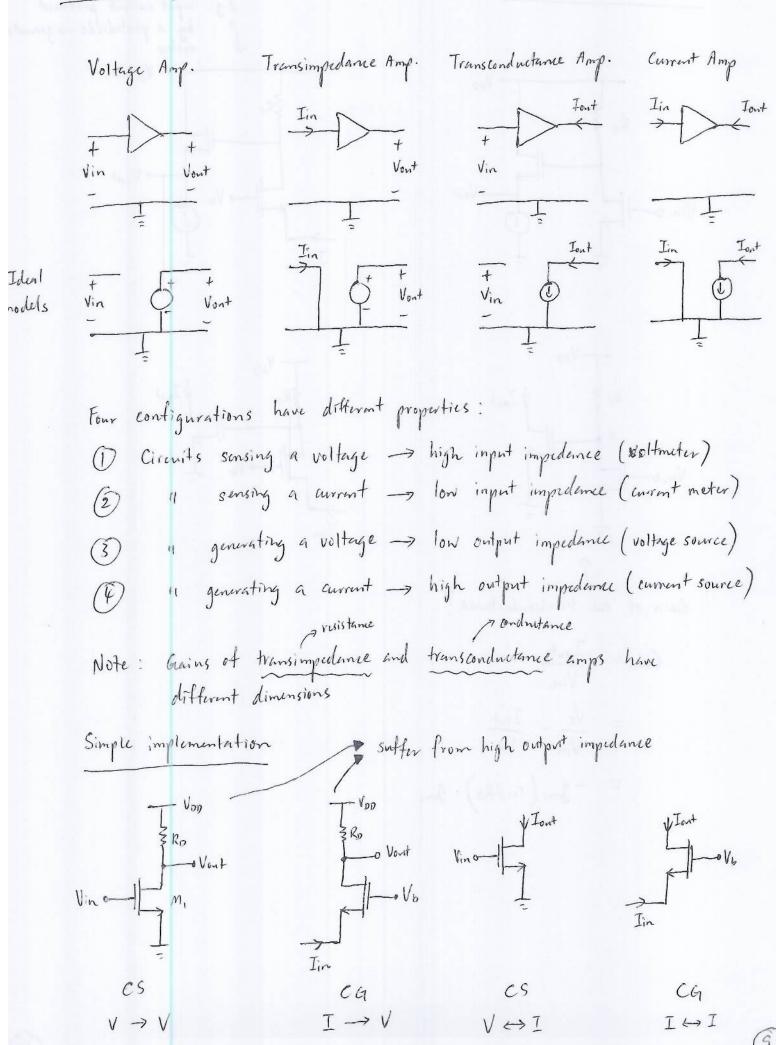
1+ S

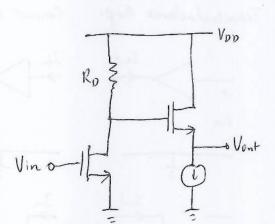
(1+BAo) No pole at (1+BAo) No

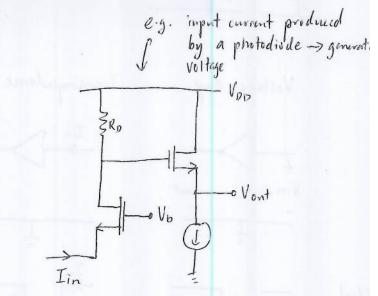
Thus, the 3-dB bandwidth has increased by a factor I+BA.

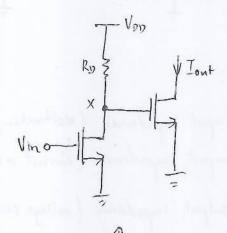


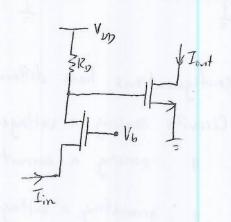
- Grain bandwidth product of one-pole system does not change. - How feedback improves the speed it a high gain is required? (E.S.) Suppose need to amplify a 20MHz square wave by a factor of 100 and maximum bandwidth but we have only a single-pole amplifier with an open-loop gain of 100 and 3-dB bandwidth of 10MHz. fords = lumhz Av = 100 Vow $T \approx 16 \text{ ns}$ (long risetime and fall time because time constant is equal to $1/(2\pi f_{3dB}) \approx 16 \text{ ns}$) fids = 100MHZ fula = 100MHz Av= 10 Bascade of two 100MHZ feedback amplifiers. Vont / ₹ 1. bns Consumes twice as much power (4) Nonlinearity Reduction









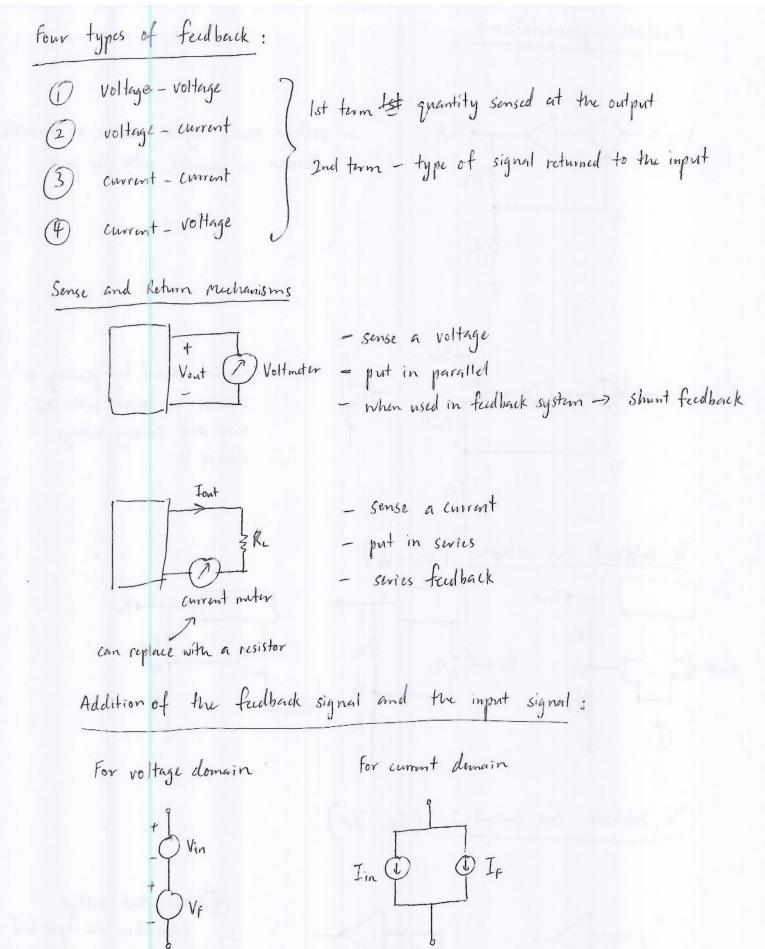


Gain of the transconducture?

$$G_{m} = \frac{I_{out}}{V_{in}}$$

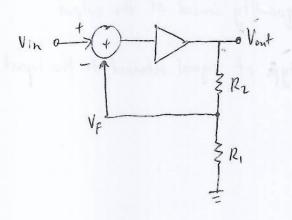
$$= \frac{V_{x}}{V_{in}} \cdot \frac{I_{out}}{V_{x}}$$

$$= -g_{m_{1}}(r_{oi}//R_{o}) \cdot g_{m_{2}}$$

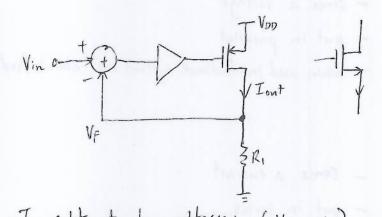


- no influence on the operation of the open-loop amplifier (ideally)

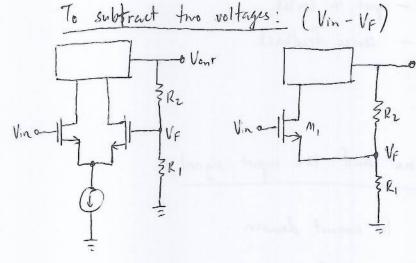
- introduces loading effects that must be taken into account (practically)

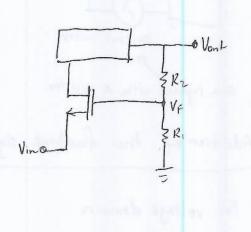


- voltage sensed by a resistive or capacitive divider in parallel with the port.

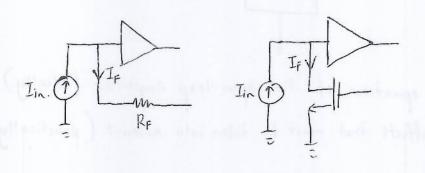


- current sensed by placing a resistor in series with the wire and sensing voltage across it.





To subtract two currents: (Iin-IF)



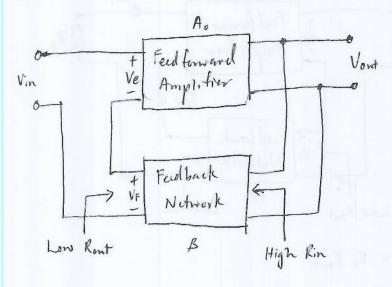
Note that voltage

Subtraction the input and
output signals are applied
feedback
to two distinct nodes
whereas current substraction
they are applied to a
single node.

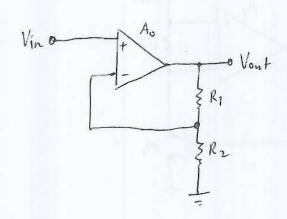
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Fudback Topologies

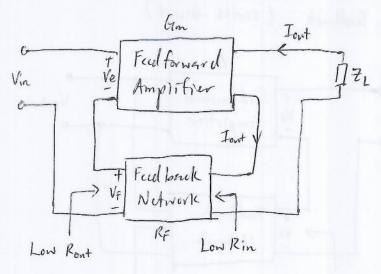
1 Voltage - voltage Feedback (series -shunt)



Simple example:



(2) Current - Voltage Feedback (series-series)



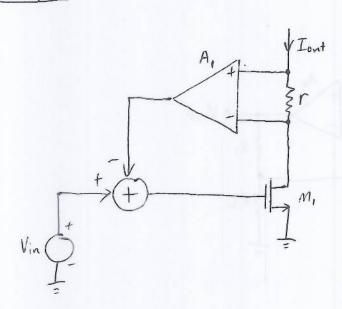
$$V_F = R_F I_{out}$$

$$V_e = V_{in} - R_F I_{out}$$

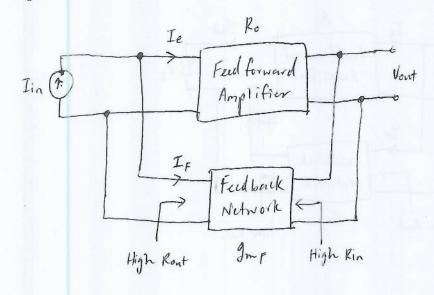
$$I_{out} = G_m \left(V_{in} - R_F I_{out} \right)$$

$$\frac{I_{out}}{V_{in}} = \frac{G_m}{1 + G_m R_F}$$

Example



(3) Voltage-Current Feedback (shunt-shunt)



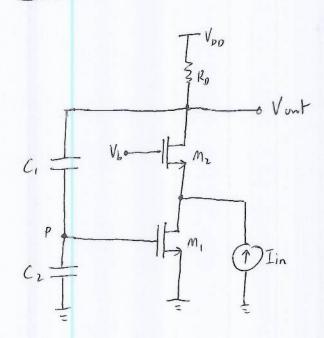
Te =
$$g_{mF}$$
 Vont

$$I_e = I_{in} - I_F$$

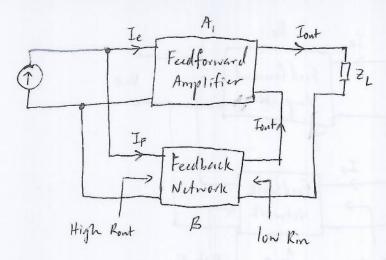
$$V_{ont} = R_o I_e = R_o \left(I_{in} - g_{mF} V_{ont} \right)$$

$$\frac{V_{ont}}{I_{in}} = \frac{R_o}{1 + g_{mF} R_o}$$

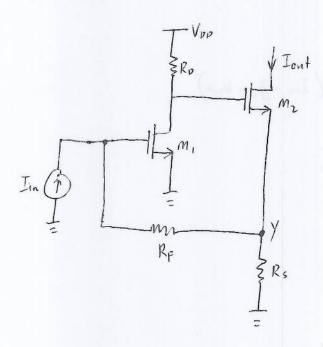
Example



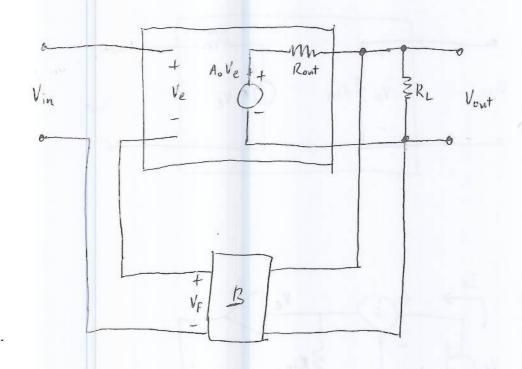
(4) Current- current Feedback (Shunt-series)

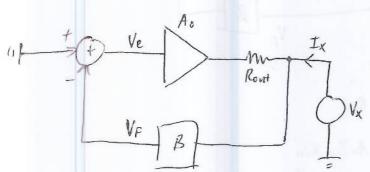


Example



Effect of voltage-voltage feedbak on output resistance





$$V_{P} = BV_{X}$$

$$V_{e} = -\beta V_{X}$$

$$V_{M} = -\beta A_{o} V_{X}$$

$$I_{X} = \left[V_{X} - \left(-\beta A_{o} V_{X} \right) \right] / R_{out}$$

$$\frac{V_{X}}{I_{X}} = \frac{R_{out}}{1 + \beta A_{o}}$$

Current - voltage

$$\frac{V_X}{I_X} = R_{out} (1 + G_{mRF})$$

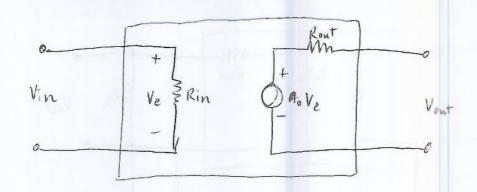
Voltage - current

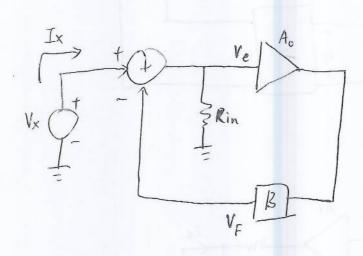
 $\frac{V_X}{I_X} = \frac{R_{out}}{1 + g_{mF} R_o}$

Current - current

 $\frac{V_X}{I_X} = R_{out} (1 + BA_o)$

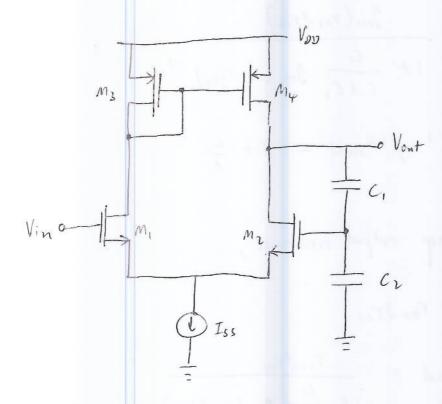
Effect of voltage - voltage feedback on input impedance



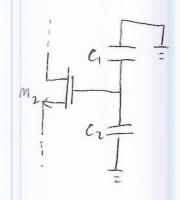


Ix Rin = Vx - BAO Ix Rin

(18



To find open-loop voltage gain, break the feedback loop



The open-loop gain, Aopen = gm, (roz // roy)

To find loop gain, $\frac{1}{\sqrt{1}}c_1$ $\frac{1}{\sqrt{1}}v_t$

$$V_F = -V_t \frac{C_1}{C_1 + C_2} g_{m_1}(r_{o_2} || r_{o_4})$$

Aclosed =
$$\frac{\int_{m_1} (r_{oz} / | r_{oy})}{1 + \frac{C_1}{C_1 + C_2}} \int_{m_1} (r_{oz} / | r_{oy})$$

(*) if
$$BA_0 >> 1$$
, $A_{closed} \approx 1 + \frac{C_2}{C_1}$

= Rout, closed =
$$\frac{r_{oz}/|r_{oy}|}{1+\frac{c_1}{c_1+c_2}g_{m_1}(r_{oz}/|r_{oy})}$$