AMS/IMS Read Me

# Operation

Pull up resistor used for IMS input and for AMS input. This is more wasteful of energy but if power to AMS or IMD fail it will open the emergency shutdown circuit. Failure state is LOW

A nand gate is used to implement not logic so that input to thyristors are HIGH when failure.

Both nand gates connected to two independent thyristors and relays. The relay is a 2-coil latching relay, with a set and a reset coil and will remain in the state it is set to when coils de-energised. When reset coil of relays are energised by the thyristors: one pair of relay poles are opened (disconnecting the emergency shutdown circuit), another pair of poles are closed (illuminating a led on the dashboard).

# To be completed

Board layout

Resistor values

# Possible Improvements

# List of components

See file AMS/IMD\_partlist