Report Lab 2 Experimental Physics for AI 2

Doğa Tekeli, Osea Fracchia, Carlotta Maria Portinaro, Gabriele Roberto Bovo

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Abstract

The study examines the potential differences across resistors and capacitors in RC circuits, and across resistors and inductors in RL circuits using an oscilloscope and deriving and validating time constants for these systems. The same setup is then used to study RLC circuits. These procedures help understanding the study of time-dependent circuit dynamics.

Chapter 1

Study of RC and RL circuits in pulsed current

1.1 Goal

We will study the behavior of RC and RL circuits in response to pulsed current generated by a square-wave signal.

Specifically, analyzing the potential differences across the resistor and capacitance (RC Circuit) and across the resistor and inductance (RL circuit).

Since we know the resistances of each experiment, we will calculate the time constant for the circuits given the formulas, for the RC circuit:

$$\tau = R \cdot C$$

where τ is the time constant in seconds, R is the resistance in ohms, Ω and C is the capacitance in farads, F.

For the RL circuit:

$$\tau = \frac{L}{R}$$

where τ is the time constant in seconds, R is the resistance in ohms, Ω and L is the inductance in henrys, H.

1.2 Method

We will use an oscilloscope to measure the time-varying voltage signals.

Before connecting the probes to our actual circuits, we check the probe compensation by connecting the probes to the test points in our oscilloscope. It is seen in figure 1.1 that the probes are correctly calibrated and therefore result in a square wave.

Then, we configure the RC circuit, the resistor and the capacitor are connected in series as seen in figure 1.2 with a square-wave signal generated by a function generator. We record the voltage waveforms during both the capacitor's charge and discharge phases with a voltage generator (battery) at frequency $f=1400\ Hz$, constant capacitance C and with different resistances.

We apply a similar procedure for the RL circuit at frequency $f{=}500~Hz$ and by keeping the inductance L as constant.

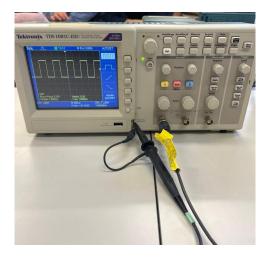


Figure 1.1: The testing of the probes

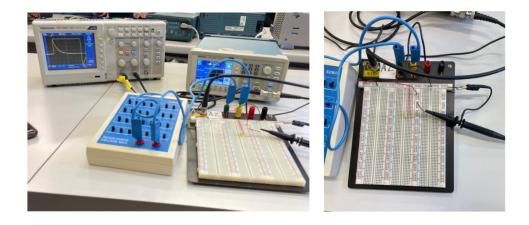


Figure 1.2: RC setup of the experiment

1.3 Data

In the manual, the TBS1000B Digital Storage Oscilloscope has its vertical accuracy specified as:

$$\pm (3\% \times \text{reading} + 0.2 \text{ div} + 1 \text{ mV}),$$

where the division size (div) depended on the selected vertical scale. The horizontal time base accuracy was:

 $\pm 50 \, \mathrm{ppm} \times \mathrm{time}$ measurement.

1.4 Analysis

1.4.1 The behavior of RC circuits

During the charging phase in RC circuits, when the circuit is closed with a voltage generator, the capacitor voltage $V_C(t)$ increases exponentially as:

$$V_C(t) = V\left(1 - e^{-t/\tau}\right)$$
, where $\tau = RC$ is the time constant.

The current I(t) decreases as:

$$I(t) = \frac{V}{R}e^{-t/\tau}.$$

During the discharging phase, when the circuit is closed on a short circuit, the capacitor voltage $V_C(t)$ decreases exponentially as:

$$V_C(t) = V_C(0)e^{-t/\tau}$$
.

Similarly, the current I(t) becomes:

$$I(t) = -\frac{V_C(0)}{R}e^{-t/\tau}.$$

The behavior when the polarity of the generator voltage is reversed, causes the capacitor to first discharge and then recharge in the opposite polarity:

$$V_C(t) = -V\left(1 - e^{-t/\tau}\right).$$

1.4.2 The behavior of RL circuits

Here, we analyze the charging and discharging phases of an RL circuit: During the charging phase, when the circuit is closed with a voltage generator, the current I(t) increases exponentially as:

$$I(t) = \frac{V}{R} \left(1 - e^{-t/\tau} \right), \quad \text{where } \tau = \frac{L}{R} \text{ is the time constant.}$$

meanwhile during the discharging phase, when the circuit is closed on a short circuit, the current I(t) decreases exponentially as:

$$I(t) = I(0)e^{-t/\tau}$$
.

The behavior when the polarity of the generator voltage is reversed, causes the current to reverse the direction as before.

$$I(t) = -\frac{V}{R} \left(1 - e^{-t/\tau} \right).$$

1.4.3 Oscilloscope Graphs of the RC Circuit

To study the charging and discharging behavior of an RC circuit, we conducted experiments with varying resistances (R) while maintaining a voltage generator with a fixed frequency of 1400 Hz. The resistances used in the experiment were $1\,\mathrm{M}\Omega$, $2\,\mathrm{M}\Omega$, $5\,\mathrm{M}\Omega$, and $10\,\mathrm{M}\Omega$. After setting the horizontal time scale and vertical voltage scale, corresponding oscilloscope readings are shown in Figures 1.3, 1.4, 1.5, and 1.6.

As the resistance increases the time constant of the circuit, $\tau = RC$, increases, leading to a slower charging and discharging process. The voltage across the capacitor (V_C) reaches steady-state values more gradually for higher resistances. The exponential nature of the charging and discharging curves is seen in the cases.

The following figures display the oscilloscope traces for different resistance values:

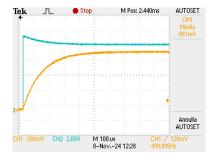


Figure 1.3: RC Circuit behavior with $R = 10 \,\mathrm{M}\Omega$

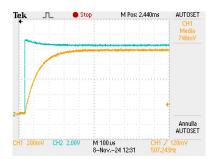


Figure 1.4: RC Circuit behavior with $R = 5 \,\mathrm{M}\Omega$

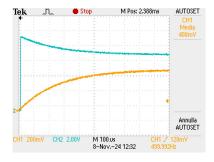


Figure 1.5: RC Circuit behavior with $R = 2 M\Omega$

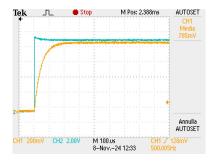


Figure 1.6: RC Circuit behavior with $R = 1 \,\mathrm{M}\Omega$

Estimating the Capacitance of an RC Circuit

We have seen earlier that in an RC charging circuit, the voltage across the capacitor V(t) follows the exponential law:

$$V(t) = V_{\text{max}} \left(1 - e^{-t/\tau} \right),\,$$

At $t = \tau$, the voltage reaches approximately 63% of V_{max} since $e^{-1} \approx 0.37$.

In the graph 1.3, we are given an RC circuit where the oscilloscope shows that the capacitor voltage reaches approximately $V_{\rm max}$ after 5 horizontal divisions, with the total time duration spanning approximately 10 seconds.

From this information, the time constant τ , corresponding to one-fifth of the total time (as the capacitor charges to V_{max} after approximately 5 time constants), is given by:

$$\tau = \frac{\text{Total time}}{5} = \frac{10\,\text{s}}{5} = 2\,\text{s}.$$

The time constant τ is related to the resistance R and the capacitance C of the RC circuit by the formula:

$$\tau = R \times C$$

Substituting the known value of $\tau = 2 \,\mathrm{s}$ and the resistance $R = 10 \,\mathrm{M}\Omega = 10^7 \,\Omega$:

$$C = \frac{\tau}{R} = \frac{2 \text{ s}}{10^7 \,\Omega} = 2 \times 10^{-7} \,\text{F} = 0.2 \,\mu\text{F}$$

Thus, the estimated capacitance of the circuit is:

$$C \approx 0.2 \,\mu\text{F}$$

1.4.4 Oscilloscope Graphs of the RL Circuit

To study the behavior of an RL circuit, we conducted experiments using varying resistances (R) while maintaining a voltage generator with a fixed frequency of 500 Hz. The resistances used in the experiment were $100\,\Omega$, $300\,\Omega$, $500\,\Omega$, and $1000\,\Omega$. After configuring the horizontal time scale and vertical voltage scale, the oscilloscope readings are presented in Figures 1.8, 1.9, 1.7, and 1.10.

As the resistance increases, the time constant of the RL circuit, $\tau = \frac{L}{R}$, decreases, resulting in a faster rise and decay of the current. This is reflected in the steeper exponential curves observed for higher resistance values. The voltage across the inductor (V_L) demonstrates the transient behavior expected in an RL circuit. In figure 1.7 since the resistance is small, it can be seen that the inductor opposes rapid changes in current more significantly.

The following figures display the oscilloscope traces for different resistance values:

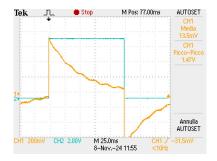


Figure 1.7: RL Circuit behavior with $R=100\,\Omega$

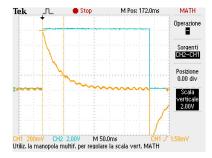


Figure 1.8: RL Circuit behavior with $R=300\,\Omega$

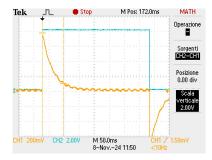


Figure 1.9: RL Circuit behavior with $R=500\,\Omega$

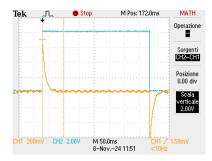


Figure 1.10: RL Circuit behavior with $R=1000\,\Omega$

Estimating the Inductance of an RL Circuit

Let's use figure 1.10 with $R=1000\,\Omega$ where τ is defined as before:

$$\tau = \frac{L}{R}$$

At $t = \tau$, the current reaches approximately 63% of $I_{\rm max}$ since $e^{-1} \approx 0.37$.

In the graph, we observe an RL circuit with a resistance $R=1000\,\Omega$. The oscilloscope indicates that the current reaches 63% of $I_{\rm max}$ after approximately 2 horizontal divisions, let's assume the time for one division corresponds to 1 ms. Therefore, the time constant becomes:

$$\tau = 2 \, \mathrm{divisions} \times 1 \, \mathrm{ms/division} = 2 \, \mathrm{ms}$$

Using the relationship between the time constant, resistance, and inductance:

$$L = \tau \cdot R$$

Substituting $\tau = 2\,\mathrm{ms} = 2\times 10^{-3}\,\mathrm{s}$ and $R = 1000\,\Omega$:

$$L \approx 2 \times 10^{-3} \,\mathrm{s} \cdot 1000 \,\Omega = 2 \,\mathrm{H}$$

Chapter 2

Study of RLC circuits in pulsed current

2.1 Goal

In this part of the experiment, our main goal is to study the trend of potential difference at the ends of each part of the RLC circuit stressed by a square-wave signal. Based on the observed waveform, we had to determine the period of the oscillations in the different regimes and compare it to the formula varying R.

2.2 Setup

The setup of the RLC circuit is shown in figure 2.1. The circuit consists of a resistor, inductor, and capacitor connected in series. The square-wave signal generated by the function generator is applied to the circuit. One probe of the oscilloscope is used to measure the voltage signals at the ends of the resistor, inductor, and capacitor, and the other probe at the ends of the resistor only.

2.3 Method

Once we constructed the RLC circuit (image 2.1a), a function generator was used to produce a square-wave signal of frequency f = 500 Hz. Using an oscilloscope, we measured each voltage signal at the ends of the different R chosen, using a probe.

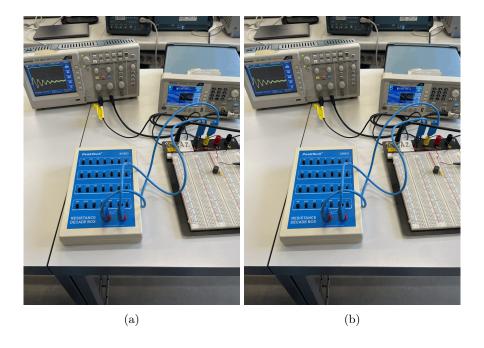
We calibrated the channels of the screen of the oscilloscope so that the third maximum of VR(t) si no less than $\frac{1}{10}$ of the first and the frequency of the square wave such that at least 5 local maxima are seen. Instead for critically damped and overdamped circuits, we set the frequency of the square wave such that VR(t) can be seen reduced to at least $\frac{1}{10}$ of its value. To visualize at best the waves we fixed the channels at CH1 = 50.0mV and CH2 = 2.00V

It is possible to observe two distinct lines that represent the two channels, CH1 and CH2 that are: the orange one (CH1) is the behavior of the circuit, while the second channel CH2 is the tension entering circuit.

2.4 Analysis

2.4.1 Behavior of RLC

The RLC circuit is made of three components:



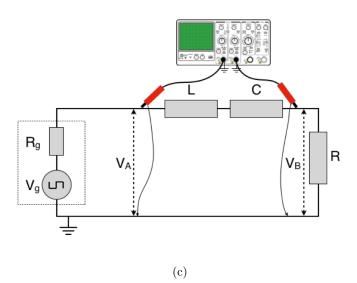


Figure 2.1: Setup of the experiment

- Resistor (R): Dissipates energy as heat.
- Inductor (L): Stores energy in its magnetic field.
- \bullet Capacitor (C): Stores energy in its electric field.

The governing equation for the RLC circuit can be derived using Kirchhoff's Voltage Law:

$$V_R + V_L + V_C = 0 \implies L \frac{d^2q}{dt^2} + R \frac{dq}{dt} + \frac{q}{C} = 0$$

When we have a damped oscillator, the general solution is:

$$q(t) = Q_0 e^{-\alpha t} \cos(w_d t + \phi)$$

where

- $\alpha = \frac{R}{2L}$ is the damping term
- $W_d = \left(\frac{1}{LC} \left(\frac{R}{LC}\right)^2\right)^{\frac{1}{2}}$ is the damped angular frequency
- $-Q_0$ is the initial charge on the capacitor

The period in the RLC circuit is found as:

$$T = \frac{2\pi}{w_d} = \frac{1}{f}$$

2.4.2 Underdamped case

In an underdamped circuit, the capacitor discharges because the energy oscillates between the capacitor and the inductor while gradually being lost as heat in the resistor. The resistance must be

$$R < \sqrt{\frac{4L}{C}}$$

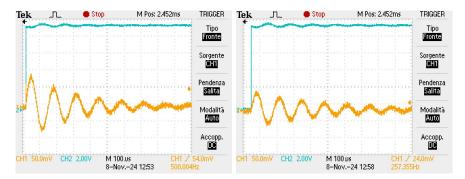


Figure 2.2: Underdamped RLC circuit

For both pictures in figure 2.2 we have an underdamped circuit, the period we can calculate from $T = \frac{1}{f}$ is

$$T_1 = \frac{1}{500.004Hz} = 0.0019s$$
 $T_2 = \frac{1}{257.355Hz} = 4ms$

Instead of calculating the period from the images we have to multiply the number of divisions with the time per division. In 2.2 left specifically we have:

$$T_1 = 2 \text{ divisions } \cdot 100 \frac{\text{microseconds}}{\text{divisions}} = 200 \mu s = 0.0002 s$$

while for the right image we have:

$$T_2 = 3.9 \text{ divisions } \cdot 100 \frac{\text{microseconds}}{\text{divisions}} = 390 \mu s = 0.00039 s$$

2.4.3 Overdamped case

In an overdamped circuit, the resistor dominates, and the oscillations are suppressed, leading to a monotonic decay of charge and current, and we get a fast decay without oscillations. The resistance must be

$$R > \sqrt{\frac{4L}{C}}$$

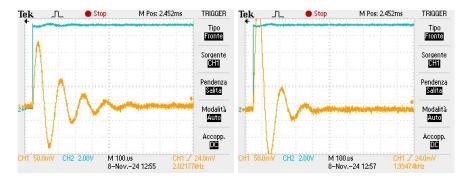


Figure 2.3: Overdamped RLC circuit

Pictures in figure 2.3 show an overdamped circuit, the period we can enumerate is

$$T_3 = \frac{1}{2.02177 \cdot 10^3 Hz} = 5 \cdot 10^{-4} s$$
 $T_4 = \frac{1}{1.99474 \cdot 10^3 Hz} = 5 \cdot 10^{-4} s$

Alternatively, the period from the images can be found by multiplying the number of divisions by the time per division. In the left image of 2.3 specifically we have that

$$T_3 = 5 \text{ divisions } \cdot 100 \frac{\text{microseconds}}{\text{divisions}} = 500 \mu s = 0.0005 s$$

while for the right image we have that

$$T_4 = 5.2 \text{ divisions} \cdot 100 \frac{\text{microseconds}}{\text{divisions}} = 520 \mu s = 0.00052 s$$

2.4.4 Critically damped case

In a critically damped circuit, we have the same state as for the overdamped one but we get a slow decay without oscillations. The resistor must be

$$R = R_C = \sqrt{\frac{4L}{C}}$$

We didn't observe any critical dumping in our analysis.

2.5 Conclusions

We studied the trend of the potential difference at the end of the resistor, the inductance and the conductor of our RLC circuit stressed by a square-wave signal.

Then we determined the period of the oscillations in the different regimes that we observed on the oscilloscope and we compared it to the one calculated from the theory. We noticed that for the underdamped circuit in figure 2.2 we have a dismatch in the two results. Instead for the overdamped circuit the results are consistent.