

#### N-Channel Enhancement Mode MOSFET

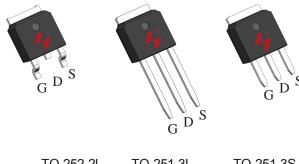
#### **Feature**

40V/72A

 $R_{DS(ON)} = 4.8 \text{m}\Omega(\text{typ.}) @V_{GS} = 10 \text{V}$  $R_{DS(ON)}$ = 5.8m $\Omega$ (typ.)@V<sub>GS</sub> = 4.5V

- 100% Avalanche Tested
- Reliable and Rugged
- Halogen Free and Green Devices Available (RoHS Compliant)

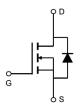
## **Pin Description**



TO-252-2L TO-251-3L TO-251-3S

### **Applications**

- Switching Application
- Power Management for DC/DC



N-Channel MOSFET

# **Ordering and Marking Information**



Package Code

D: TO-252-2L U: TO-251-3L V:TO-251-3S

Date Code **Assembly Material** YYXXX WW G:Halogen Free

Note: HUAYI lead-free products contain molding compounds/die attach materials and 100% matte tin plateTermi-Nation finish; which are fully compliant with RoHS. HUAYI lead-free products meet or exceed the lead-Free requirements of IPC/JEDEC J-STD-020 for MSL classification at lead-free peak reflow temperature. HUAYI defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this pr -oduct and/or to this document at any time without notice.



## **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Unit	
Common Rat	ings (Tc=25°C Unless Otherwise Noted)		·	
VDSS	Drain-Source Voltage		40	V
Vgss	Gate-Source Voltage		±20	V
TJ	Maximum Junction Temperature		175	°C
Tstg	Storage Temperature Range		-55 to 175	°C
Is	Source Current-Continuous(Body Diode) Tc=25°C		72	А
Mounted on	Large Heat Sink			•
Ірм	Pulsed Drain Current *	Tc=25°C	288	А
1	Outlinear Projection	Tc=25°C	72	Α
lσ	Continuous Drain Current	Tc=100°C	51	Α
		Tc=25°C	62.5	W
Po	Maximum Power Dissipation	Maximum Power Dissipation Tc=100°C		W
R₀c	Thermal Resistance, Junction-to-Case		2.4	°C/W
ReJA	Thermal Resistance, Junction-to-Ambient	Thermal Resistance, Junction-to-Ambient **		°C/W
Eas	Single Pulsed-Avalanche Energy ***	L=0.3mH	134	mJ

Note: \* Repetitive rating; pulse width limited by max. junction temperature.

\*\* Surface mounted on FR-4 board.

\*\*\* Limited by TJmax , starting TJ=25°C, L = 0.3mH, RG=  $25\Omega$ , VGS =10V.

## Electrical Characteristics (Tc = 25°C Unless Otherwise Noted)

Comple of	Davamatar	Test Conditions	HY1904					l lmi4
Symbol	Symbol Parameter Test Conditions		Min	Тур.	Max	Unit		
Static Char	Static Characteristics							
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V,I <sub>DS</sub> =250μA	40	-	-	V		
Ipss	Drain to Source Leakage Current	V <sub>DS</sub> =40V,V <sub>GS</sub> =0V	-	-	1	μA		
IDSS	Drain-to-Source Leakage Current	TJ=125°C	-	-	50	μA		
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250µA	1	1.7	3	V		
Igss	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	±100	nA		
Dragovi*	Drain Source On State Registance	V <sub>GS</sub> =10V,I <sub>DS</sub> =36A	-	4.8	6.0	O		
RDS(ON)	R <sub>DS(ON)*</sub> Drain-Source On-State Resistance V <sub>GS</sub> =4.5V,I <sub>DS</sub> =36A		-	5.8	7.0	mΩ		
Diode Char	Diode Characteristics							
V <sub>SD</sub> *	Diode Forward Voltage	I <sub>SD</sub> =36A,V <sub>GS</sub> =0V	1	0.83	1.1	V		
trr	Reverse Recovery Time	lon=26A dlon/dt=100A/ug	-	53	-	ns		
Qrr	Reverse Recovery Charge	Isp=36A,dIsp/dt=100A/µs	-	78	-	nC		



# Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Symbol	Barrantan	Test Conditions HY1904			I I m ! 4	
	Parameter	Test Conditions	Min	Тур.	Max	Unit
Dynamic	Characteristics					
Rg	Gate Resistance	$V_{GS}$ =0V, $V_{DS}$ =0V,F=1 MHz	-	1.6	-	Ω
Ciss	Input Capacitance	Vgs=0V,	-	2164	-	
Coss	Output Capacitance	V <sub>DS</sub> =25V,	-	202	-	рF
Crss	Reverse Transfer Capacitance	Frequency=1.0MHz	-	75	-	
td(ON)	Turn-on Delay Time		-	23	-	
Tr	Turn-on Rise Time	$V_{DD}$ =20 $V$ , $R_{G}$ =4 $\Omega$ ,	-	28	-	
td(OFF)	Turn-off Delay Time	IDS=36A,VGS=10V	-	29	-	ns
Tf	Turn-off Fall Time		-	34	-	
Gate Cha	Gate Charge Characteristics					
Qg	Total Gate Charge	\/ -20\/ \/ -40\/	-	51.5	-	
Qgs	Gate-Source Charge	$V_{DS} = 32V, V_{GS} = 10V,$ $I_{D} = 36A$	-	5.5	-	nC
Qgd	Gate-Drain Charge	ID-30A	-	11	-	

Note: \*Pulse test, pulse width ≤ 300us, duty cycle ≤ 2%



## **Typical Operating Characteristics**

**Figure 1: Power Dissipation** 

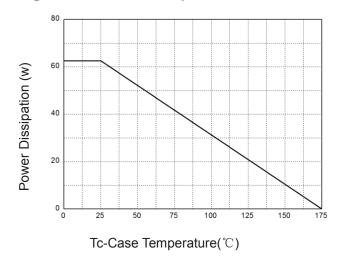


Figure 2: Drain Current

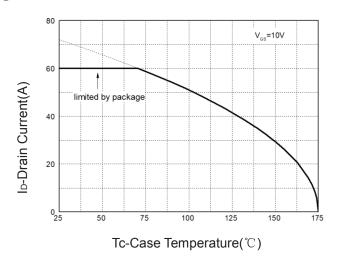
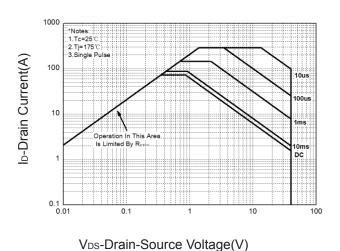
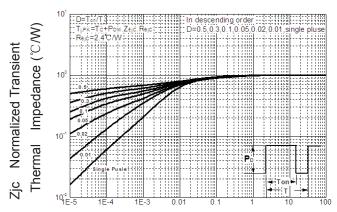


Figure 3: Safe Operation Area



**Figure 4: Thermal Transient Impedance** 



Maximum Effective Transient Thermal Impedance, Junction-to-Case

**Figure 5: Output Characteristics** 

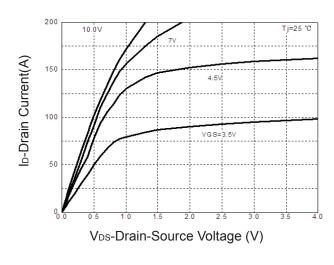
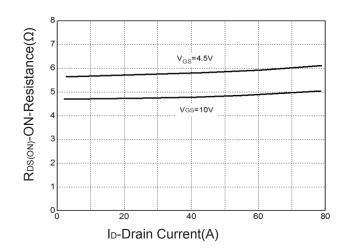


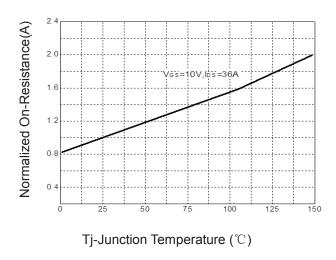
Figure 6: Drain-Source On Resistance



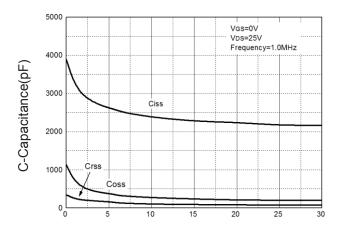


## **Typical Operating Characteristics(Cont.)**

Figure 7: On-Resistance vs. Temperature

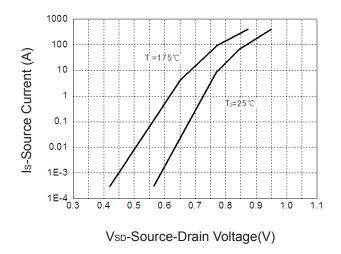


**Figure 9: Capacitance Characteristics** 

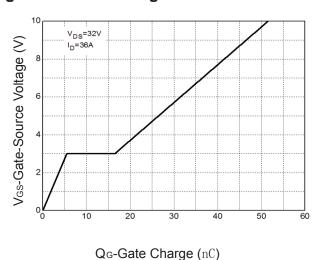


V<sub>DS</sub>-Drain-Source Voltage (V)

Figure 8: Source-Drain Diode Forward

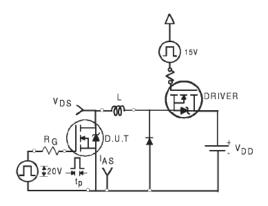


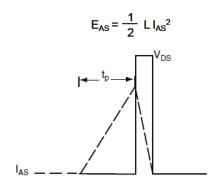
**Figure 10: Gate Charge Characteristics** 



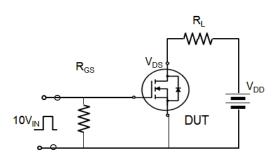


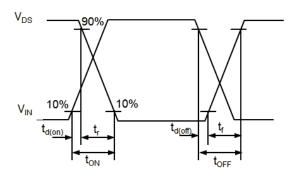
## **Avalanche Test Circuit**



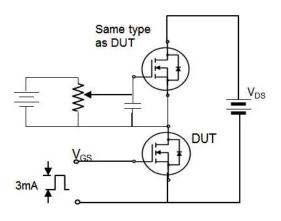


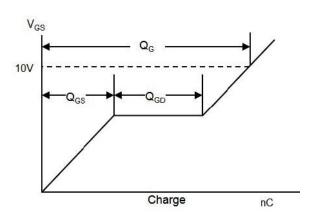
# **Switching Time Test Circuit**





# **Gate Charge Test Circuit**





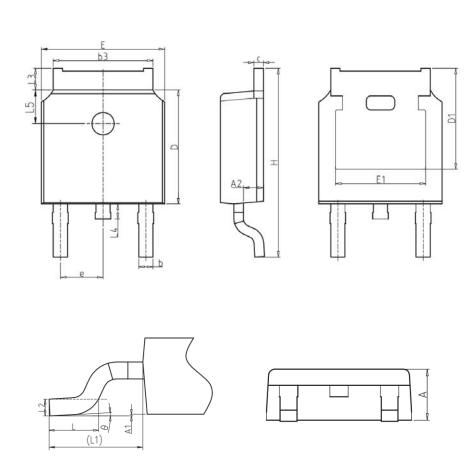


# Device Per Unit

Package Type	Unit	Quantity
TO-252-2L	Tube	75
TO-252-2L	Reel	2500
TO-251-3L	Tube	75
TO-251-3S	Tube	75

# **Package Information**

TO-252-2L

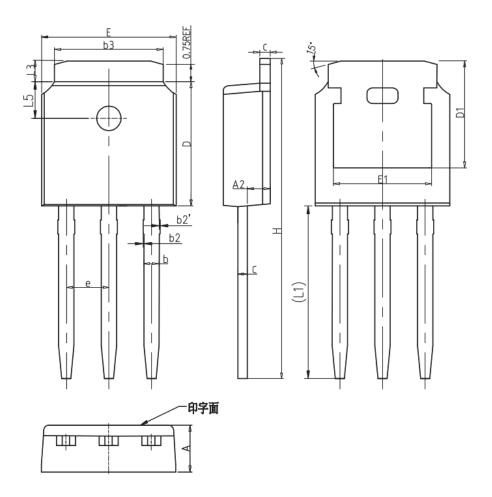


### **COMMON DIMENSIONS**

SYMBOL		mm	
STIVIDOL	MIN	NOM	MAX
Α	2.20	2.30	2.40
A1	0.00	-	0.20
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b3	5.20	5.33	5.50
С	0.43	0.53	0.63
D	5.98	6.10	6.22
D1	5.30REF		
E	6.40	6.60	6.80
E1	4.63	-	-
е		2.286BS	C
Н	9.40	10.10	10.50
L	1.38	1.50	1.75
L1		2.90REF	•
L2	0.51BSC		
L3	0.88	-	1.28
L4	-	-	1.00
L5	1.65	1.80	1.95
θ	0°	-	8°



TO-251-3L

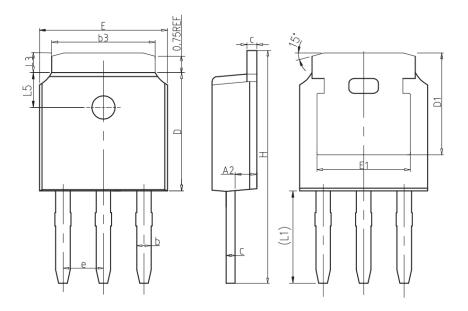


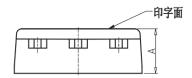
#### **COMMON DIMENSIONS**

CVMDOL		mm	
SYMBOL	MIN	NOM	MAX
А	2.20	2.30	2.40
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b2	0.00	0.04	0.10
b2'	0.00	0.04	0.10
b3	5.20	5.33	5.50
С	0.43	0.53	0.63
D	5.98	6.10	6.22
D1	5.30REF		
E	6.40	6.60	6.80
E1	4.63	-	-
е	2.286BSC		
Н	16.22	16.52	16.82
L1	9.15	9.40	9.65
L3	0.88	1.02	1.28
L5	1.65	1.80	1.95



### TO-251-3S



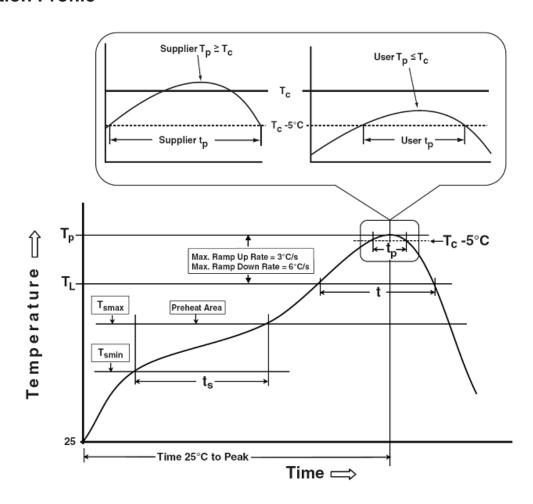


#### **COMMON DIMENSIONS**

CVMDOL		mm	
SYMBOL	MIN	NOM	MAX
А	2.20	2.30	2.40
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b3	5.20	5.33	5.50
С	0.43	0.53	0.63
D	5.98	6.10	6.22
D1		5.30REF	
Е	6.40	6.60	6.80
E1	4.63	-	-
е		2.286BSC	
Н	10.00	11.22	11.44
L1	3.90	4.10	4.30
L3	0.88	1.02	1.28
L5	1.65	1.80	1.95



### **Classification Profile**



## **Classification Reflow Profiles**

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly		
Preheat & Soak	100 °C	150 °C		
Temperature min (T <sub>smin</sub> )	150 °C	200 °C		
Temperature max (T <sub>smax</sub> )	60-120 seconds	60-120 seconds		
Time (Tsmin to Tsmax) (t <sub>s</sub> )	00-120 Seconds	00-120 Seconds		
Average ramp-up rate	3 °C/second may	2°C/cocond mov		
(T <sub>smax</sub> to T <sub>P</sub> )	3 °C/second max.	3°C/second max.		
Liquidous temperature (T <sub>L</sub> )	183 °C	217 °C		
Time at liquidous (t∟)	60-150 seconds	60-150 seconds		
Peak package body Temperature	See Classification Temp in table 1	See Classification Temp in table 2		
(T <sub>p</sub> )*	See Classification Temp in table 1	See Classification Temp in table 2		
Time (t <sub>P</sub> )** within 5°C of the specified	20**	20**		
classification temperature (T <sub>c</sub> )	20** seconds	30** seconds		
Average ramp-down rate (Tp to Tsmax)	6 °C/second max.	6 °C/second max.		
Time 25°C to peak temperature	6 minutes max.	8 minutes max.		

<sup>\*</sup>Tolerance for peak profile Temperature (Tp) is defined as a supplier minimum and a user maximum.

<sup>\*\*</sup> Tolerance for time at peak profile temperature (t<sub>p</sub>) is defined as a supplier minimum and a user maximum.



Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

#### Table 2.Pb-free Process – Classification Temperatures (Tc)

Package	Volume mm³	Volume mm³	Volume mm³
Thickness	<350	350-2000	≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

## **Reliability Test Program**

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168 Hrs/500 Hrs/1000Hrs, Bias @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -55°C~150°C

#### **Customer Service**

Worldwide Sales and Service: sales@hymexa.com Technical Support: Technology@hymexa.com

Huayi Microelectronics Co., Ltd.

No.8928, Shangji Road, Economic and Technological Development Zone, Xi'an, China

TEL: (86-029) 86685706 FAX: (86-029) 86685705 E-mail: sales@hymexa.com Web net: www.hymexa.com