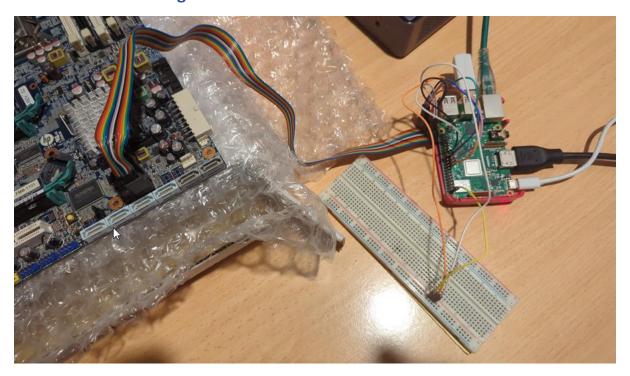
Connection & Reading



Used Rasbperry Pi 3B.

Connectors were mainly M-F for most connections from the plug of the clip (directly connected) and M-M used for power on the breadboard due to not enough pins available on the Pi.

Wiring diagram on SuperThunders write-up. Tried to minimise connections, particularly for data lines.

```
oysters@raspberrypi:~ $ flashrom -p linux_spi:dev=/dev/spidev0.0, spispeed=10000 flashrom unknown on Linux 6.12.25+rpt-rpi-v8 (aarch64) flashrom is free software, get the source code at https://flashrom.org

Using clock_gettime for delay loops (clk_id: 1, resolution: 1ns).
Found Winbond flash chip "W25Q128.V" (16384 kB, SPI) on linux_spi.
===

This flash part has status UNTESTED for operations: WP
The test status of this chip may have been updated in the latest development version of flashrom. If you are running the latest development version, please email a report to flashrom@flashrom.org if any of the above operations work correctly for you with this flash chip. Please include the flashrom log file for all operations you tested (see the man page for details), and mention which mainboard or programmer you tested in the subject line.

Thanks for your help!

No operations were specified.
```

Successful connection will show "Found" and your chip type in this case wasthe standard Winbond.

```
oysters@raspberrypi: S flashrom -p linux_spi:dev=/dev/spidev8.0,spispeed=1000 -r z426
flashrom unknown on Linux 6.12.25+rpt-rpi-v8 (aarch64)
flashrom is free software, get the source code at https://flashrom.org
Using clock_gettime for delay loops (clk_id: 1, resolution: ins).
Found Winbond flash chip "W25Q128.V" (16384 kB, SPI) on linux_spi,
This flash part has status UNTESTED for operations: WP
The test status of this chip may have been updated in the latest development
version of flashrom. If you are running the latest development version,
please email a report to flashrom@flashrom.org if any of the above operations
work correctly for you with this flash chip. Please include the flashrom log
file for all operations you tested (see the man page for details), and mention
which mainboard or programmer you tested in the subject line.
Thanks for your help!
Reading flash... done.
oysters@raspberrypi: $ sha256sum z420_bios_1.bin
aebf3f8f38989ca4d36b2542cf9d289c3a7eb1532c95235c54147b9468155302 z420_bios_1.bin
               errypi: $ flashrom -p linux_spi:dev=/dev/spidev0.0,spispeed=1000 -r z420
flashrom unknown on Linux 6.12.25+rpt-rpi-v8 (aarch64)
flashrom is free software, get the source code at https://flashrom.org
 Using clock gettime for delay loops (clk_id: 1, resolution: 1ns).
 Found Winbond flash chip "W25Q128.V" (16384 kB, SPI) on linux_spi.
 ---
 This flash part has status UNTESTED for operations: WP
 The test status of this chip may have been updated in the latest development
 version of flashrom. If you are running the latest development version,
 please email a report to flashrom@flashrom.org if any of the above operations
 work correctly for you with this flash chip. Please include the flashrom log
 file for all operations you tested (see the man page for details), and mention
 which mainboard or programmer you tested in the subject line.
 Thanks for your help!
 Reading flash... done.
 oysters@raspberryp1: $ sha256sum z420_bios_2.bin
a0bf3f8f38989ca4d36b2542cf9d289c3a7eb1532c95235c54147b9468155302 z420_bios_2.bin
 oysters@raspberrypi: 5 flashrom -p linux_spi:dev=/dev/spidev0.0, spispeed=1000 -r z420_bid
 flashrom unknown on Linux 6.12.25+rpt-rp1-v8 (aarch64)
 flashrom is free software, get the source code at https://flashrom.org
 Using clock_gettime for delay loops (clk_id: 1, resolution: ins).
 Found Winbond flash chip "W250128,V" (16384 kB, SPI) on linux_spi.
  This flash part has status UNTESTED for operations: WP
 The test status of this chip may have been updated in the latest development version of flashrom. If you are running the latest development version,
  please email a report to flashrom@flashrom.org if any of the above operations work correctly for you with this flash chip. Please include the flashrom log
  file for all operations you tested (see the man page for details), and mention
  which mainboard or programmer you tested in the subject line.
  Thanks for your help!
Reading flash... done,
  oysters@raspberryp1: S sha256sum z420_bios_3.bin
a@bf3f8f38989ca4d36b2542cf9d289c3a7eb1532c95235c54147b9468155302 z420_bios_3.bin
```

Same hash values from 3 reads.

Checking

Completed 3 downloads from the chip (saved as z420_bios_1, 2, 3.bin) and checked hash value to ensure all are the same.

```
oysters@raspberrypi:~ $ sha256sum z420_bios_3.bin
a0bf3f8f38989ca4d36b2542cf9d289c3a7eb1532c95235c5414
oysters@raspberrypi:~ $ du -b z420_bios_1.bin
16777216          z420_bios_1.bin
oysters@raspberrypi:~ $ du -b z420_bios_2.bin
16777216          z420_bios_2.bin
oysters@raspberrypi:~ $ du -b z420_bios_3.bin
16777216          z420_bios_3.bin
oysters@raspberrypi:~ $
```

Checked size, all the same, should be 16777216.

Writing to Chip

```
oysters@raspberrypi:~ $ flashrom -p linux_spi:dev=/dev/spidev0.0,spispeed=10000 -w z420_bios_1 }
flashrom unknown on Linux 6.12.25+rpt-rpi-v8 (aarch64
flashrom is free software, get the source code at https://flashrom.org
Using clock_gettime for delay loops (clk_id: 1, resolution: ins). flashrom was built with GCC 12.2.0, little endian
Command line (5 args): flashrom -p linux_spi:dev=/dev/spidev0.0,spispeed=10000 -w z429_bios_1_Mod
Initializing linux_spi programmer
Using device /dev/spidev0.0
Using 10000kHz clock
get_max_kernel_buf_size: Using value from /sys/module/spidev/parameters/bufsiz as max buffer size
linux_spi_init: max_kernel_buf_size: 4096
The following protocols are supported: SPI.
Probing for ANIC A251010, 128 kB: compare_id: id1 0xef, id2 0x4018
Probing for ANIC A25L016, 2048 kB: compare_id: id1 0xef, id2 0x4018
Probing for ANIC A25L020, 256 kB: compare_id: id1 0xef, id2 0x4018
Probing for ANIC A25L032, 4096 kB: compare_id: id1 0xef, id2 0x4018
 Probing for ANIC A251040, 512 kB: compare_id: id1 0xef, id2 0x4018
 Probling for AMIC A25L05PT, 64 kB: compare_id: id1 0xef, id2 0x4018
 Probing for ANIC A25L05PU, 64 kB: compare_id: id1 0xef, id2 0x4018
 Probling for ANIC A251080, 1024 kB: compare_id: id1 0xef, id2 0x4018
 Probing for AMIC A25L10PT, 128 kB: compare_id: id1 0xef, id2 0x4018
 Probing for ANIC A25L10PU, 128 kB: compare_id: id1 0xef, id2 0x4018
 Probling for AMIC A25L16PT, 2048 kB: compare_id: id1 0xef, id2 0x4018
 Probing for AMIC A25L16PU, 2048 kB: compare_id: id1 0xef, id2 0x4018
 Probing for ANIC A25L29PT, 256 kB: compare_id: id1 0xef, id2 0x4018
Probing for ANIC A25L29PU, 256 kB: compare_id: id1 0xef, id2 0x4018
  Probling for AMIC A25L49PT, 512 kB: compare_id: id1 0xef, id2 0x4018
  Probling for AMIC A25L40PU, 512 kB: compare_id: id1 0xef, id2 0x4018
  Probing for AMIC A25L512, 64 kB: compare_id: id1 0xef, id2 0x4018
Probing for AMIC A25L80P, 1024 kB: compare_id: id1 0xef, id2 0x401
```

After this it failed to complete, didn't catch that screen. However after detatching and re-attaching clip to the Bios chip, checking with a read and re-writing it declared: "Warning: Chip content is identical to the requested image"

The whole time the raspberry Pi was throwing low voltage warnings despite having large enough power supply. Larger supply was used for the second write which probably helped it finish.

```
-0x194111:S,
                                                     -extetttt:S, exf
                                                                 extbettf:S, extb7
                                                   0-exfc1fff:S, exfc200
                                          -Bxfccfff:S, 8xfcd000-8xfcdfff:S, 8xfce0
ff:S, 8xfd3000-8xfd3fff:S, 8xfd9000-8xfd
xfe3000-8xfe3fff:S, 8xfe4000-8xfe4fff:S,
                                  kta7fff:S, Oxfd
                  M-Bxfe2fff:S, 0xfe3000-0xfe3fff:S, 0xfe4000-0xfe4fff:S
HTT:S. 0xfee000-0xfeefff:S, 0xfef000-0xfeffff:S, 0xff0
                                                                                                   extrettt:S, extr
  KTTSTTT:S, BXTT9000-BXTT9TTT:S, BXTT8000-
 karming: Chip content is identical to the requested image.
Erase/write done.
                                                                                     /snidev . 0, spispeed=1000 -r z420_bios_1
Flashrom umknown on Linux 6.12.25+rpt-rpi-v8 (aarch64)
 flashron is free software, get the source code at https://flashrom.org
Using clock_gettime for delay loops (clk_id: 1, resolution: ins). Found Minbond flash chip "W25Q128.V" (16384 kB, SPI) on limux_spi.
 This flash part has status UNTESTED for operations: WP
The test status of this chip may have been updated in the latest development version of flashrom. If you are running the latest development version, please email a report to flashrom@flashrom.org if any of the above opera-
work correctly for you with this flash chip. Please include the flashrom
  file for all operations you tested (see the man page for details), and mention which mainboard or programmer you tested in the subject line.

Thanks for your help!
       ding flash... done.
                              1:- $ du -b 2420_blos_1_Mod_R1.bin
   oysters@raspberrypi:- 8 du -b z420 bio
16777216 z420 bios 1 Mod R1.bin
                    pberrypi:- 8 sha256sum z420_bios_1_Mod.bin
               oysters@raspberryp1:~ $
```

Checking

```
oysters@raspberrypi: $ sha256sum z420_bios_4.bin
a0bf3f8f38989ca4d36b2542cf9d289c3a7eb1532c95235c54147b9468155302 z420_bios_4.bin
oysters@raspberrypi: $ sha256sum z420_bios_5.bin
a0bf3f8f38989ca4d36b2542cf9d289c3a7eb1532c95235c54147b9468155302 z420_bios_5.bin
oysters@raspberrypi: $ sha256sum z420_bios_6.bin
a0bf3f8f38989ca4d36b2542cf9d289c3a7eb1532c95235c54147b9468155302 z420_bios_6.bin
oysters@raspberrypi: $ sha256sum z420_bios_6.bin
```

After flashing (writing), read again 3 times and checked hash was the same.

Opened those bins with bin used to write in HxD and they were the same.