10. Design and implement 3-bit full adder using logisim simulator.

***TRUTH TABLE:***

***X Y Z SUM CARRY***

0 0 0 0 0

0 0 1 1 0

0 1 0 1 0

0 1 1 0 1

1 0 0 1 0

1 0 1 0 1

1 1 0 0 1

1 1 1 1 1

***OUTPUT:*** 

