Memory hierarchy pyramid:

- **L0:** Register
- **L1:** On-chip cache (SRAM)
- **L2:** On-chip cache (SRAM)
- **L3:** Main Memory (DRAM)
- **>L3:** Local secondary storage (disks, SSD) and Remote secondary storage (tape, cloud, …)

Maybe more than 2 levels

Larger, slower, cheaper per byte