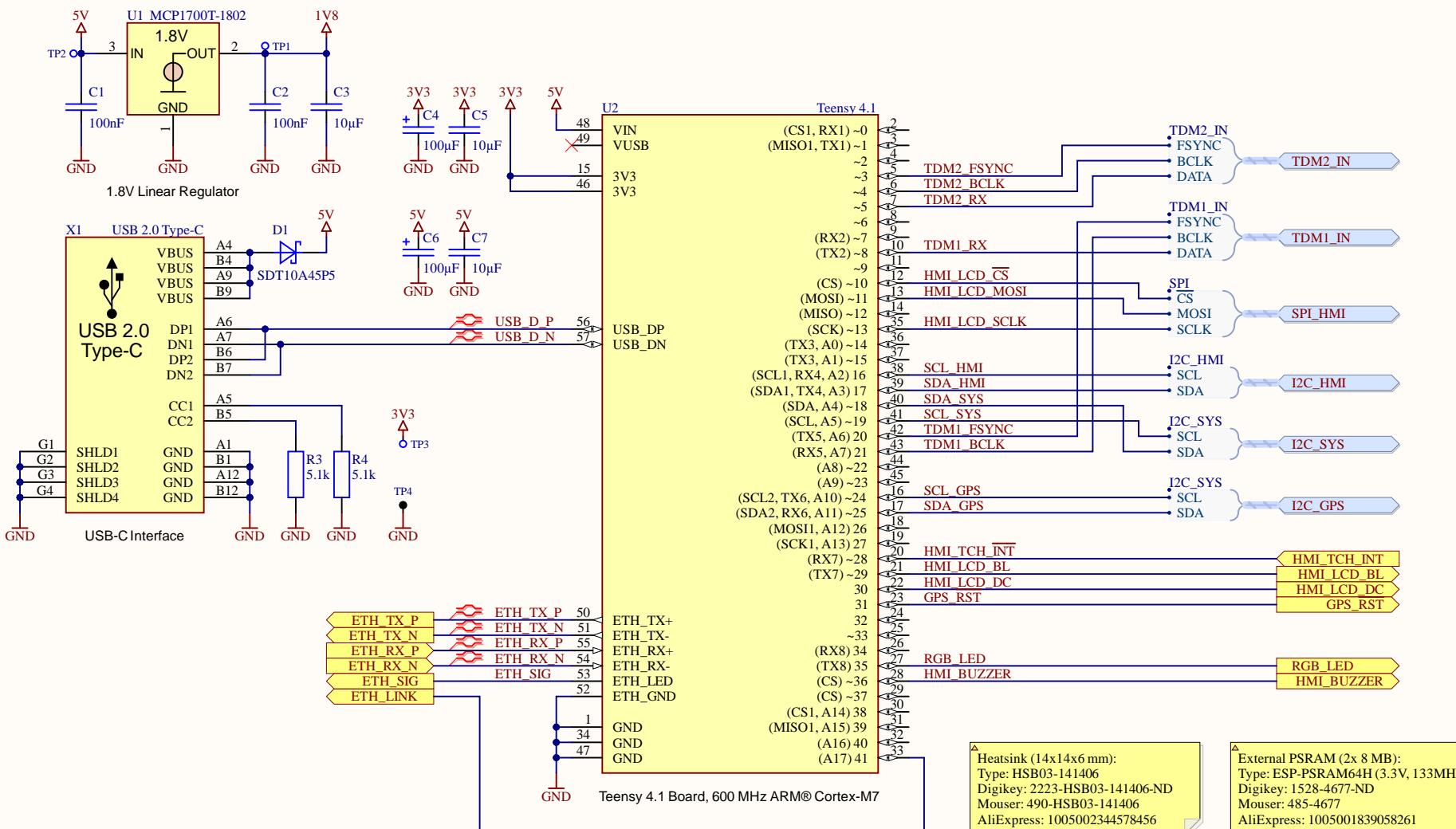


Solderable Standoff Nuts (M3 4mm)

Title Mainboard	 OST Österreichische Fachhochschule	 ICAI
Subtitle Overview		
Author: <i>Florian Baumgartner & Alain Keller</i>	Date: 08.01.2024	Rev: V1.0
File: Mainboard_V1.0.SchDoc		Sheet 1 of 7



Pogo-Pins for USB-Connection (2x):
Type: P19-2221 (\varnothing 1.5mm, 17.04mm)
Digikey: 952-2604-ND
Mouser: 855-P19-2221
Farnell: 9960171

Male Connector for Ethernet-Connection
Type: 0877580616 (6-Pin, 2.0mm)
Digikey: WM18834-ND
Mouser: 538-87758-0616
Farnell: 2751441

▲ Female Connector for Ethernet-Connection:
Type: ESQT-103-02-L-D-410 (6-Pin, 2.0mm)
Digikey: ESQT-103-02-L-D-410-ND
Mouser: 200-ESQTL10302LD410
Farnell: 3733870

Title **Mainboard**

Subtitle **MCU**

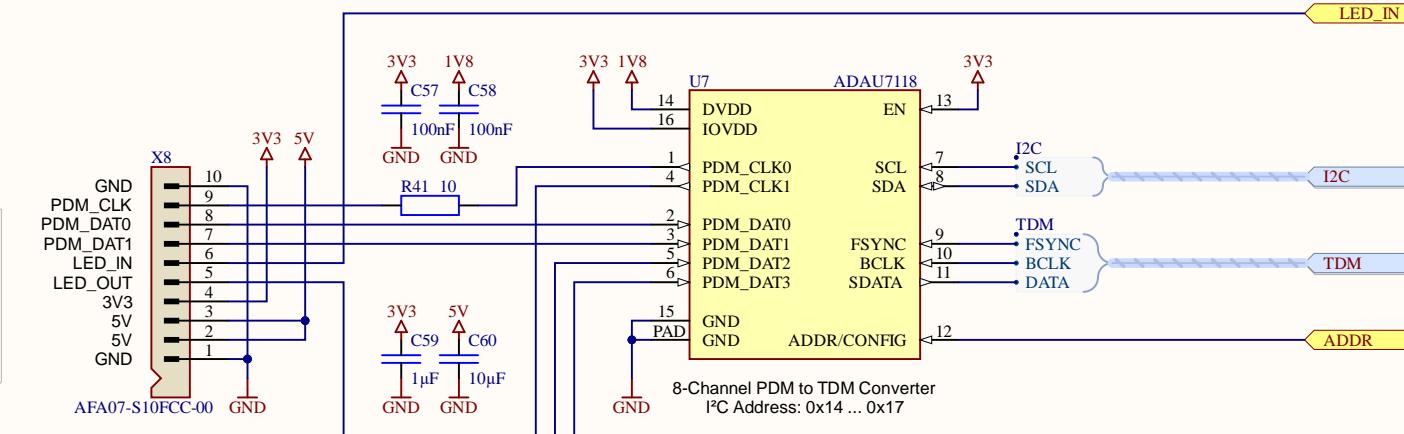
Author: *Florian Baumgartner & Alain Keller*

File: Mainboard V1.0 MCU.SchDoc

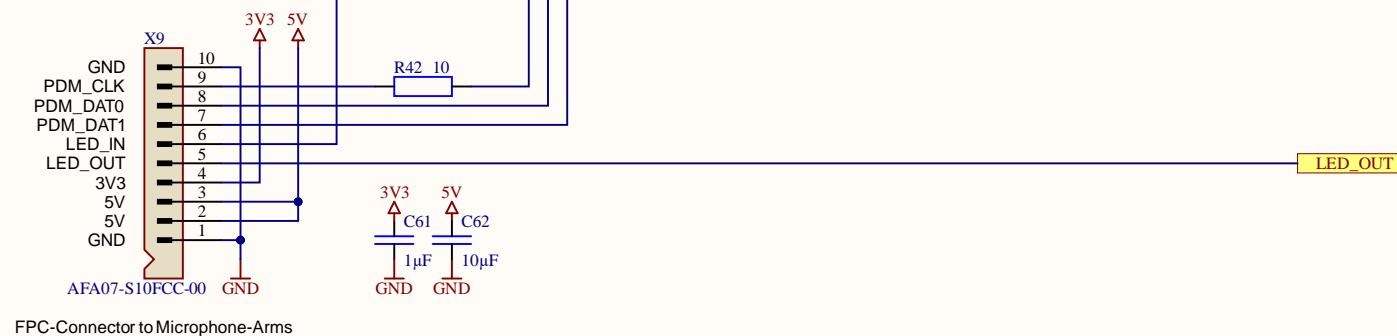


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FPC/FFC-Cable
Type: MOLEX 15167-0247
Pins: 10
Pin-Pitch: 1.0mm
Length: 51.0mm
Width: 11.0mm
Contacts: Same Side Contacts
Stiffner: 0.3mm



FPC-Connector to Microphone-Arms



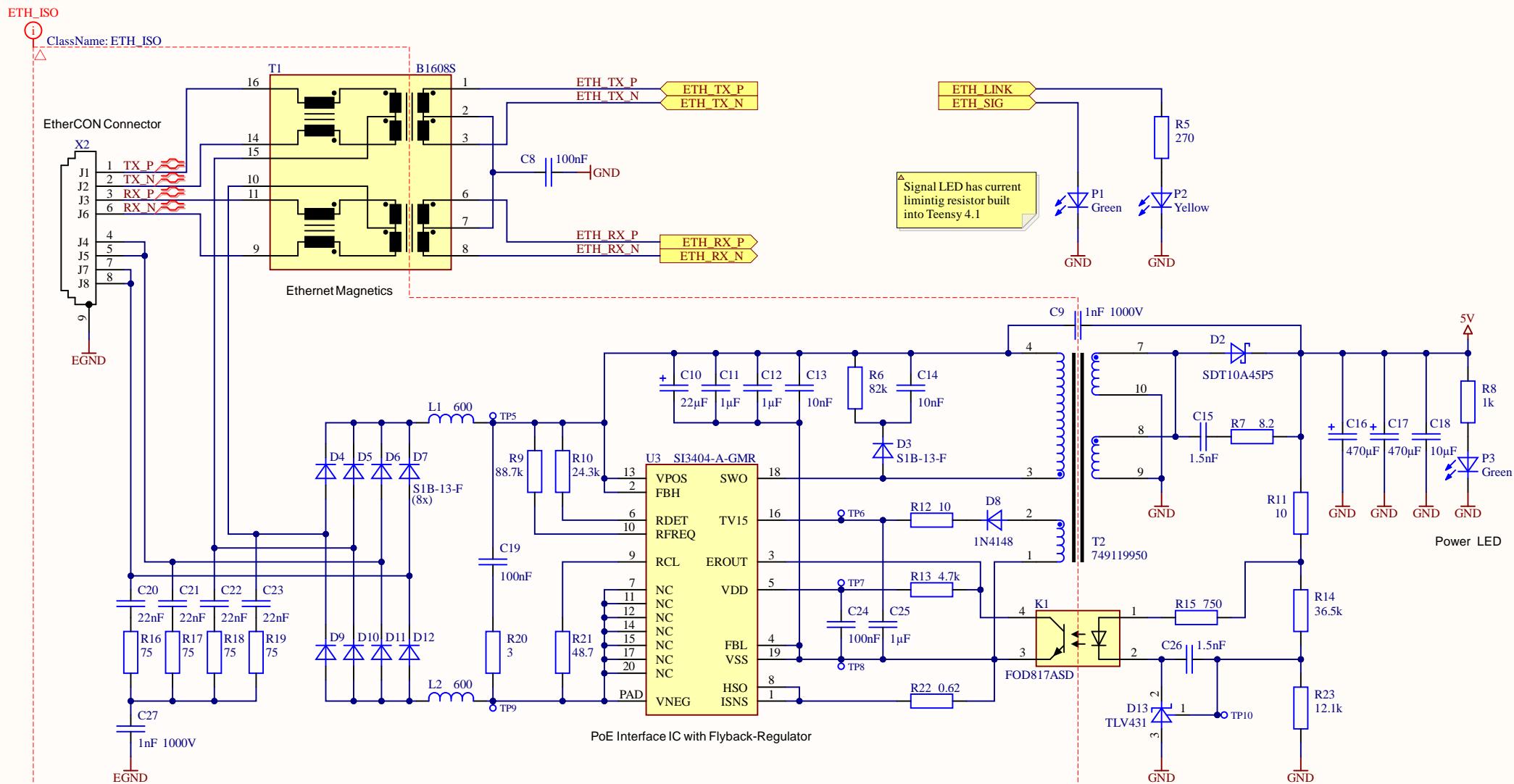
Title

MainboardSubtitle **PDM-Microphone Input**Author: *Florian Baumgartner & Alain Keller*File: *Mainboard_V1.0_Input.SchDoc*

Date: 08.01.2024

Rev: V1.0

Sheet 3 of 7



Title Mainboard		OST Ostschweizer Technikum	ICAI
Subtitle Ethernet PoE (IEEE 802.3at)			
Author: Florian Baumgartner & Alain Keller	Date: 08.01.2024	Rev: V1.0	
File: Mainboard_V1.0_Ethernet.SchDoc			Sheet 4 of 7

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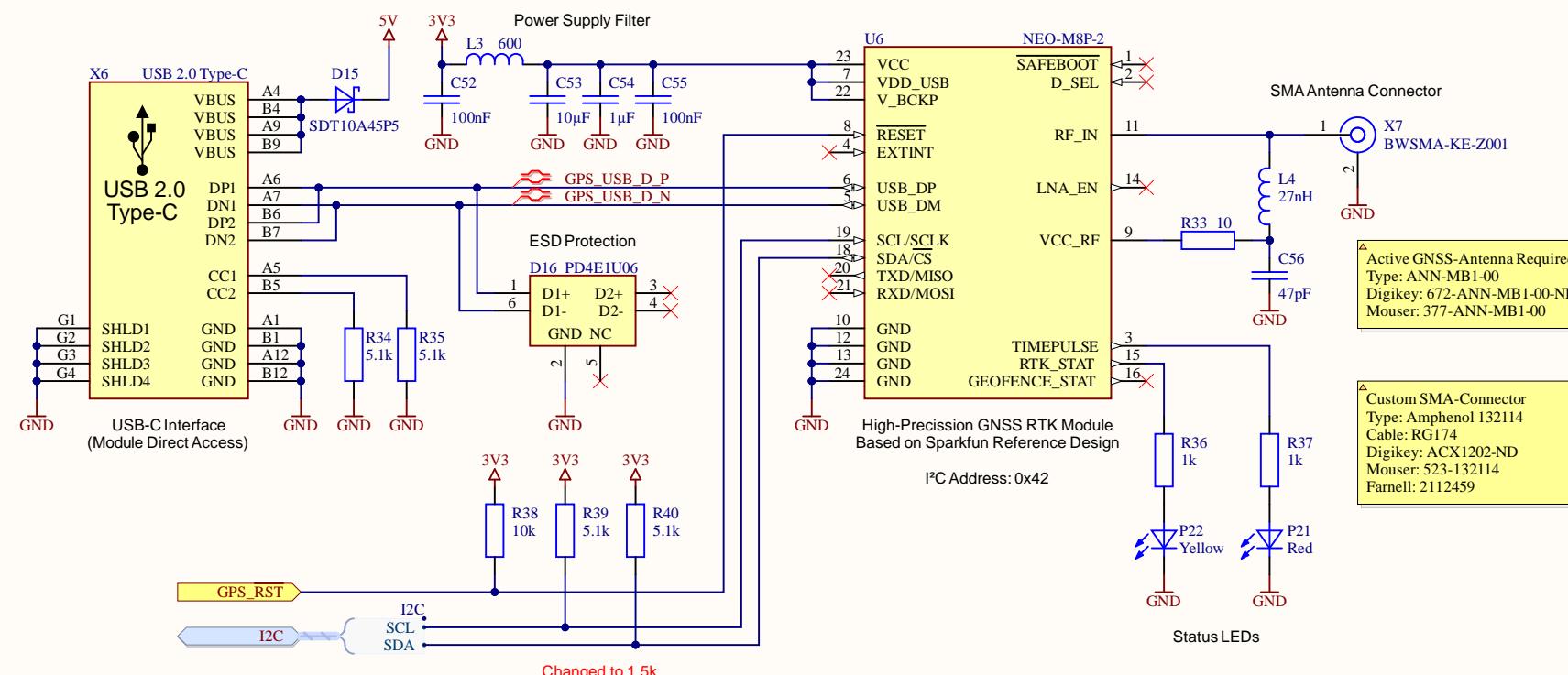
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Title

Mainboard

Subtitle GPS

Author: Florian Baumgartner & Alain Keller

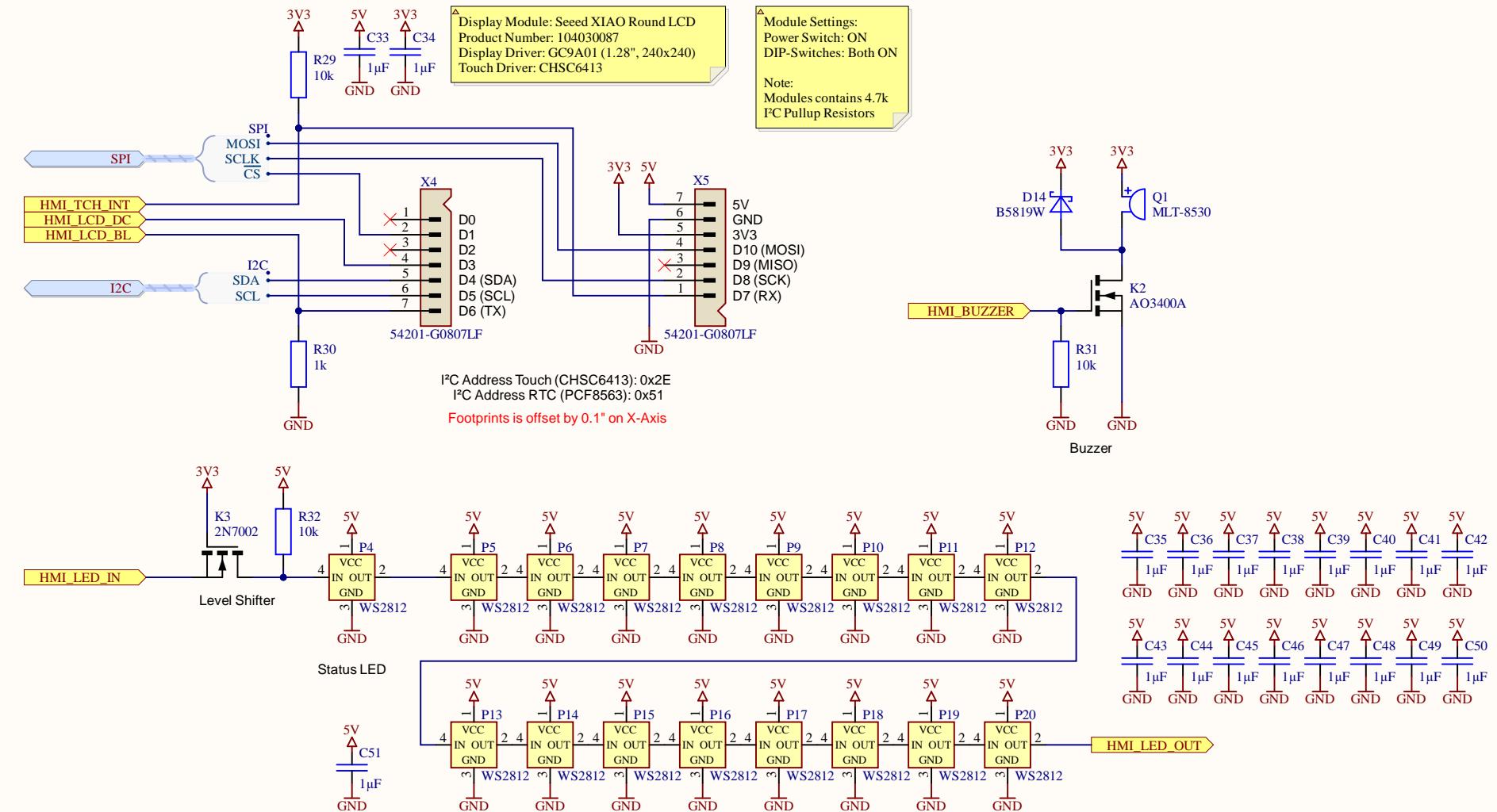
File: Mainboard_V1.0_GPS.SchDoc



Date: 08.01.2024

Rev: V1.0

Sheet 5 of 7



Mainboard

Subtitle HMI

Author: Florian Baumgartner & Alain Keller

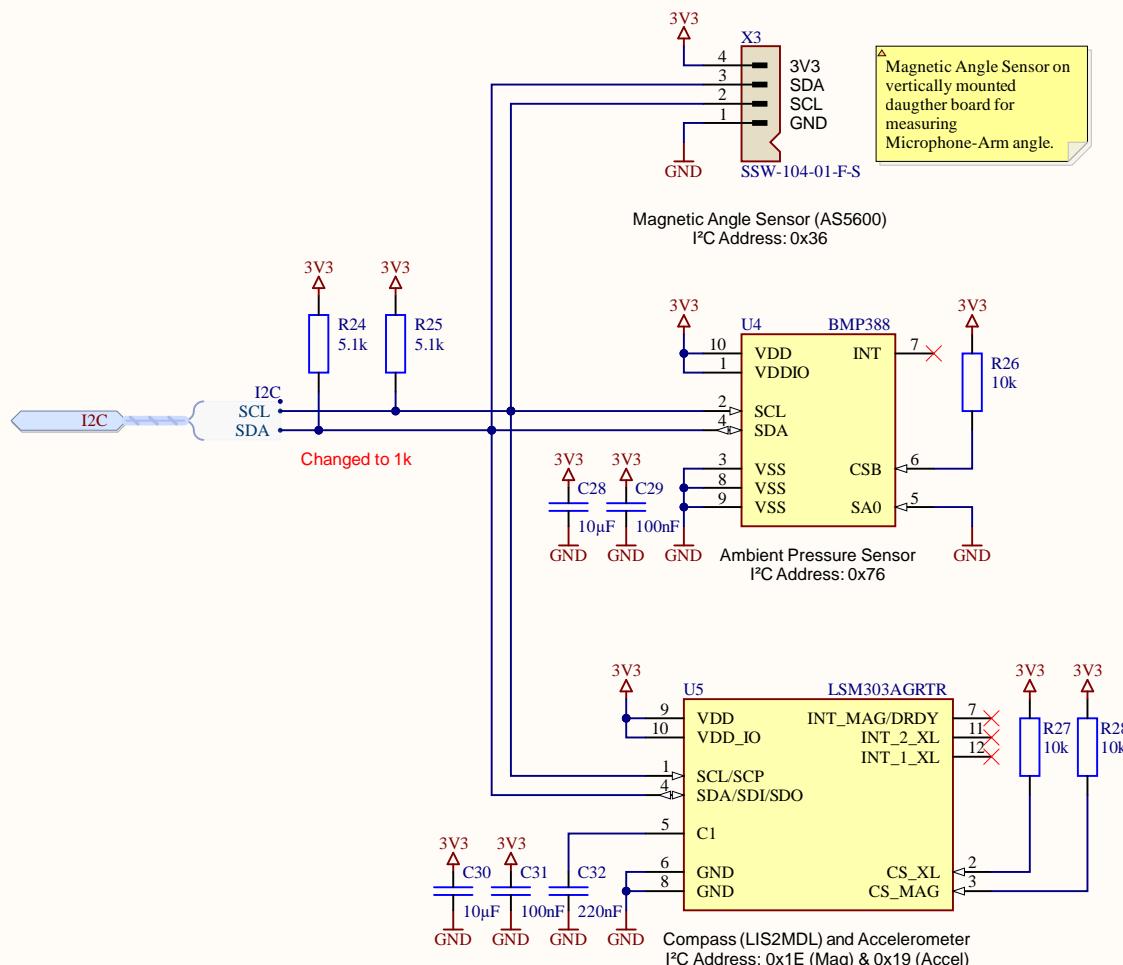
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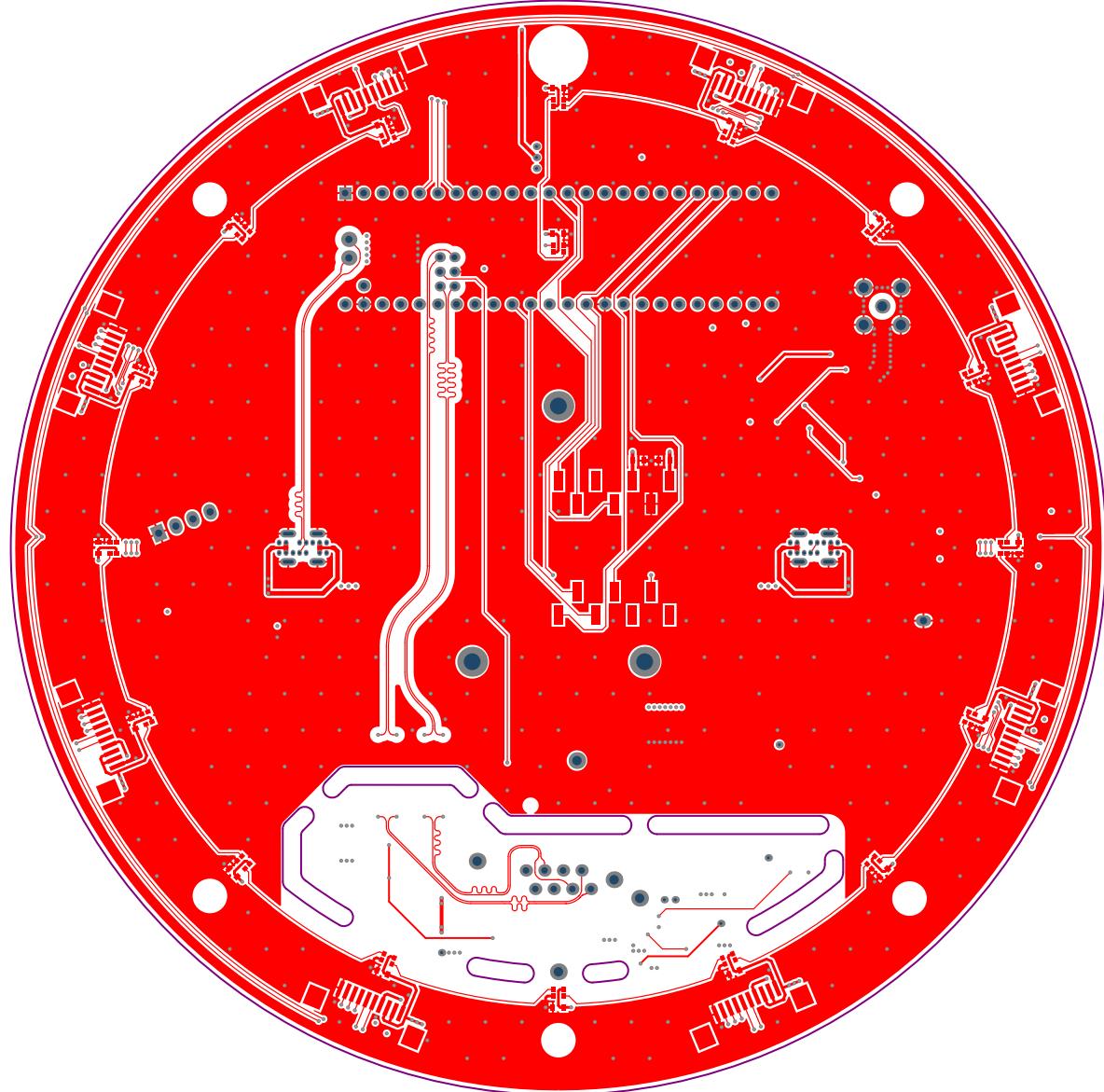


Date: 08.01.2024

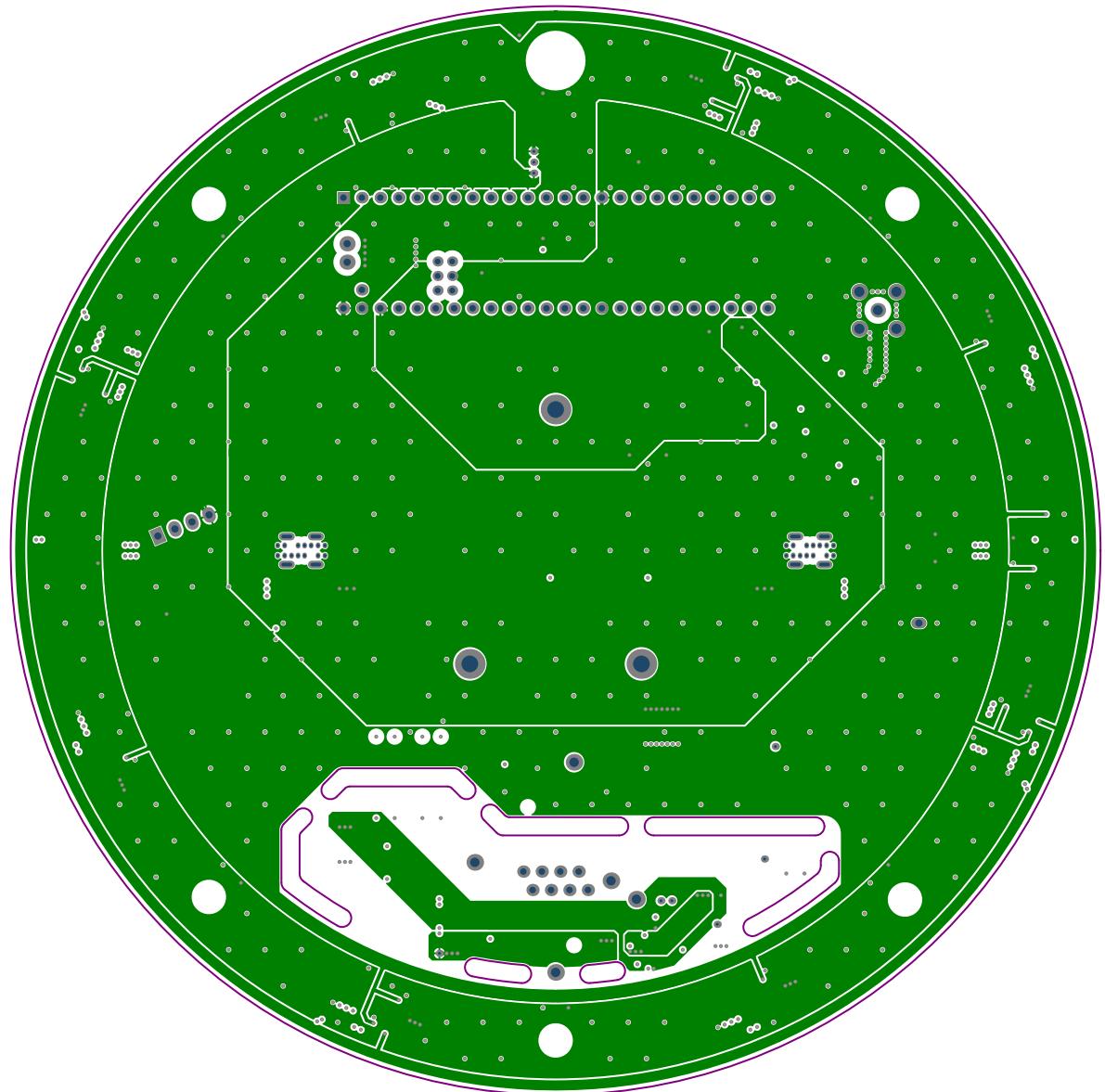
Rev: V1.0

Sheet 6 of 7





ENGINEER: Florian Baumgartner	TITLE: Mainboard		
PCB DESIGNER: Florian Baumgartner			
DATE: 02.12.2023	PART NO.:	1/1	REV: V1.1
FILE NAME: Mainboard_V1.0.PcbDoc	DWG NO.:	1/1	SCALE: 1:1



ENGINEER:
Florian Baumgartner

PCB DESIGNER:
Florian Baumgartner

DATE:
02.12.2023

FILE NAME:
Mainboard_V1.0.PcbDoc

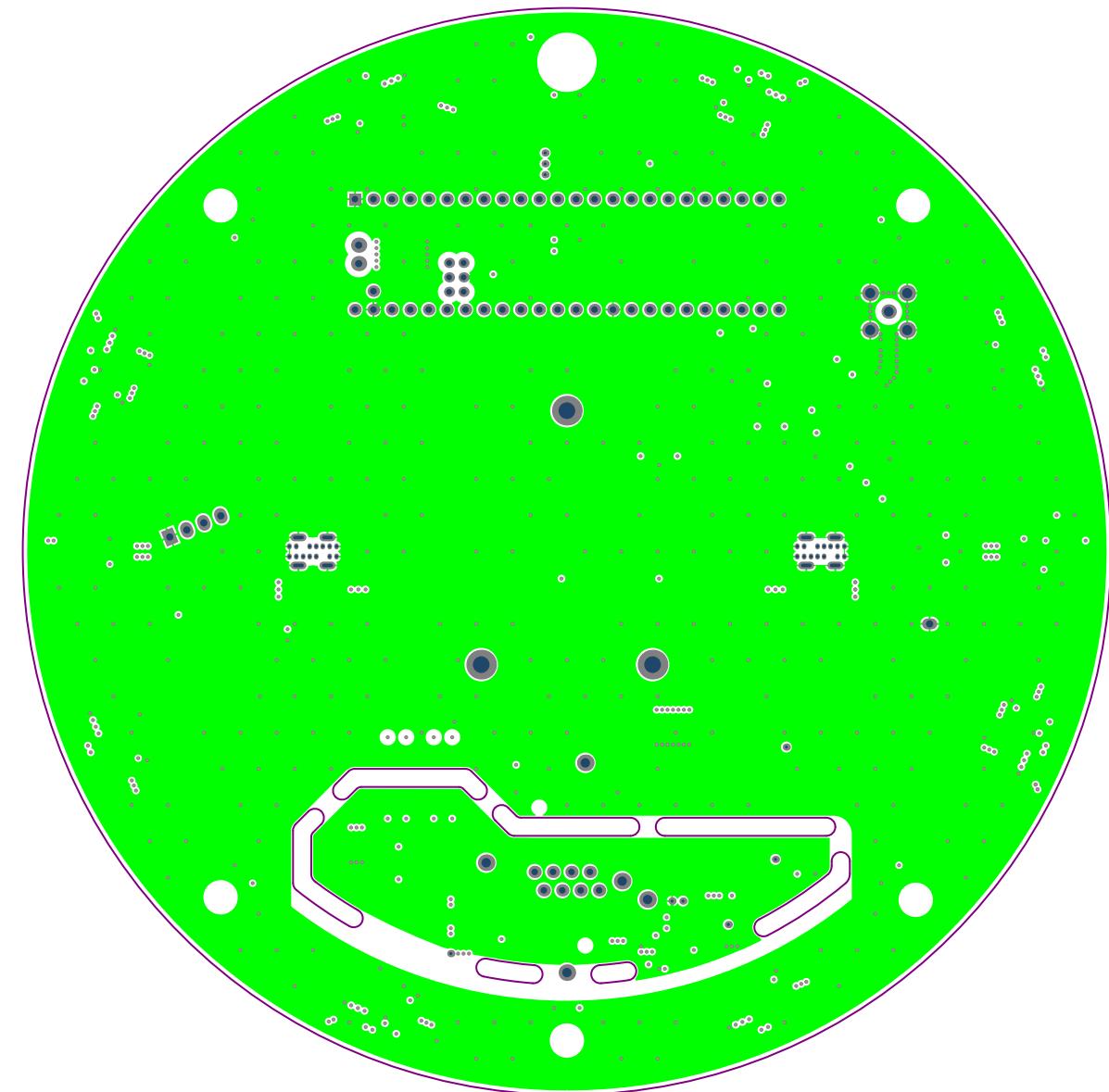
TITLE:
Mainboard

PART NO.:
1/1

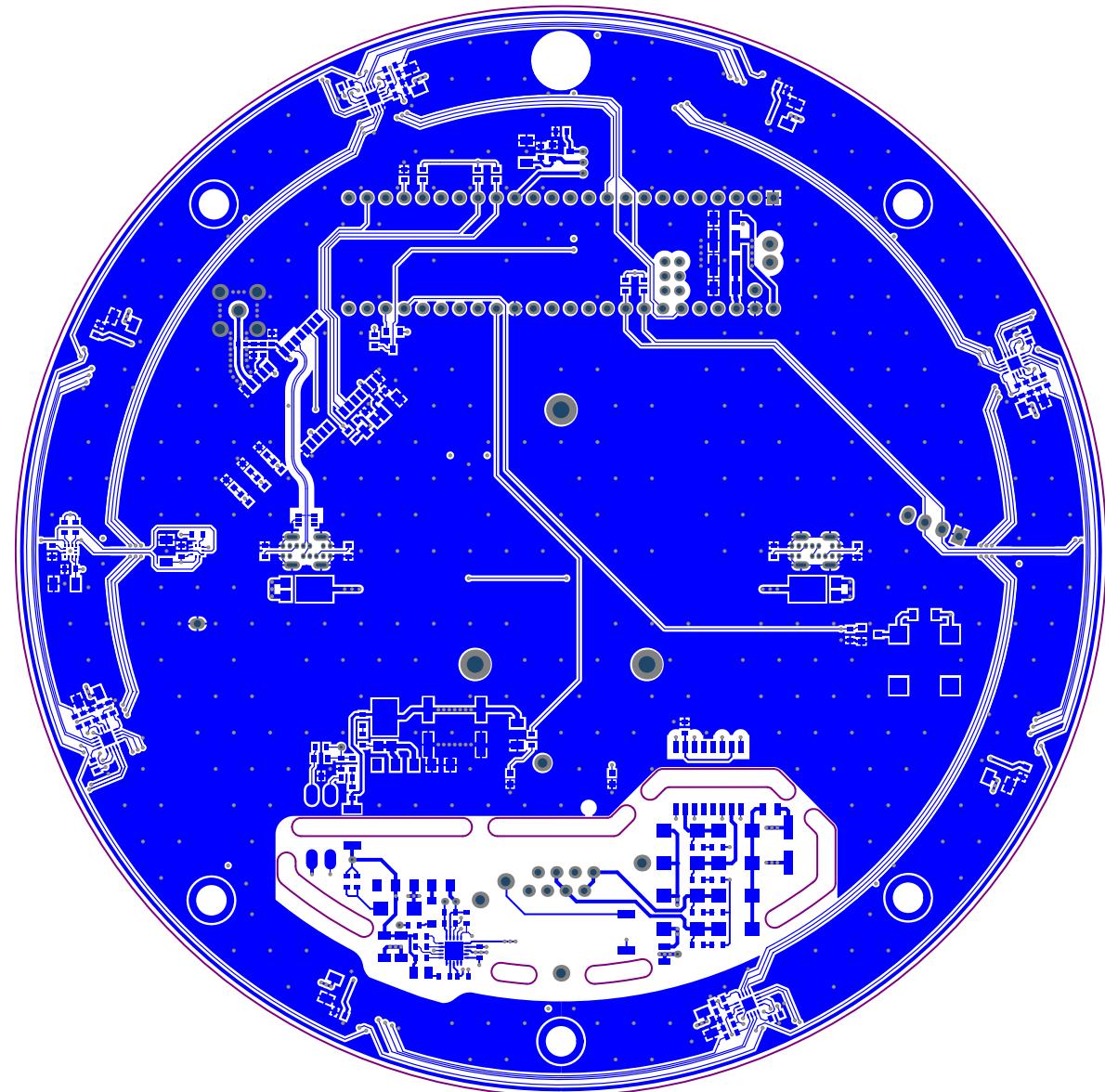
DWG NO.:
1/1

REV:
V1.1

SCALE:
1:1



ENGINEER: Florian Baumgartner	TITLE: Mainboard		
PCB DESIGNER: Florian Baumgartner			
DATE: 02.12.2023	PART NO.:	1/1	REV: V1.1
FILE NAME: Mainboard_V1.0.PcbDoc	DWG NO.:	1/1	SCALE: 1:1



ENGINEER: Florian Baumgartner	TITLE: Mainboard		
PCB DESIGNER: Florian Baumgartner			
DATE: 02.12.2023	PART NO.:	1/1	REV.:
FILE NAME: Mainboard_V1.0.PcbDoc	DWG NO.:	1/1	SCALE: 1:1

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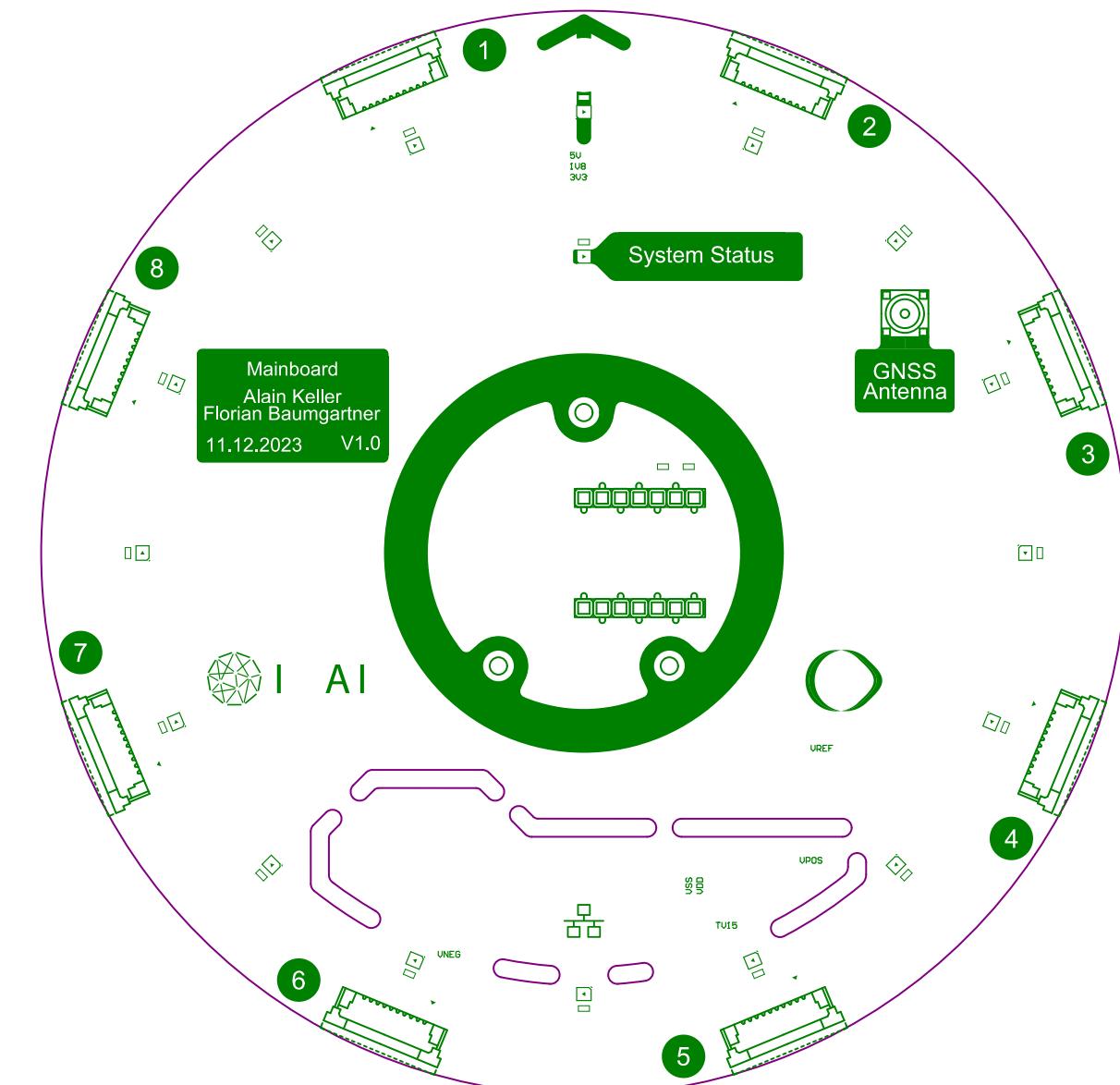
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ENGINEER: Florian Baumgartner	TITLE: Mainboard
PCB DESIGNER: Florian Baumgartner	
DATE: 02.12.2023	PART NO.: 1/1
FILE NAME: Mainboard_V1.0.PcbDoc	REV: V1.1
	DWG NO: 1/1
	SCALE: 1:1

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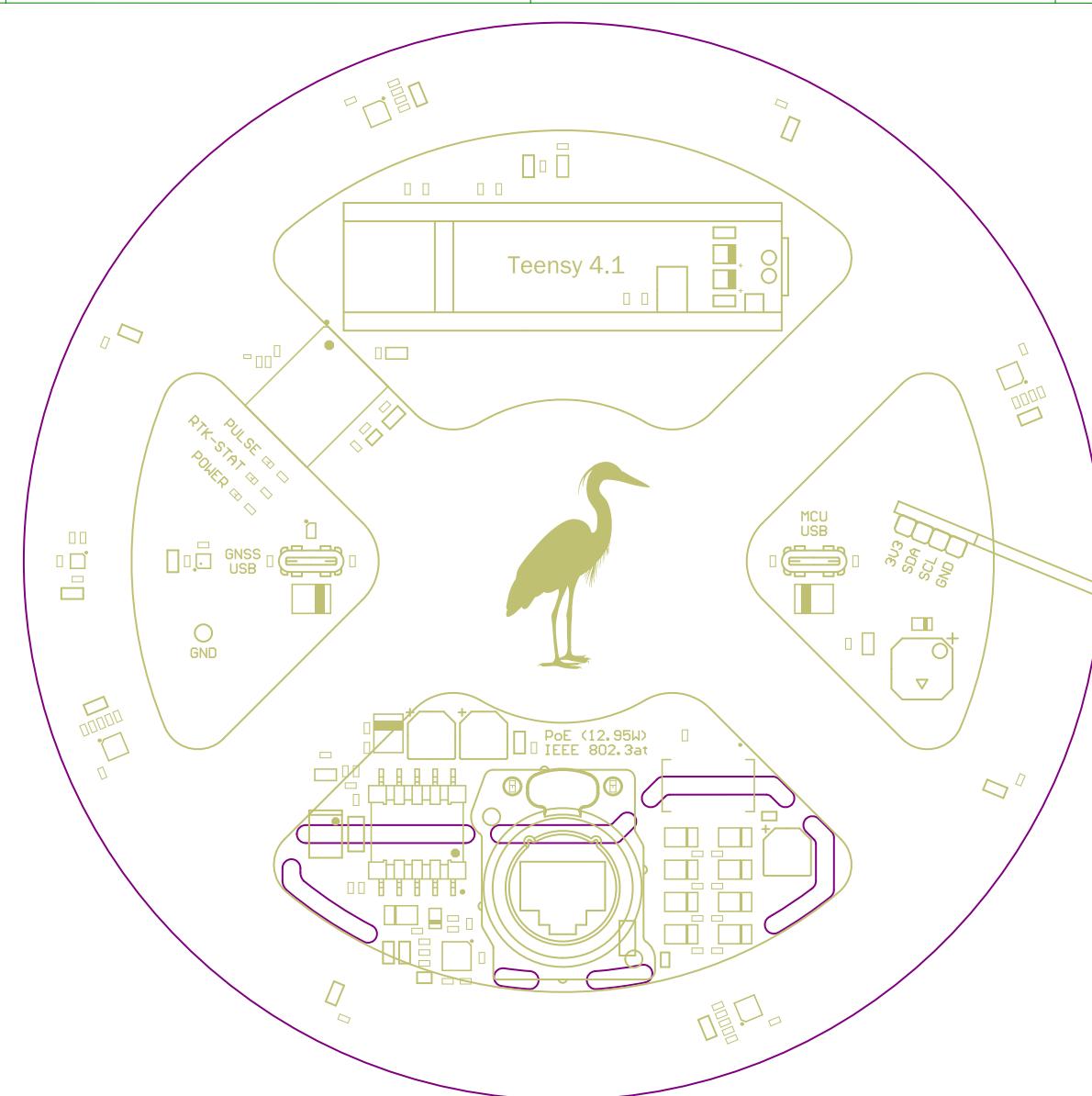
D

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ENGINEER:
Florian Baumgartner
PCB DESIGNER:
Florian Baumgartner

DATE:
02.12.2023

FILE NAME:
Mainboard_V1.0.PcbDoc

TITLE:
Mainboard

PART NO.:
1/1

DWG NO.:
1/1

REV:
V1.1

SCALE:
1:1

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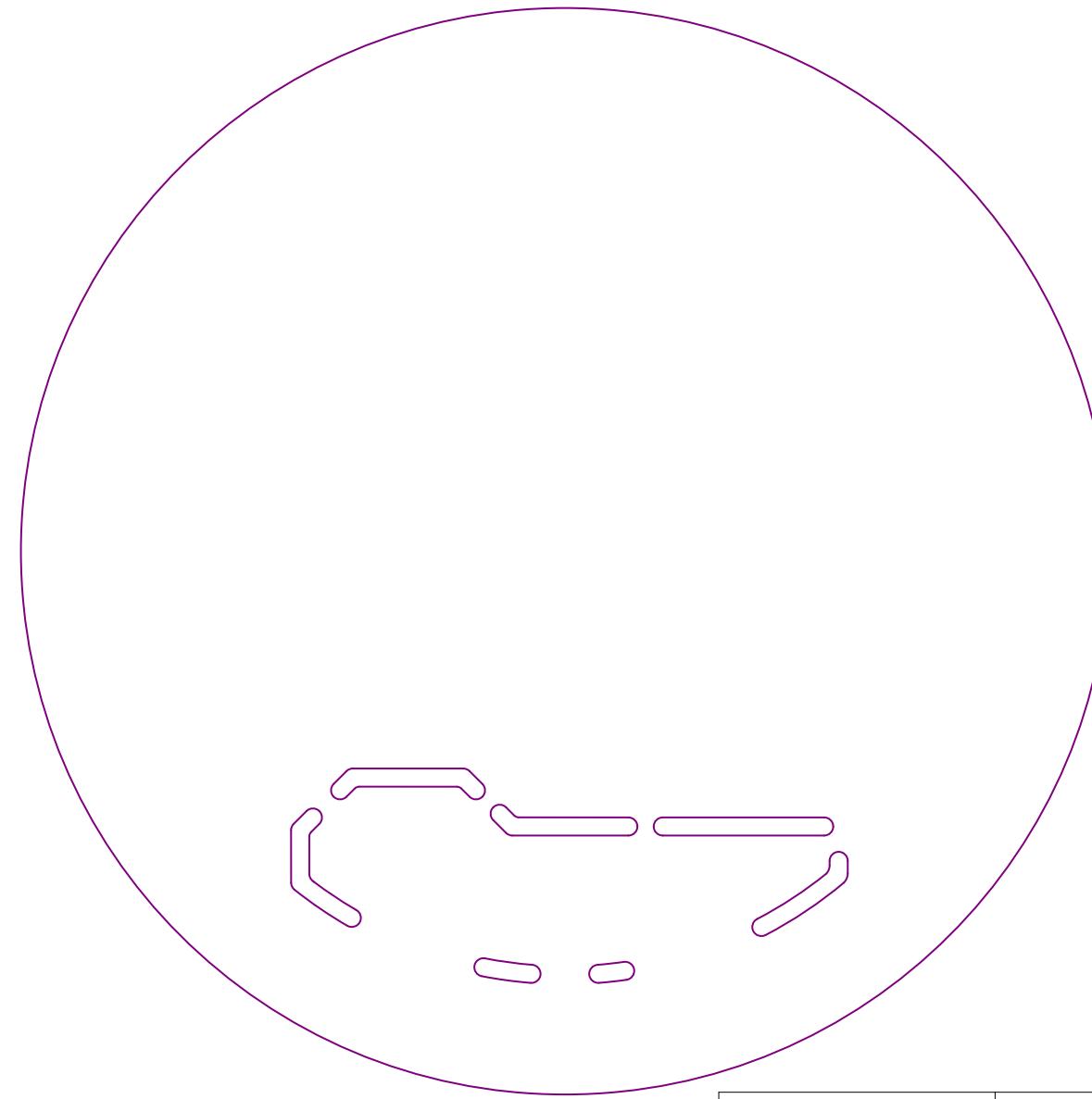
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ENGINEER:
Florian Baumgartner

PCB DESIGNER:
Florian Baumgartner

DATE:
02.12.2023

FILE NAME:
Mainboard_V1.0.PcbDoc

TITLE:
Mainboard

PART NO.:
1/1

DWG NO.:
1/1

REV:
V1.1

SCALE:
1:1

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