

PARMA-DITAM Workshop CALL FOR PAPERS

PARMA-DITAM 2026

PARMA 2026: 17th Workshop on Parallel Programming and Run-Time Management Techniques for Many-core Architectures

DITAM 2026: 15th Workshop on Design Tools and Architectures for Multi-Core Embedded Computing Platforms

Recent advances in AI have increased pressure on classical computing architectures, prompting researchers and engineers to explore more efficient ways to perform computations. From HPC systems to mobile and embedded platforms, there is a growing need to design both hardware and software with attention to energy efficiency, privacy, security, data locality, and raw performance.

The upcoming edition of the PARMA-DITAM workshop will focus on technologies and methodologies that address evolving computational demands while taking into account environmental, societal, and political constraints.

TOPIC AREAS

This year's workshop will have six main topics:

- T1: Specialized digital and analog accelerators, including in-memory and near-memory processing
- T2: Energy optimization in hardware and software, including approximate computing techniques
- T3: Heterogeneous, parallel, distributed, and edge computing systems
- T4: Compilers, tools, and frameworks supporting the use, development, and performance analysis of novel computing architectures
- T5: Open-source and sovereign hardware design, including RISC-V
- T6: Case studies, applications, and real-world demonstrations related to T1–T5

ORGANIZATION

General Chair: Lev Denisov, Politecnico di Milano, lev.denisov@polimi.it

Program Co-Chair: Nitin Shukla, CINECA, n.shukla@cineca.it

Program Co-Chair: Paola Busia, University of Cagliari, paola.busia@unica.it

Publicity Chair: Maria Pasaylo, University of Florida, mpasaylo@ufl.edu

Publication Chair: Davide Baroffio, Politecnico di Milano, davide.baroffio@polimi.it

Submission and Web Chair: Serena Curzel, Politecnico di Milano, serena.curzel@polimi.it

Steering Committee:

- João Bispo, Universidade do Porto/INESCTEC, jbispo@fe.up.pt
- Giovanni Agosta, Politecnico di Milano, agosta@acm.org
- João M.P. Cardoso, Universidade do Porto, jmpc@acm.org
- Stefano Cherubin, NTNU, stefano.cherubin@ntnu.no
- William Fornaciari, Politecnico di Milano, william.fornaciari@polimi.it
- Michael Hübner, Brandenburg University of Technology Cottbus-Senftenberg, huebner@b-tu.de
- Cristina Silvano, Politecnico di Milano, cristina.silvano@polimi.it
- Dimitrios Soudris, National Technical University of Athens, dsoudris@microlab.ntua.gr

PAPER SUBMISSION INSTRUCTIONS

Accepted papers will be published online in the Dagstuhl OASIcs OpenAccess Series in Informatics. Papers should be submitted electronically through the EasyChair website:

<https://easychair.org/conferences/?conf=parmaditam2026>

The review process will be **double-blind**. Electronic paper submission requires a full paper, up to **10 pages single column**, excluding title page and references. Papers should be submitted in PDF format. Please use the following template when preparing your manuscript: <https://submission.dagstuhl.de/documentation/authors#oasics>

IMPORTANT DATES

Paper submission deadline:

28 November 2025 23:59 UTC

Notification of acceptance:

19 December 2025

Camera ready version:

08 January 2026

Workshop:

27 January 2026 (co-located with HiPEAC)

Website: parma-ditam-workshop.github.io

LinkedIn: linkedin.com/company/parma-ditam-workshop/