

## PARMA-DITAM'2020

### PARMA 2020: 11<sup>th</sup> Workshop on Parallel Programming and Run-Time Management Techniques for Many-core Architectures

### DITAM 2020: 9<sup>th</sup> Workshop on Design Tools and Architectures for Multi-Core Embedded Computing Platforms

The current trend towards many-core and the emerging accelerator-based architecture requires a global rethinking of software and hardware design. The PARMA-DITAM workshop focuses on the advanced many core architectures, accelerator based many-core system-on-chips, programming models, design space exploration and tools and run-time management techniques to exploit the features of such (heterogeneous) many-core processor architectures from embedded to high-performance computing platforms.

#### TOPIC AREAS

The workshop will have six main topics:

- **T1:** Programming models and languages, compilers and virtualization techniques
- **T2:** Runtime adaptivity, runtime management, power management and memory management
- **T3:** Heterogeneous and reconfigurable many-core architectures, accelerator-based architectures, and design space exploration
- **T4:** Design tools and methodologies for many-core architectures
- **T5:** Parallel applications for many-core platforms
- **T6:** Architectures and compiler techniques to accelerate deep neural networks, neuromorphic accelerators

#### ORGANIZATION

**Program Chair:** Muhammad Shafique, TU Wien, [muhammad.shafique@tuwien.ac.at](mailto:muhammad.shafique@tuwien.ac.at)

**Publicity Chair:** Amir H. Ashouri, University of Toronto, [aashouri@ece.utoronto.ca](mailto:aashouri@ece.utoronto.ca)

**Publication / Submission Chair:** João Bispo, Universidade do Porto, [jbispo@fe.up.pt](mailto:jbispo@fe.up.pt)

**Web Chair:** Stefano Cherubin, Politecnico di Milano, [stefano.cherubin@polimi.it](mailto:stefano.cherubin@polimi.it)

#### Steering Committee

Giovanni Agosta, Politecnico di Milano, [agosta@acm.org](mailto:agosta@acm.org)

João M.P. Cardoso, Universidade do Porto, [jmpc@acm.org](mailto:jmpc@acm.org)

William Fornaciari, Politecnico di Milano, [william.fornaciari@polimi.it](mailto:william.fornaciari@polimi.it)

Michael Huebner, Ruhr-Universitaet Bochum, [Michael.Huebner@ruhr-uni-bochum.de](mailto:Michael.Huebner@ruhr-uni-bochum.de)

Cristina Silvano, Politecnico di Milano, [cristina.silvano@polimi.it](mailto:cristina.silvano@polimi.it)

Dimitrios Soudris, National Technical University of Athens, [dsoudris@microlab.ntua.gr](mailto:dsoudris@microlab.ntua.gr)

#### PAPER SUBMISSION INSTRUCTIONS

Proceedings of the workshop will be published in the ACM International Conferences Proceedings Series (under approval). Papers should be submitted electronically through the workshop website. The review process will be double-blind.

Electronic paper submission requires a full paper, up to 6 double column ACM format pages, including figures and references. Papers should be submitted in PDF format. Please use the following template when preparing your manuscript: <http://www.acm.org/sigs/publications/proceedings-templates>

#### POSTER SUBMISSION INSTRUCTIONS

We invite posters for each of the six workshop topics. Poster submissions should include a 150-200 word abstract and the one-page poster itself in an A4 size format. A workshop digest based upon the one-page abstract and one-page poster will be distributed to all participants of the workshop. Note that the accepted posters will NOT be included in the proceedings. Posters will be published online at the Workshop web site.

#### IMPORTANT DATES

Paper/Poster submission deadline:	November 15, 2019	
Notification of acceptance:	December 15, 2019	
Camera ready version:	December 21, 2019	
Workshop:	January 2020	(co-located with HiPEAC Conference)