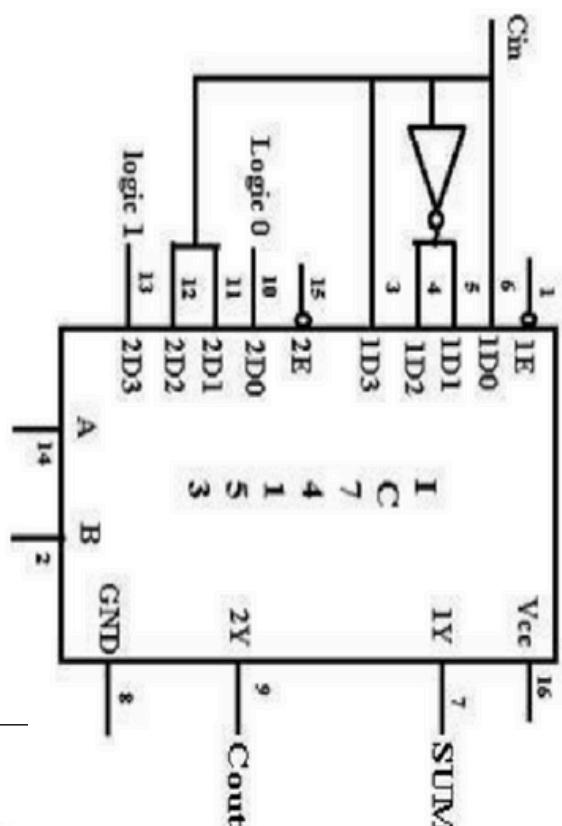
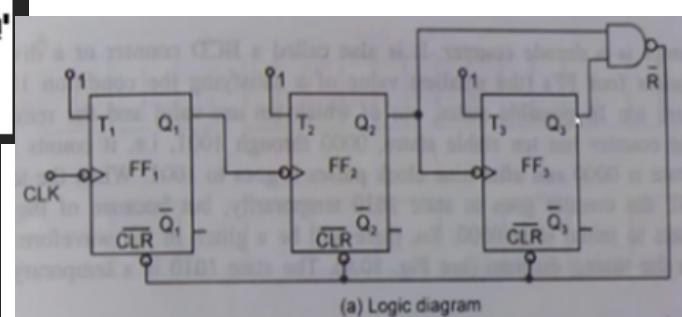
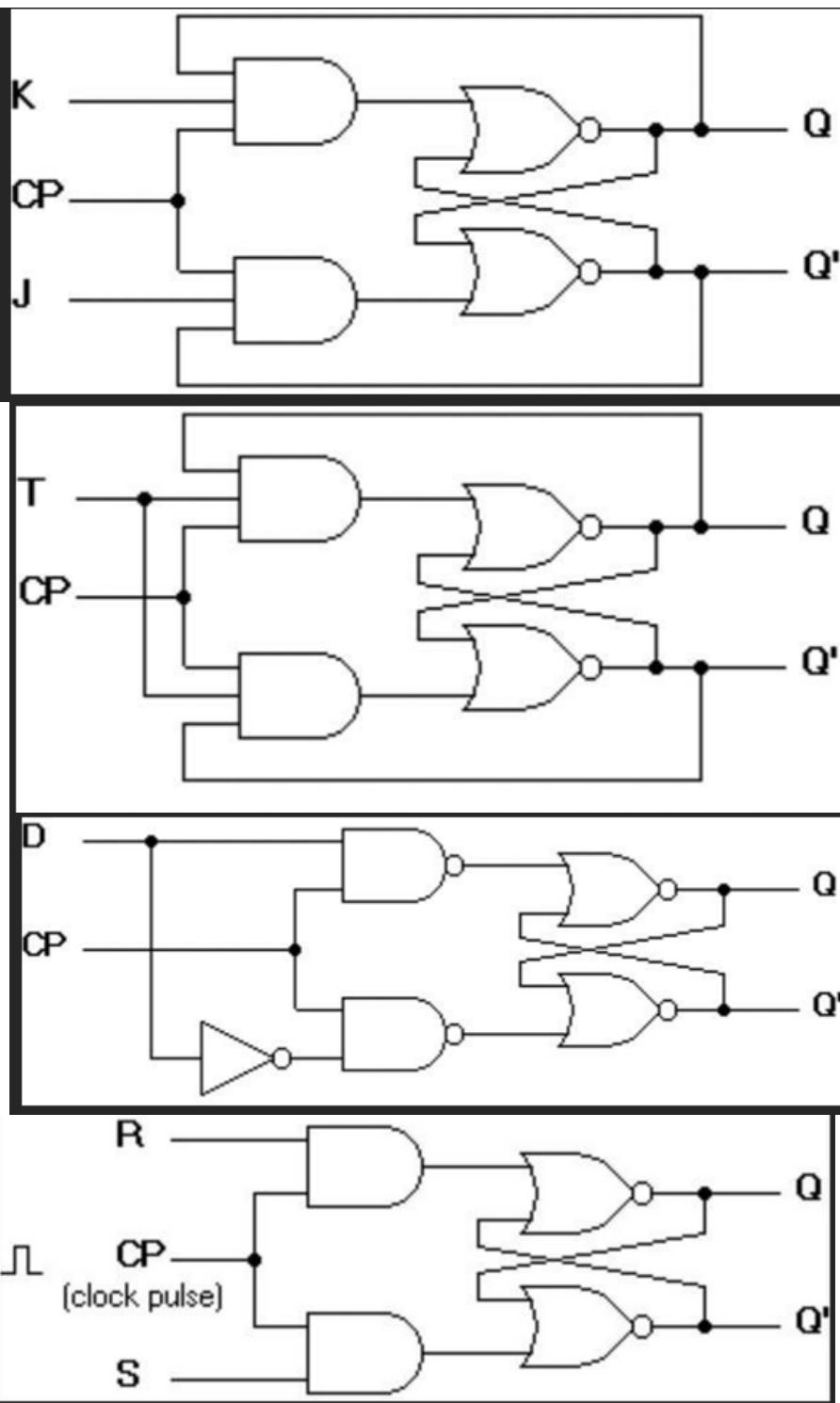
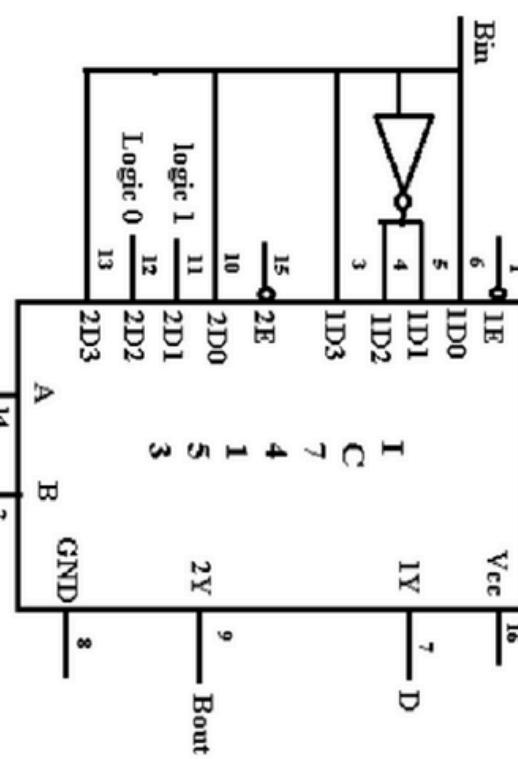


## BLOCK DIAGRAM:

Full Adder using 74153



Full Subtractor using 74153



(a) Logic diagram