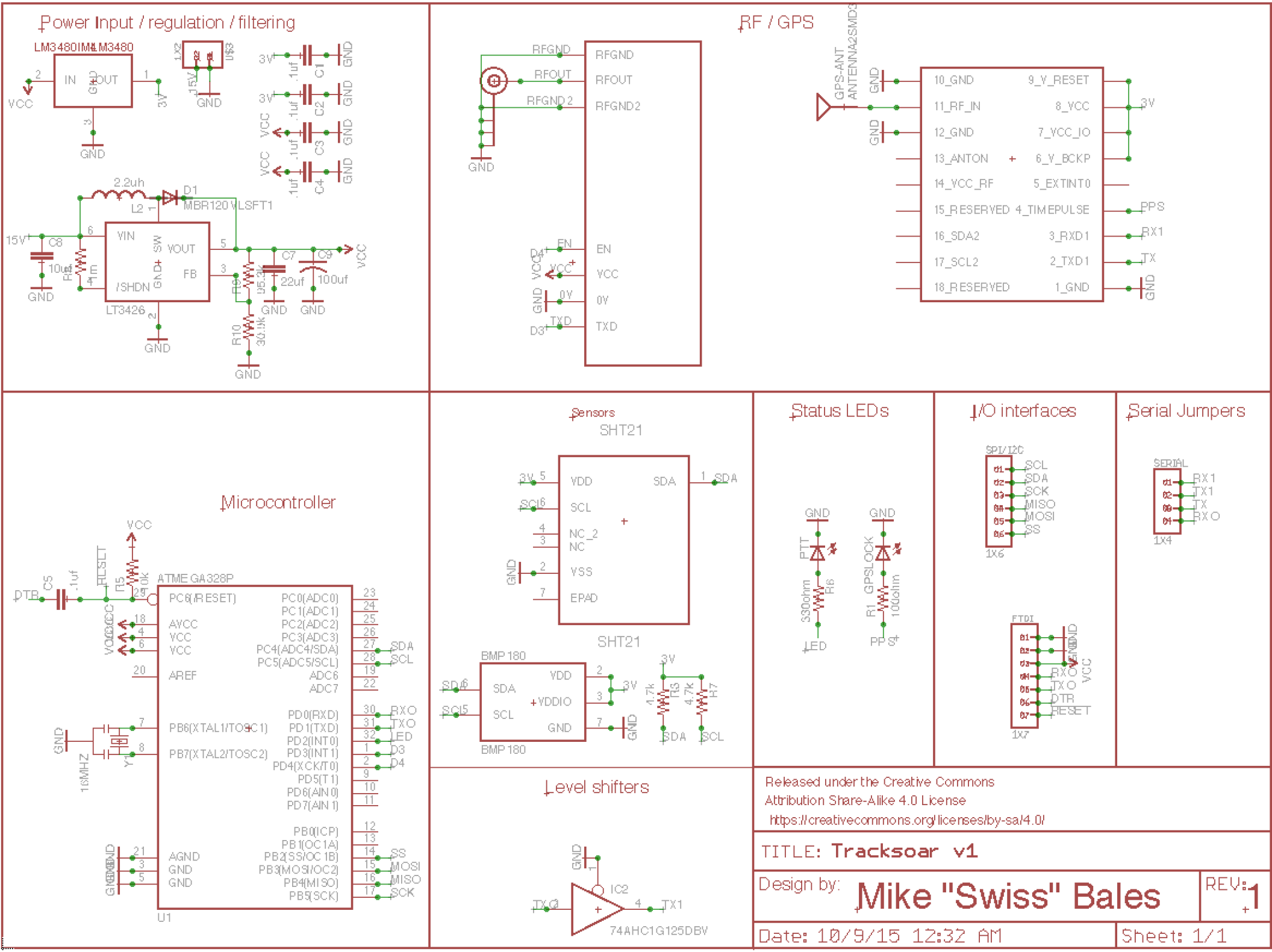
Power Input / regulation / filtering
LM3480IM3
LM3480
IN
OUT
GND
VCC
3V
15V
U3
C1
C2
C3
C4
.1uf
2.2uh
L2
D1
MBR120VLSFT1
VIN
VOUT
SW
FB
/SHDN
LT3426
C8
10uf
R8
4m
R9
95.3k
R10
30.1k
C7
22uf
C9
100uf
RF / GPS
RFGND
RFOUT
RFGND2
EN
VCC
0V
TXD
D4
D3
GPS-ANT
ANTENNA2SMD3
10_GND
11_RF_IN
12_GND
13_ANTON
14_VCC_RF
15_RESERVED
16_SDA2
17_SCL2
18_RESERVED
9_V_RESET
8_VCC
7_VCC_IO
6_V_BCKP
5_EXTINT0
4_TIMEPULSE
3_RXD1
2_TXD1
1_GND
PPS
RX1
TX
Microcontroller
ATMEGA328P
PTB
C5
.1uf
RLSLT
R5
10k
PC6(/RESET)
AVCC
VCC
AREF
PB6(XTAL1/TOSC1)
PB7(XTAL2/TOSC2)
16MHZ
Y1
AGND
GND
PC0(ADC0)
PC1(ADC1)
PC2(ADC2)
PC3(ADC3)
PC4(ADC4/SDA)
PC5(ADC5/SCL)
ADC6
ADC7
PD0(RXD)
PD1(TXD)
PD2(INT0)
PD3(INT1)
PD4(XCK/T0)
PD5(T1)
PD6(AIN0)
PD7(AIN1)
PB0(ICP)
PB1(OC1A)
PB2(SS/OC1B)
PB3(MOSI/OC2)
PB4(MISO)
PB5(SCK)
SDA
SCL
RXO
TXO
LED
D3
D4
SS
MOSI
MISO
SCK
U1
Sensors
SHT21
VDD
SCL
NC_2
NC
VSS
EPAD
BMP180
VDDIO
Status LEDs
PTT
330ohm
R6
LED
GPSLOCK
R1
100ohm
PPS
I/O interfaces
SPI/I2C
1X6
FTDI
1X7
DTR
RESET
Serial Jumpers
SERIAL
RX1
TX1
TX
RXO
1X4
Level shifters
IC2
74AHC1G125DBV
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TITLE: Tracksoar v1
Design by: Mike "Swiss" Bales
REV: 1
Date: 10/9/15 12:32 AM
Sheet: 1/1