

Blockdiagram

FPGA 10Gig Switch

PCB Arts GmbH

Project name	Customer	Author	Revision	Date
FPGA 10Gig Switch	Symbiotic GmbH	Alexander Brehl	1.3	03.05.2023

Rev.	Date	changed by	Brief description of the change
1.0	24.11.2022	Alexander Brehl	authoring the document
1.1	15.12.2022	Alexander Brehl	Implementing changes from Zoomcall 09.12.22
1.2	07.02.2023	Alexander Brehl	Updated Interfaces and Bank numbers
1.3	03.05.2023	Alexander Brehl	Added: 2 nd USB MUX for Flashing CM4 Added: EMCCLK Changed: 125MHz Clock to 156.25MHz Clock

Overview

