110 Appendix Maxc Operations

OLD BIPOLAR CARD CHIP CHANGING MAP MAXC1 ONLY

Memory	Bits	Slot	Memory	Low 2 Address Bits	Row
SM/DM	0-17	16	SM, DM, MAP	00	D
	18-35	17		01	C
				10	В
MAP	18-35	19		11	A
IM 0-1777	0-17 LH	2	IM	00	A
IIVI O-1777	18-35 LH	3	1111	01	B
	0-17 RH (36-53)	4		10	C
	18-35 RH (54-71)	5		10	D
	10-33 KH (34-71)			11	D
IM 2000-3777	0-17 LH	6			
	18-35 LH	7			
	0-17 RH (36-53)	8			
	18-35 RH (54-71)	9			

Bit numbers for I-3107 storage chips

Top numbers for cards storing bits 0-17. Bottom numbers for cards storing bits 18-35.

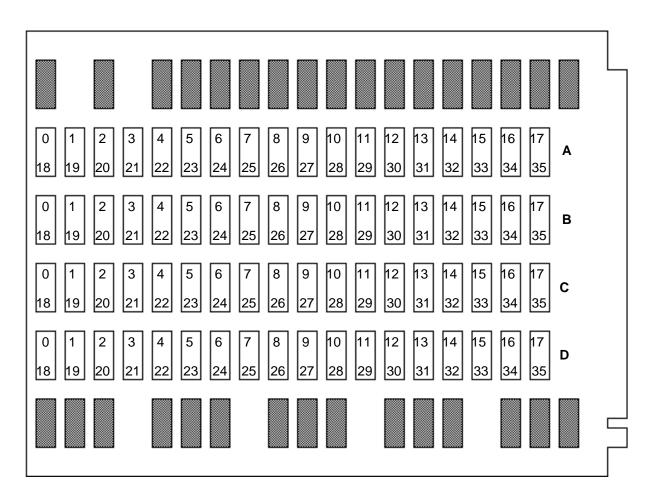


Figure 1.