Maxc Operations Appendix 111

## OLD BIPOLAR CARD CHIP CHANGING MAP MAXC2 ONLY

Memory	Bits	Slot	Memory	Low 2 Address Bits	Row
SM/DM	0-17	8	SM, DM, MAP	00	D
	18-35	9		01	C
				10	В
MAP	18-35	5		11	Α
IM 0-1777	0-17 LH	10	IM	00	A
	18-35 LH	11		01	В
	0-17 RH (36-53)	12		10	C
	18-35 RH (54-71)	13		11	D
IM 2000-3777	0-17 LH	14			
IWI 2000-3777	18-35 LH	15			
		16			
	0-17 RH (36-53)				
	18-35 RH (54-71)	17			
			l		

## Bit numbers for I-3107 storage chips

Top numbers for cards storing bits 0-17. Bottom numbers for cards storing bits 18-35.

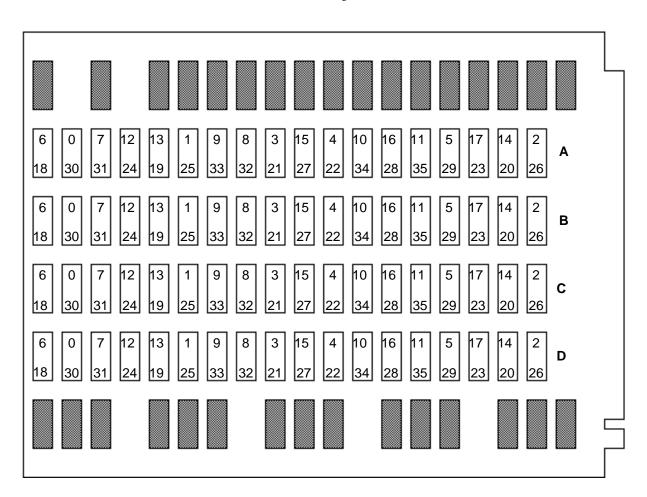


Figure 2.