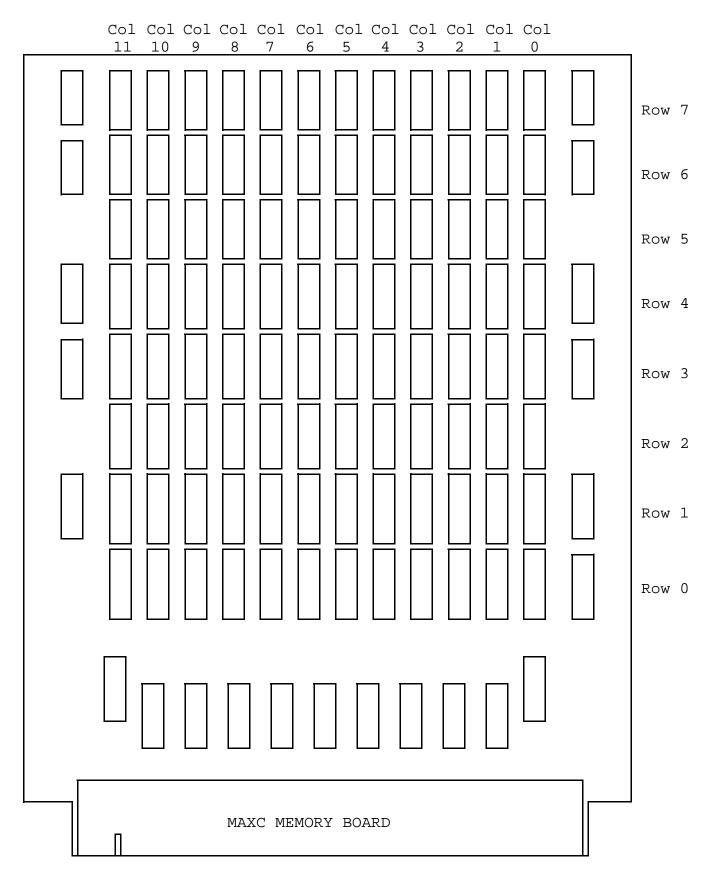
Maxc Operations



MEMORY CHIP LOCATION

Figure 4.