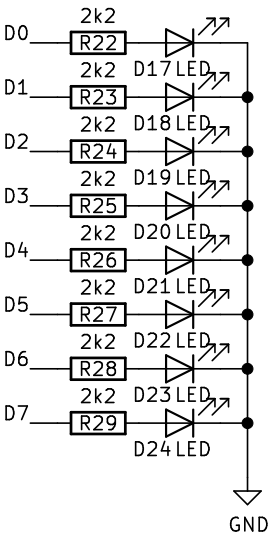
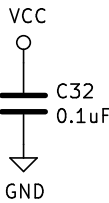


Sound module design will be in the next PCB.

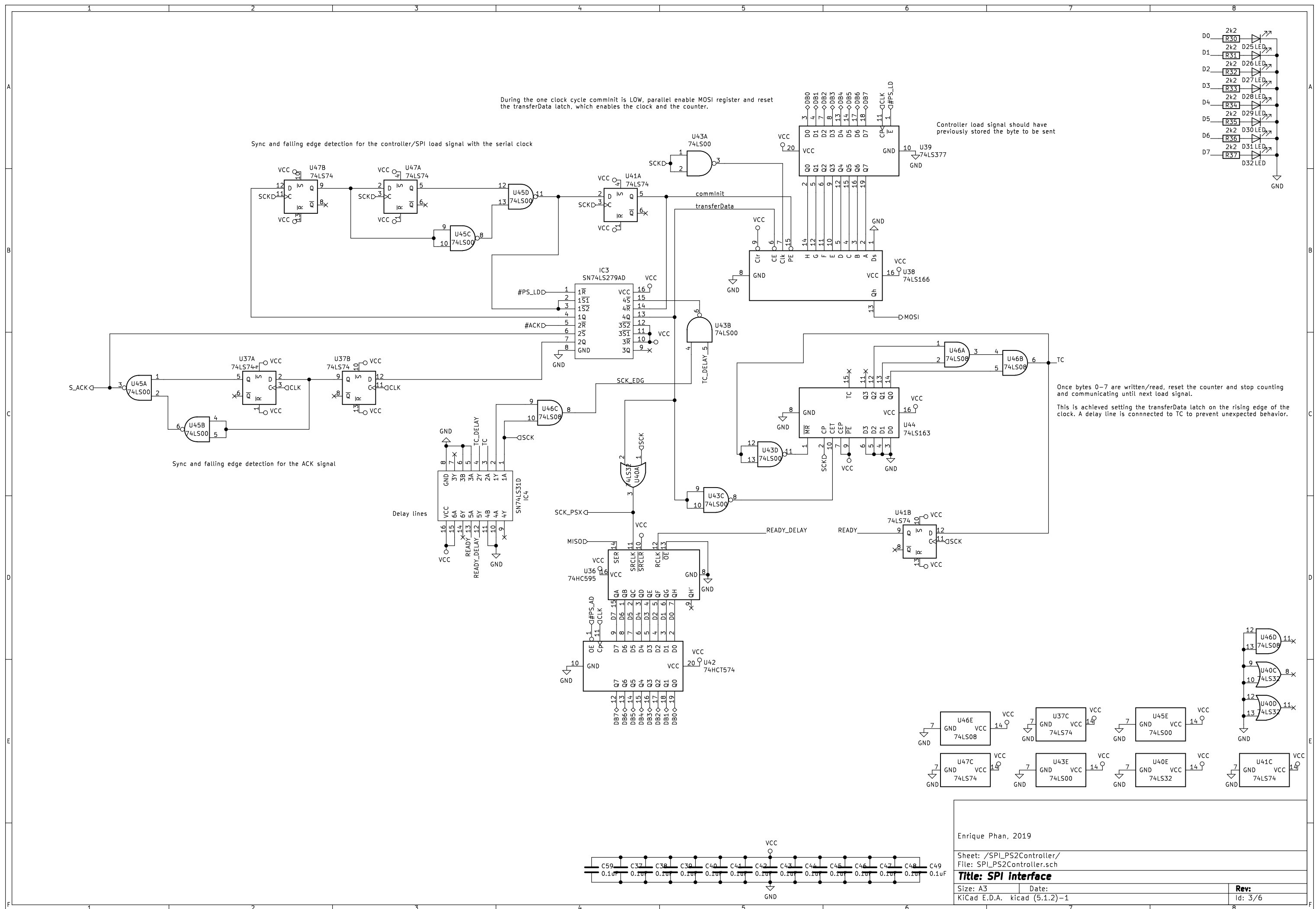


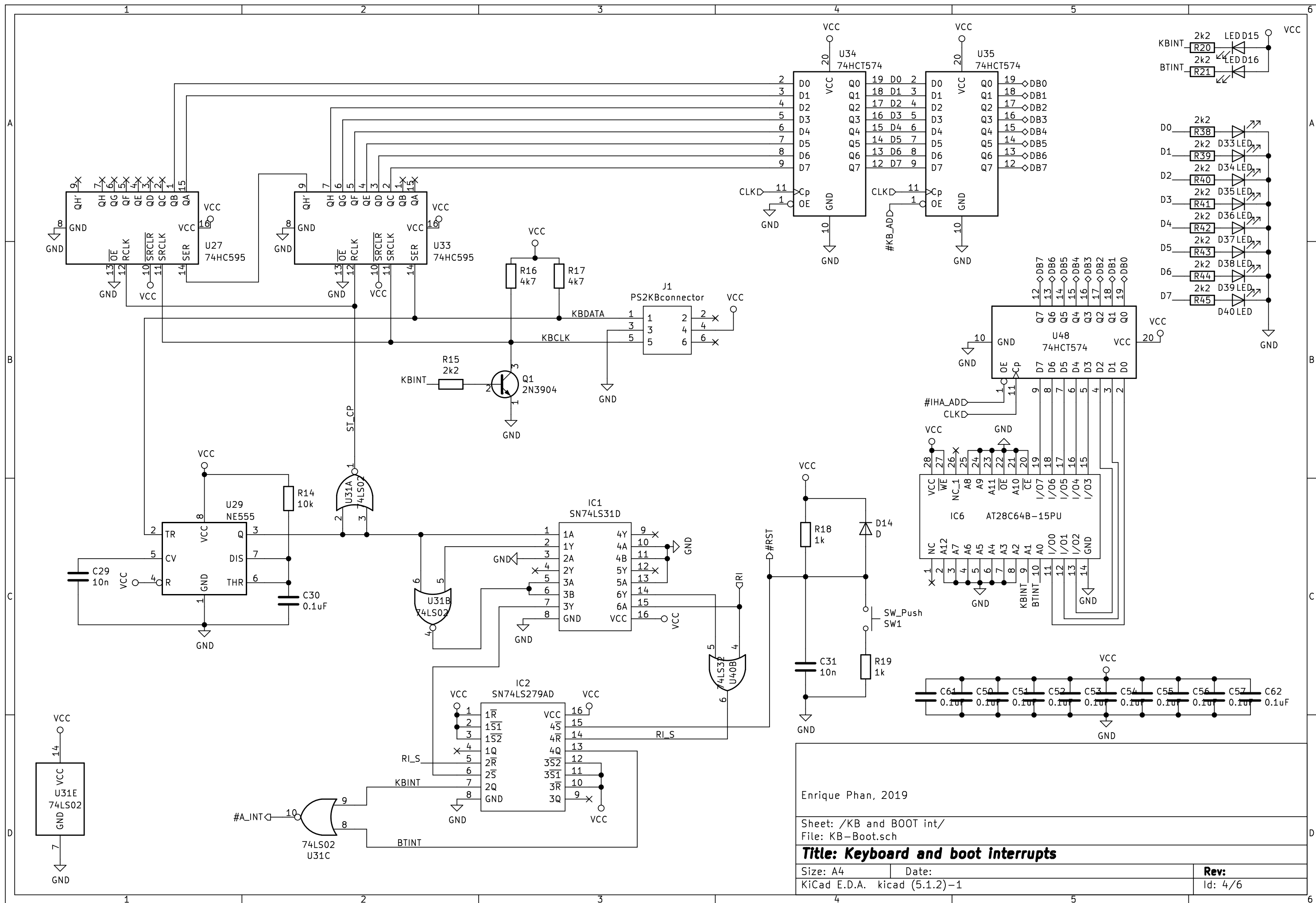
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Sheet: /SoundModule\_UF/  
File: SoundModule.sch

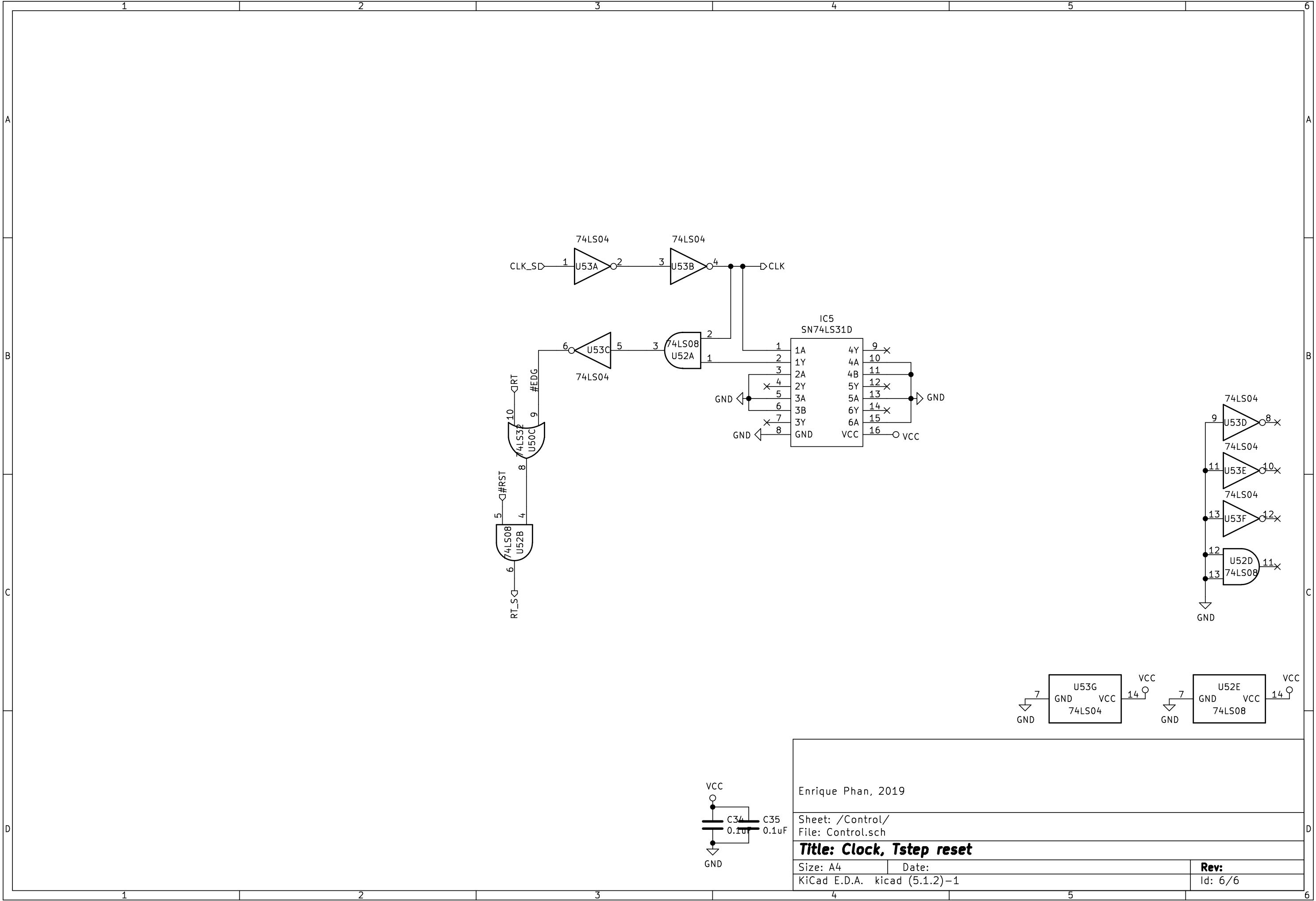
**Title: Output register**

|                              |       |         |
|------------------------------|-------|---------|
| Size: A4                     | Date: | Rev:    |
| KiCad E.D.A. kicad (5.1.2)-1 |       | Id: 2/6 |









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Sheet: /Control/  
File: Control.sch

**Title: Clock, Tstep reset**

|                              |       |         |
|------------------------------|-------|---------|
| Size: A4                     | Date: | Rev:    |
| KiCad E.D.A. kicad (5.1.2)-1 |       | Id: 6/6 |