

RELEASED 30-SEP-2025

Page	Index
1	COVER PAGE
2	BLOCK DIAGRAM
3	COMPONENT BLOCKS 1
4	COMPONENT BLOCKS 2
5	MCU

DESIGN CONSIDERATIONS:

DESIGN NOTE:

Example text for informational

DESIGN NOTE:

Example text for debug notes.

LAYOUT NOTE:

Example text for critical
layout guidelines.


DESIGN NOTE:

Example text for cautionary
design notes.

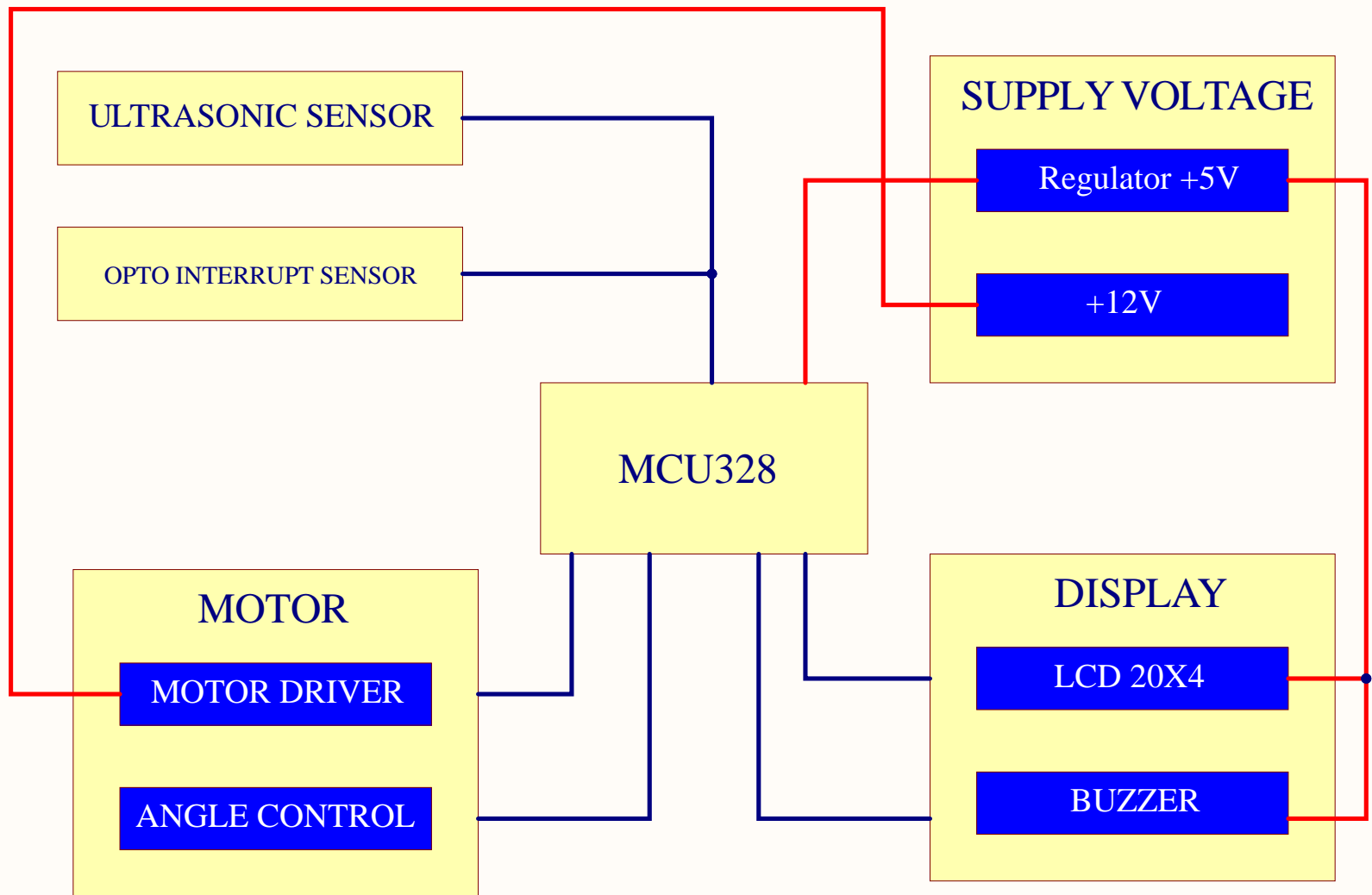
DESIGN NOTE:

Example text for critical
design notes.

Project:			TKHTN.PrjPcb		
Page Contents:			Coverpage.SchDoc		
Revision: v1		Design: Group 2	Date: 11/5/2025		
Size: A4	Document Number: 1		Sheet 1 of 5		



BLOCK DIAGRAM




Project:		TKHTN.PrjPcb	
Page Contents:		BLOCK DIAGRAM.SchDoc	
Revision: v1	Design: Group 2	Date: 11/5/2025	
Size: A4	Document Number: 2	Sheet 2 of 5	



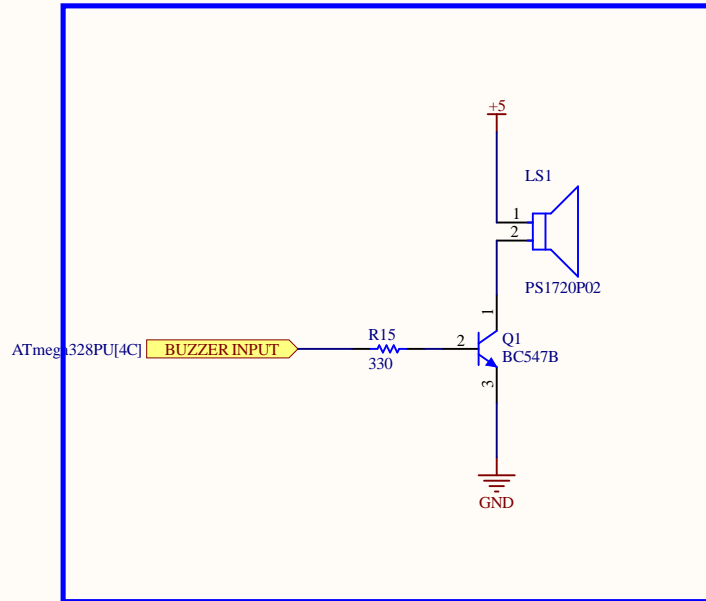
The diagram shows a 12V to 5V buck converter circuit. It features a 12V DC source (P1) connected to the input of an L7805ABV voltage regulator (U4). The regulator's output is connected to a 5V load (P2). The circuit includes several capacitors: a 1000uF capacitor (C5) at the input, a 1pF capacitor (C1) at the output, and a 1000uF capacitor (C6) at the output. The ground connection is labeled GND.

Project: <i>TKHTN.PrjPcb</i>			
Page Contents: <i>COMPONENT BLOCKS 1.SchDoc</i>			
Revision: v1		Design: Group 2	Date: 11/5/2025
Size: A4	Document Number: 3		Sheet 3 of 5

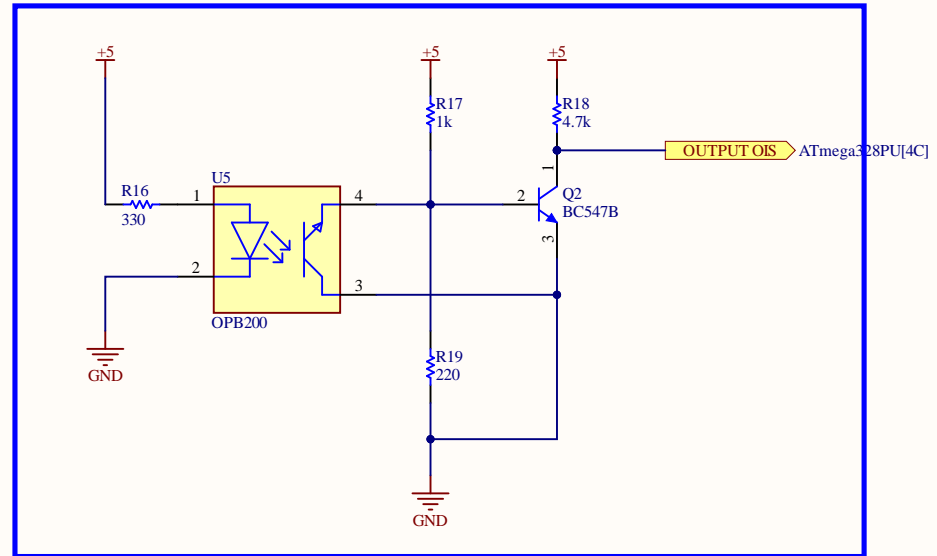




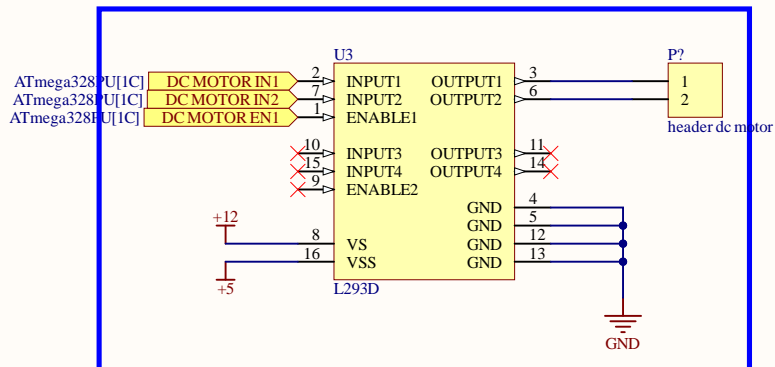
BUZZER



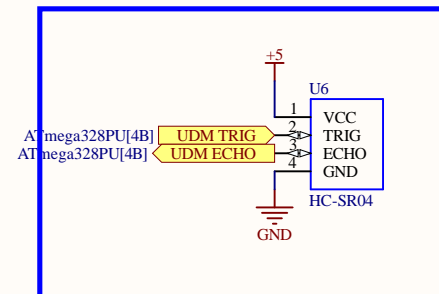
OPTO INTERRUPT SENSOR



DC MOTOR DRIVER



UDM SENSOR



Project: *TKHTN.PrjPcb*

Page Contents: *COMPONENT BLOCKS 2.SchDoc*

Revision: v1

Design: Group 2

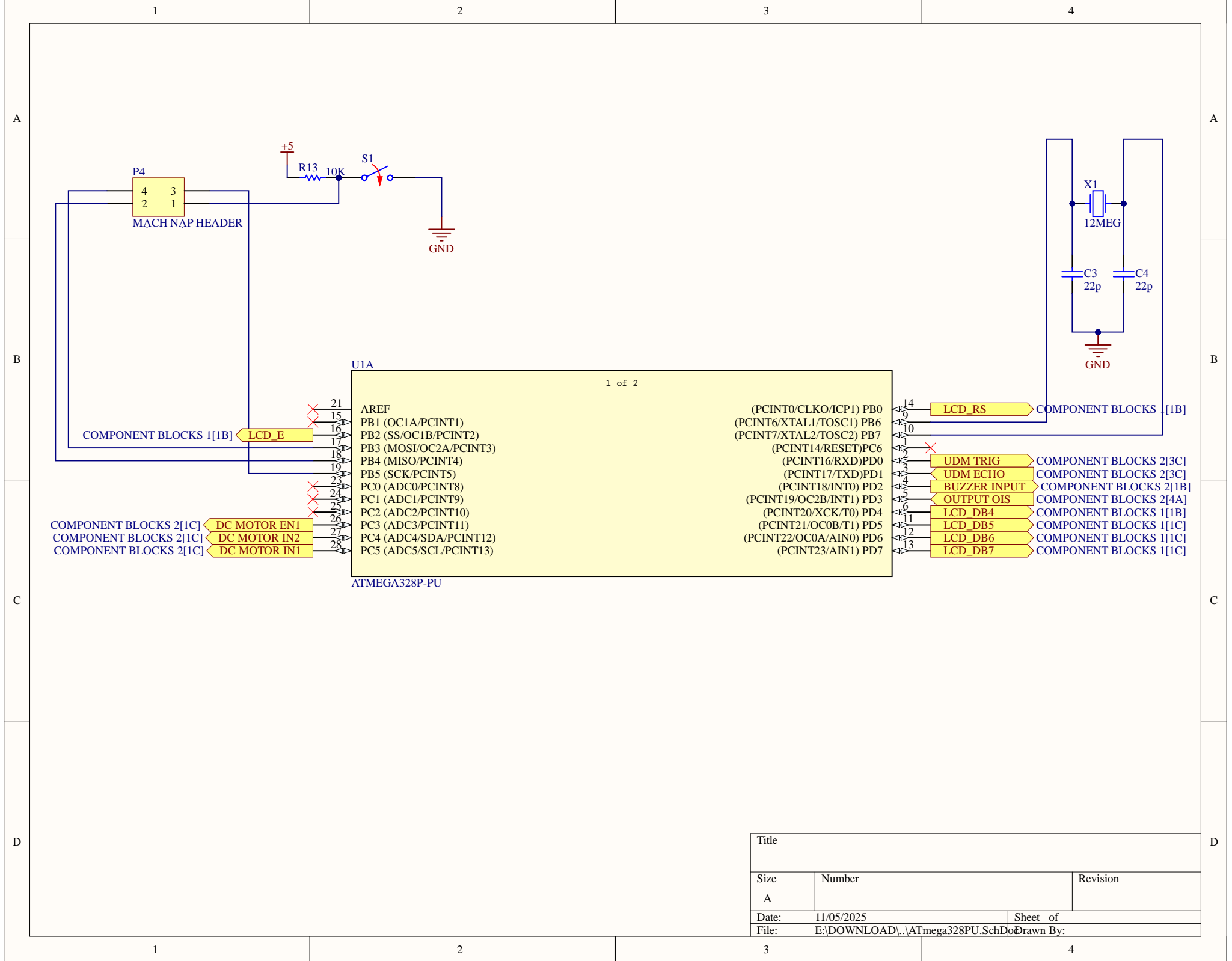
Date: 11/5/2025

Size: A4

Document Number: 4

Sheet 4 of 5





Title		
Size	Number	Revision
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Date:	11/05/2025	Sheet of
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