Checkpoint 2

1) Pin Symbol Augusts for anotog signeds to be converted into digital signals by the APC
Pin powerides a ground convertour to the postessessof the circuits inside the APC that
control the digital signals. (this is done not to mix the analog & digital signals inside

H. D.C. (1971) 1-8 Cho-CA7 9 DGND the ADC chips) 10 CS (SADN Shir pin controls when the chip is intented & turned off. When pulled high' (ie: when a high voltage is pared through it, or I is the transition will be put on low power stend by & when julled low' (ie: wen a low voltage "is passed through it, or O) the ADC will be in this was 33V This pin sends digital signals into the ADC (in this was to enable the ADC to communicate in this case; OV 11 M the Raspberry Pi) This pin outras allows for the output of digital signals that the ADX has converted from analog signal Pir that controls sampling rate & of the ADC & clock out every but of the signal corners 13 done by the ADC AGNO Pin provides a ground connection to the wicints in the ADC that control the analog rignal This provides a voltage that will be used as a deferance when compains input analog Shirpin supplies the urrent needed to operate the chip. Return value explination Method Name Keturns the number of channels available on the ADC . analog Count () Returns the resolution of the ADC in bits . analog Resolution () Return the mascimum integer that the ADC can output (limitation of avoilable bils) andley Mascimum () Robbin intiger ratio of the digital regard being output from descriet dancel analog Reference () Returns integer value of the digital regical being out first from the desired chance analog Read (int) * 幸 Returns floot value of the digital signal beeing unjust output from the desired charel analog Read Float (int) Keturns the voltage being input from the desired channel analog Read Volt (int) Does rame as their respective methods (see above) but will output Beturn the readings for all available channels in form of a list. analog Read All () analog Read All Float (analog Read No Volt ()

or any voltage lenthon the min-resolution of the ADC

0	3)	Sh	not dir	at ADC mit is that of OV as it will always consequent to the minimum divital		
	- 3,3V	3) The most direct ADC output is that of OV as it will always correspond to the minimum digitary number possible that the ADC can output.				
(2)	Ly					
	ch order to Sind what the Sinal voltage output of the ADC, the following formula can employed: ADC Values = Analog Mesural Voltage x Resolution of ADC System voltage WAN					
	7	eny	loyed:	Analog Mesung Voltage x Resolution al ADI		
7 k D \$	AVC	System volage Why				
onnd -						
rund	4)	As an LDR has an inverse relation lectures light intensity & Resistance when the LDR is				
		LOVE	covered by the hand, it's registance increases. As the LDR is set up in a voltage chirals w/a			
		fisce	fixed resitor, the output Voltage when the LDR is covered will decrease "She ADC will reced			
0		thi	this reduced voltage & will compare it to the combant reference voltage. She ADC will then			
3	3,3	use	use that comparison (which will be lower than the previous wold input voltage) & output a			
		reduced digital signal (ie: number)				
(3)	1	0 /				
(2)	5)) Pin	Symbol	Shis pin supplies the current needed to operate the ADC chip		
	7	1	V _{pp}	This pin supplies the current needed to operate the ADC chip		
2	100	2	NC			
1	4	3	C S	When pulled lon, thin pin will enable the DAC to profundate featurem & Seried clock (ie: two) Pin that controls the controls the Seried clock of the DAC Controls the Seried date input, Enables communications between the Baspbery Pi & the DAC		
-	Topi	4	SCK	Pin that controls the controls the Serial clash of the DAC		
0		5	SDI	Controls the Social duty input, Enables communication between the Baspbery ?: Ethe DAC		
		6\$7	NC			
		8	LEDAC	When this pin is pulled low, the DAC will output the specified voltages from both Voltage outpins.		
		q	SHOW	When this pin is pulled low, the DAC will output the specified voltages from lasth Voltage outputs. When this pin is pulled low, the DAC will shirt down, seasing any more voltage output from		
				The folloge output wind.		
		10	VOUTB	The two (reporate) output chands of the bod (. Out put oltages will come out of there pain		
		14	Vous			
		11	VAEFB	? Provides a comfunt voltage that can be used as a reference in order to correctly output the		
		13	VREEL	Provides a combail voltage that can be used as a reference incorder to correctly output the desired voltage of to the output pins. Each reference voltage pin outs as a reference for it's respective output poltage channel		
				outque polage channel		
0		12	V _{SS}	Pin that provides a ground connection for the entire circuitry in the DAC.		
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