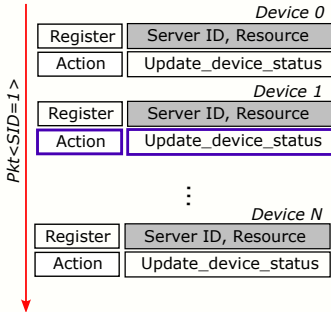


Storage (N register arrays)



Switch operations

