

# It's About Time: A Language-Based Approach to Constant-Time Programming

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## ABSTRACT

In this paper we present **CONSTANC**: a domain specific language for writing constant-time code. We describe the two main aspects of **CONSTANC**: the high- and low-level languages. The high-level language includes general-purpose programming constructs such as conditionals and loops, while the low-level language is built off of constant-time primitives. We formalize a translation between the two languages which preserves functional equivalence. We then describe the compiler we built for **CONSTANC**. Using the **CONSTANC** compiler, we implement various cryptographic functions and verify they run in constant time.

## 1. INTRODUCTION

Timing side channels are an important class of security bugs whereby an attacker is able use the timing of events in a system to discern secret data. One well-known timing attack was the “Lucky 13” attack against various SSL implementations [1], which used minute differences in the duration between a malicious request to a server and the resulting error message to recover an SSL session key.

Due to the sensitive nature of data that can be leaked from timing attacks, developers must take extra care to defend against such attacks. The current state-of-the-art used in many cryptographic libraries is constant-time programming. The idea behind constant-time programming is to structure code so that the time a program takes to run is not influenced by sensitive data. There are three main issues with constant-time programming: First, it restricts how the developer can use high level constructs like if-statements and loops, making the code difficult to read and write. This level of complexity can easily cause the developer to write buggy code if it is not tested properly. Second, since the code is written in a high level language, a developer may be using operations that are not actually constant time (e.g. floating point division). Finally, the compiler may optimize constant-time code to form a program that is not actually constant time.

We address the need to easily write verified constant-time code by making several contributions to constant-time programming: We formalize a core language, **CONSTCORE**, built using constant-time low-level primitives; we formalize code transformations from our high-level language, **CON-**

**STANC**, to our core language; we build a compiler for **CONSTANC** that produces constant-time programs; and finally, we implement cryptographic functions in **CONSTANC** and verify they run in constant time with *ct-verify* tool [2].

## 2. LANGUAGE DESIGN

The **CONSTANC** system is comprised of two distinct languages: **CONSTANC** proper, which end-users will develop programs in, and **CONSTCORE**, an internal language with specific timing guarantees. Before diving into the languages, we briefly outline our threat model.

### 2.1 Threat Model

For **CONSTANC**, we assume an adversary has remote access to the machine running the code. The adversary has the ability to measure the amount of time it takes for **CONSTANC** code to run. We also assume the caller has not been compromised and that they are invoking **CONSTANC** functions with valid values (as opposed to null values).

### 2.2 Constanc

The **CONSTANC** language is the language we intend end-users of our system to program in. As **CONSTANC** is intended to replace low-level code, the language is designed to feel very similar to C, to make it more accessible for developers.

There are, however, some necessary restrictions on what can be expressed in **CONSTANC**. For example, recursion of any sort is disallowed. Loop constructs are restricted to for-loops where loop bounds can only be constants. While these would be onerous restrictions for a general programming language, **CONSTANC** is intended for a very specific domain where these features of a language are generally unnecessary.

The language supports 32-bit signed integers, boolean values, and arrays of bytes. Most arithmetic and bitwise operations on these values are supported.

We do not prove timing guarantees directly in **CONSTANC**; instead, we show that the **CONSTCORE** language has the security guarantees we require, and transform all programs in **CONSTANC** to functionally equivalent programs in **CONSTCORE**.

The full grammar of **CONSTANC** can be found in Appendix A.3.

$$\begin{array}{c}
\text{STT-VAR-ASSIGN} \\
\frac{\llbracket e_h \rrbracket_t = e \quad e' = \text{ctx} \& \text{rnset} \quad e'' = e \& e' \quad e''' = x \& (\sim e')}{\llbracket x := e_h \rrbracket_{\text{ctx}} = x := (e'' \mid e''')}
\end{array}
\qquad
\begin{array}{c}
\text{STT-IF} \\
\frac{\llbracket e_h \rrbracket_t = e \quad \llbracket s_{h1} \rrbracket_{(\text{ctx}' \& \text{ctx})} = s_1 \quad \llbracket s_{h2} \rrbracket_{(\text{ctx}' \& \text{ctx})} = s_2}{\llbracket \text{if}_h e_h \text{ then } s_{h1} \text{ else } s_{h2} \rrbracket_{\text{ctx}} = \text{def ctx}' := e; s_1; \text{ctx}' := (\sim \text{ctx}'); s_2}
\end{array}$$

$$\begin{array}{c}
\text{STT-RET} \\
\frac{\llbracket e_h \rrbracket_t = e \quad e' = e \& (\text{ctx} \& \text{rnset})}{\llbracket \text{return}_h e_h \rrbracket_{\text{ctx}} = \text{rval} := (e' \mid \text{rval}); \text{rnset} := (\text{rnset} \& (\sim \text{ctx}))}
\end{array}$$

Figure 1: Excerpt of semantic transformations from CONSTANC to CONSTCORE. Full rule set can be found in Appendix A.4.

## 2.3 ConstCore

The CONSTCORE language is a WHILE-like language [11] with highly limited control flow. Notably, there are no conditional branching instructions, and the only loop construct is a for loop with static loop bounds. The type system is also more restrictive than that of CONSTANC: we support only 32-bit signed integers and byte arrays.

Comparison operators in CONSTCORE evaluate integer-width bitmasks of either all high bits (if the comparison is true) or all low bits (if false). The resulting bitmasks are then typically used to mask expressions that depend on the conditional result. For example, if a variable  $x$  was to be incremented by 3 only if the variable  $y$  was greater than 5, we could express that as the following statement:

$$x := x + ((y > 5) \& 3)$$

The result of the expression  $y > 5$  will either be 0b1111... if true or 0b0000... if false. The  $\&$  operator performs a bitwise masking operation, either leaving the value 3 as-is (if the bitmask was all 1s) or zeroing it out (if the bitmask was all 0s). Thus  $x$  gets incremented only if the condition holds.

To show the constant-time nature of CONSTCORE, we adopt the *program-transcript security model* from Molnar et al. [11], keeping a log of executed instructions as part of our small-step semantics (see Appendix A). We then show that every function in CONSTCORE has the following property: For every function  $f$ , there exists a transcript  $f_\kappa$  such that for any input  $x_1, \dots, x_n$  to  $f$  and current transcript  $\kappa$ , the transcript  $\kappa'$ , when  $f$  returns, is exactly  $\kappa + f_\kappa$ . That is to say, the program path through any function in CONSTCORE does not depend on the inputs to the function.

However, even if a function always follows the same program transcript, it might still take varying amounts of real-world time, as physical instructions themselves may have timing variations depending on input registers [6]. Our compiler currently targets LLVM IR, but with Intel x86 assembly in mind; it is currently difficult which instructions in this architecture are truly constant time without support from Intel. However, certain instructions, such as integer division (`idiv`), are known to have timing variations based on the input values [5]. Our mitigation strategy is to restrict ourselves to instructions that, to the best of our knowledge,

run in constant time with respect to arguments. These include basic arithmetic (excluding division and modulo) and comparison operators, as well as simple loads, stores, and calls. The constant-time nature of compiled CONSTCORE is thus reduced to the timing properties of the set of chosen instructions.

## 2.4 Transformations

If we can transform any program in CONSTANC to an equivalent program in CONSTCORE, then we can compile any CONSTANC program to a program with the timing guarantees shown in CONSTCORE.

Since the inputs and outputs of both CONSTCORE and CONSTANC are restricted to function parameters, return values, and memory access, we simply need to show that for every function  $f$  in CONSTANC, the corresponding transformed function  $f_{\text{core}}$  returns an equivalent value and sets equivalent memory values when given equivalent arguments and starting memory state.

We ensure this equivalence by keeping a “context”, which is a bitmask representing the control flow the of the function at the current statement. This bitmask is either *high* (all 1s) or *low* (all 0s). Any variable assignment in CONSTANC is transformed via the semantic transformation rule STT-VAR-ASSIGN shown in Figure 1, where  $\text{ctx}$  is the context bitmask and  $\&$ ,  $\mid$ ,  $\sim$  represent bitwise *and*, *or*, and *not* operations respectively. The  $\text{rnset}$  variable is an additional bitmask used for tracking early function return and is described below. With this transformation, variables are only updated to new values if the context at the time of execution indicates that the original program control flow would have made it to that statement.

Conditional branches are transformed by executing the statements in both branches. However, before each block is executed, the context bitmask is updated with the branch condition. Thus the statement is transformed via the rule STT-IF in Figure 1, where  $\langle s_1 \rangle$  and  $\langle s_2 \rangle$  are the transformations of  $s_1$  and  $s_2$ , and  $b$  is a fresh temporary variable for each instance of a branching statement. This ensures that nested conditionals still function as expected.

Return statements in CONSTANC are dealt with by constructing two additional variables for every function:  $\text{rval}$

(initialized low) and *rnset* (initialized high). A return statement in CONSTANC is translated to an assignment to *rval*, gated by the context as above. Additionally, *rnset* (“return value not set”) is updated to reflect the current return status. In this way, *rnset* remains high until a “return statement” is executed under an active control flow path, at which point it is set low and remains low for the rest of the function. Since all variable assignments are gated with *rnset* in addition to the context, no further variable assignments will cause updates. The formal transformation can be seen in the rule STT-RET in Figure 1.

### 3. COMPILER

We implemented a compiler for the CONSTANC language in OCaml, targeting LLVM bytecode. As syntax design is largely unimportant, we left the lexer and parser for future work. This allowed for fast iteration on the AST with minimal code changes. There are four main parts of the CONSTANC compiler: the *driver*, *type system*, *transformer*, and *IR generator*.

**Driver.** The driver controls the compilation process and is the interface between the developer and the compiler. It is a standard compiler driver, so we will omit all other details about it.

**Type System.** Although CONSTANC is a statically typed language, the type system is rather primitive: the only types are booleans, ints, and byte arrays. We guarantee type safety by type checking the CONSTANC AST. Types are checked again during IR generation, as LLVM IR is also typed. At the moment, we do not have support for structs or other record types; we leave this for future work.

CONSTANC supports a *for* loop, but ensures that it runs a constant number of cycles. Furthermore, CONSTANC only allows byte arrays to be indexed by constants or loop indices. The type checker ensures this property, guaranteeing that array accesses do not depend on secret values. The type checker also prevents out-of-bounds errors by checking the index against the byte array size, which can be done at compile time.

**Transformer.** The transformer converts the CONSTANC AST to CONSTCORE following the rules formally defined in Appendix A.4. Since CONSTCORE is built using known constant-time primitives, we can generate our LLVM IR using this language, ensuring our output runs in constant time.

**IR Generator.** The IR generator takes a CONSTCORE program and produces LLVM IR. The primitives used in the IR are critical to the resulting code running in constant time. To the best of our knowledge, all of the allowed operations run in constant time with 32 bit integers.

### 4. EVALUATION

We were not able to evaluate our implementation due to a variety of reasons.

[2]

Moreover, we were not able to perform microbenchmarks on this function because it would not have been fair. We only implemented one function that cannot stand on its own — in order to truly test this, we would like to implement the functions needed to encrypt and decrypt, so that we can test the function in its full form.

TODO: Note that the branch in 2a is legal since the lengths in the conditional are public.

### 5. FUTURE WORK

Our goal for this quarter was to create a few implementations in our language to show that this concept is feasible and worth further research. As such, we have a plethora of future work that we would like to explore in the months to follow. The simplest to tackle is the creation of a syntax for our language — currently we must write programs directly in the AST. Having a corresponding syntax is key to stabilizing our language.

Another useful feature will be the concept of public and private labels on data. Our language currently treats all values as private — we would like to augment it to be able to relax restrictions on data that does not need protection which allow for more programmer flexibility.

Finally, an important goal of this project is to determine a method of usability testing for programming languages. A huge impediment in this task is that there are currently no meaningful ways of user testing programming languages at scale. One needs to be able to find a key group of users and be able to test in-depth at a large scale in order to get proper data. This is an open question that we are hoping to address in the following months.

### 6. RELATED WORK

The security community has been aware of side channel attacks for over 40 years [3, 7, 8, 17]. Recently, side channels have been shown to leak information via power analysis [15]. Furthermore, timing side channels have been shown to leak information from the cache [9] or other aspects of processor architecture [16].

Due to the rise in the number of side channel attacks, there has much work in creating defenses. [10, 12] use information flow control to prevent information leaks on encrypted data. [13] uses instruction-based scheduling to address cache based timing attacks. [4, 14] address timing attacks in LIO, a concurrent information flow control system for Haskell. Most closely related to CONSTANC, [11] uses C to C code transformations to avoid timing side channels.

### 7. CONCLUSION

In this paper, we introduced CONSTANC, a high level language that can be used for constant-time programming. We formalized the semantics of CONSTANC and CONSTCORE and the transformation from CONSTANC to CONSTCORE. With this formalization, we built a CONSTANC compiler which we successfully used to write various cryptographic

```

int ssl3_cbc_remove_padding(const SSL *s,
    SSL3_RECORD *rec, unsigned block_size,
    unsigned mac_size) {

    unsigned padding_length, good;
    const unsigned overhead = 1 + mac_size;

    if (overhead > rec->length) return 0;
    padding_length = rec->data[rec->length - 1];

    good = constant_time_ge(
        rec->length, padding_length + overhead);
    good &= constant_time_ge(
        block_size, padding_length + 1);

    padding_length = good & (padding_length + 1);

    rec->length -= padding_length;
    rec->type |= padding_length << 8;

    return constant_time_select_int(good, 1, -1);
}

```

(a) The original OpenSSL function.

```

int ssl3_cbc_remove_padding(const SSL *s,
    SSL3_RECORD *rec, unsigned block_size,
    unsigned mac_size){

    unsigned padding_length, good;
    const unsigned overhead = 1 + mac_size;

    if (overhead > rec->length) return 0;
    padding_length = rec->data[rec->length - 1];

    if(rec->length < padding_length + overhead)
        return -1;
    if(block_size < padding_length + 1)
        return -1;

    padding_length += 1;

    rec->length -= padding_length;
    rec->type |= padding_length << 8;

    return 1;
}

```

(b) The OpenSSL function expressed more intuitively, without using their constant-time branch-avoiding routines.

Figure 2: OpenSSL function used to evaluate Constanc.

functions. Our evaluation is limited to the success of writing these functions. In the future, we plan to evaluate CONSTANC based on performance and usability. Furthermore, we plan to create a syntax for CONSTANC and implement a lexer and parser for the compiler. Finally, we plan to add public labels to the type system allowing for more flexibility for the developer and better performance.

## 8. REFERENCES

- [1] N. J. Al Fardan and K. G. Paterson. Lucky thirteen: Breaking the tls and dtls record protocols. In *Security and Privacy (SP), 2013 IEEE Symposium on*, pages 526–540. IEEE, 2013.
- [2] J. B. Almeida, M. Barbosa, G. Barthe, F. Dupressoir, and M. Emmi. Verifying constant-time implementations. In *25th USENIX Security Symposium (USENIX Security 16)*, pages 53–70, Austin, TX, Aug. 2016. USENIX Association.
- [3] D. Brumley and D. Boneh. Remote timing attacks are practical. *Computer Networks*, 48(5):701–716, 2005.
- [4] P. Buiras, A. Levy, D. Stefan, A. Russo, and D. Mazières. *A Library for Removing Cache-Based Attacks in Concurrent Information Flow Systems*, pages 199–216. Springer International Publishing, Cham, 2014.
- [5] Intel. Intel 64 and ia-32 architectures optimization reference manual. *Intel Corporation*, June, 2016.
- [6] T. Kaufmann, H. Pelletier, S. Vaudenay, and K. Villegas. When constant-time source yields variable-time binary: Exploiting curve25519-donna built with msvc 2015. In *International Conference on Cryptology and Network Security*, pages 573–582. Springer, 2016.
- [7] B. W. Lampson. A note on the confinement problem. In *Communications of the ACM*, volume 16, pages 613–615. ACM, 1973.
- [8] S. B. Lipner. A comment on the confinement problem. In *SOSP '75 Proceedings of the fifth ACM symposium on Operating systems principles*. ACM, 1975.
- [9] F. Liu, Y. Yarom, Q. Ge, G. Heiser, and R. B. Lee. Last-level cache side-channel attacks are practical. In *2015 IEEE Symposium on Security and Privacy*, pages 605–622, May 2015.
- [10] J. C. Mitchell, R. Sharma, D. Stefan, and J. Zimmerman. Information-flow control for programming on encrypted data. In *2012 IEEE 25th Computer Security Foundations Symposium*. IEEE, 2012.
- [11] D. Molnar, M. Piotrowski, D. Schultz, and D. Wagner. The program counter security model: Automatic detection and removal of control-flow side channel attacks. In *Proceedings of the 8th International Conference on Information Security and Cryptology, ICISC'05*, pages 156–168, Berlin, Heidelberg, 2006. Springer-Verlag.
- [12] J. Planul and J. C. Mitchell. Oblivious program execution and path-sensitive non-interference. In *Computer Security Foundations Symposium (CSF)*. IEEE, 2013.
- [13] D. Stefan, P. Buiras, E. Yang, A. Levy, D. Terei, A. Russo, and D. Mazières. Eliminating cache-based timing attacks with instruction-based scheduling. In *Proceedings of the 18th European Symposium on Research in Computer Security (ESORICS 2013)*, 2013.
- [14] D. Stefan, A. Russo, P. Buiras, A. Levy, J. C. Mitchell, and D. Mazières. Addressing covert termination and timing channels in concurrent information flow systems. In *Proceedings of the 17th ACM SIGPLAN International Conference on Functional Programming, ICFP '12*, pages 201–214, New York, NY, USA, 2012. ACM.
- [15] S. Vaudenay. *Side-Channel Attacks on Threshold Implementations Using a Glitch Algebra*, pages 55–70. Springer International Publishing, Cham, 2016.
- [16] Z. Wang and R. Lee. Covert and side channels due to processor architecture. In *ACSAC '06 Proceedings of the 22nd Annual Computer Security Applications Conference*. ACM, 2004.
- [17] J. C. Wray. An analysis of covert timing channels. In *ACM Journal of Computer Science*, volume 1, pages 219–222. ACM, 1991.

## APPENDIX

### A. SEMANTICS OF CONSTANC

#### A.1 CONSTCORE Grammar

$\ominus$	$::=$   $\sim$	unary operations bitwise not
$\oplus$	$::=$   $+$   $-$   $*$   $<<$   $>>$   $\&$   $ $   $==_s$   $!=_s$   $>_s$   $<_s$   $>=_s$   $<=_s$	binary operations integer addition integer subtraction integer multiplication bitshift left bitshift right bitwise and bitwise or equals (sign extended) not equals (sign extended) greater than (sign extended) less than (sign extended) greater than or equal (sign extended) less than or equal (sign extended)
$e$	$::=$   <b>TRUE</b>   <b>FALSE</b>   $c$   $a$   $x$   $a[e]$   $\ominus e$   $e_1 \oplus e_2$   $f(e_1, \dots, e_n)$	expressions bitmask true (0b1111...) bitmask false (0b0000...) numeric value bytearray variable array access unary operation binary operation function application
$s$	$::=$   <b>skip</b>   $s_1; s_2$   <b>def</b> $x := e$   <b>adef</b> $x := a$   $x := e$   $a[e_1] := e_2$   <b>for</b> $x$ <b>from</b> $v_1$ <b>to</b> $v_2$ <b>:</b> $s$	statements skip sequence variable declaration array declaration variable assignment array assignment for loop
$fval$	$::=$   $(x_1, \dots, x_n) : s @ e$	function spec
$fndef$	$::=$   <b>fdef</b> $f fval$	function definition
$program$	$::=$   $fndef_1; \dots; fndef_n; \mathbf{expose} fndef$	program list of fdefs

## A.2 CONSTCORE Small-Step Semantics

$\Lambda$	$::=$	function store
	$\emptyset_\Lambda$	empty function store
	$\Lambda[f \mapsto fval]$	define function
$\Gamma$	$::=$	global memory
	$\emptyset_\Gamma$	
	$\Gamma[a \mapsto []]$	new array
	$\Gamma[a \mapsto \Gamma(a)[v_1 \mapsto v_2]]$	array update
$\mu$	$::=$	local memory
	$\emptyset_\mu$	empty memory
	$\mu[x \mapsto v]$	add/update variable
	$\mu_1 \triangleright \mu_2$	push stack frame
$\{\sigma\}$	$::=$	variable substitution
	$\{x_1/v_1, \dots, x_k/v_k\}$	

$$\boxed{\{\Lambda, \Gamma, \mu, \kappa\} e \longrightarrow \{\Lambda', \Gamma', \mu', \kappa'\} e'} \quad (e \text{ reduces to } e')$$

$$\frac{\text{EXR-VAR} \quad \mu = \mu'[x \mapsto v] \quad \kappa' = \kappa \triangleright \mathbf{load}}{\{\Lambda, \Gamma, \mu, \kappa\} x \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} v}$$

$$\frac{\text{EXR-ARR-GET-EXPR} \quad \{\Lambda, \Gamma, \mu, \kappa\} e \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} e'}{\{\Lambda, \Gamma, \mu, \kappa\} a[e] \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} a[e']}$$

$$\frac{\text{EXR-ARR-GET-VAL} \quad v' = \Gamma(a)[v] \quad \kappa' = \kappa \triangleright \mathbf{load}}{\{\Lambda, \Gamma, \mu, \kappa\} a[v] \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} v'}$$

$$\frac{\text{EXR-UNOP-EXPR} \quad \{\Lambda, \Gamma, \mu, \kappa\} e \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} e'}{\{\Lambda, \Gamma, \mu, \kappa\} \ominus e \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} \ominus e'}$$

$$\frac{\text{EXR-UNOP-VAL} \quad v' \equiv \llbracket \ominus v \rrbracket \quad \kappa' = \kappa \triangleright \ominus}{\{\Lambda, \Gamma, \mu, \kappa\} \ominus v \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} v'}$$

$$\frac{\text{EXR-BINOP-L} \quad \{\Lambda, \Gamma, \mu, \kappa\} e_1 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} e'_1}{\{\Lambda, \Gamma, \mu, \kappa\} e_1 \oplus e_2 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} e'_1 \oplus e_2}$$

$$\frac{\text{EXR-BINOP-R} \quad \{\Lambda, \Gamma, \mu, \kappa\} e_2 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} e'_2}{\{\Lambda, \Gamma, \mu, \kappa\} v \oplus e_2 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} v \oplus e'_2}$$

$$\frac{\text{EXR-BINOP-VAL} \quad v_3 \equiv \llbracket v_1 \oplus v_2 \rrbracket \quad \kappa' = \kappa \triangleright \oplus}{\{\Lambda, \Gamma, \mu, \kappa\} v_1 \oplus v_2 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} v_3}$$

$$\frac{\text{EXR-SUBST-EMPTY}}{\{\Lambda, \Gamma, \mu, \kappa\} \{\} e \longrightarrow \{\Lambda, \Gamma, \mu, \kappa\} e}$$

$$\frac{\text{EXR-SUBST-EXPR} \quad \{\Lambda, \Gamma, \mu, \kappa\} e \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} e'}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} e \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} \{\sigma\} e'}$$

$$\frac{\text{EXR-SUBST-VAR} \quad \kappa' = \kappa \triangleright \mathbf{load}}{\{\Lambda, \Gamma, \mu, \kappa\} \{x_1/v_1, \dots, x_k/v_k\} x_i \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} x_i}$$

$$\frac{\text{EXR-SUBST-NO-VAR}}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} x \longrightarrow \{\Lambda, \Gamma, \mu, \kappa\} x}$$

$$\frac{\text{EXR-SUBST-VAL}}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} v \longrightarrow \{\Lambda, \Gamma, \mu, \kappa\} v}$$

$$\frac{\text{EXR-FN-EXPR} \quad \{\Lambda, \Gamma, \mu, \kappa\} e_1 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} e'_1}{\{\Lambda, \Gamma, \mu, \kappa\} f(v_1, \dots, v_k, e_1, e_2, \dots, e_n) \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} f(v_1, \dots, v_k, e'_1, e_2, \dots, e_n)}$$

$$\frac{\text{EXR-FN-SUBST} \quad \kappa' = \kappa \triangleright \mathbf{store}}{\{\Lambda, \Gamma, \mu, \kappa\} f(x'_1/v'_1, \dots, x'_k/v'_k, v_1, v_2, \dots, v_n) \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} f(x'_1/v'_1, \dots, x'_k/v'_k, x_1/v_1, v_2, \dots, v_n)}$$

$$\frac{\text{EXR-FN-CALL} \quad \Lambda = \Lambda'[f \mapsto (x_1, \dots, x_k) : s @ e] \quad \mu' = \mu \triangleright \emptyset_\mu \quad \kappa' = \kappa \triangleright f}{\{\Lambda, \Gamma, \mu, \kappa\} f(x_1/v_1, \dots, x_k/v_k) \longrightarrow \{\Lambda, \Gamma, \mu', \kappa'\} \{x_1/v_1, \dots, x_k/v_k\} s @ e}$$

EXR-SKIP-EXPR

$$\frac{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} e \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} e'}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} \mathbf{skip} @e \longrightarrow \{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} \mathbf{skip} @e'}$$

EXR-SKIP-VAL

$$\frac{\mu = \mu_1 \triangleright \mu_2 \quad \kappa' = \kappa \triangleright \mathbf{ret}}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} \mathbf{skip} @v \longrightarrow \{\Lambda, \Gamma, \mu_1, \kappa'\} v}$$

EXR-SEQ

$$\frac{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} s_1 @e_0 \longrightarrow \{\Lambda, \Gamma, \mu', \kappa'\} \{\sigma'\} s'_1 @e_0}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} s_1; s_2 @e_0 \longrightarrow \{\Lambda, \Gamma, \mu', \kappa'\} \{\sigma'\} s'_1; s_2 @e_0}$$

EXR-SEQ-SKIP

$$\frac{}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} \mathbf{skip}; s @e_0 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} s @e_0}$$

EXR-DEF-EXPR

$$\frac{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} e \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} e'}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} \mathbf{def} x := e @e_0 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} \{\sigma\} \mathbf{def} x := e' @e_0}$$

EXR-DEF-VAL

$$\frac{\mu' = \mu[x \mapsto v] \quad \kappa' = \kappa \triangleright \mathbf{store}}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} \mathbf{def} x := v @e_0 \longrightarrow \{\Lambda, \Gamma, \mu', \kappa'\} \{\sigma\} \mathbf{skip} @e_0}$$

EXR-DEF-ARR

$$\frac{\Gamma' = \Gamma[a \mapsto []] \quad \mu' = \mu[x \mapsto a] \quad \kappa' = \kappa \triangleright \mathbf{store}}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} \mathbf{adef} x := a @e_0 \longrightarrow \{\Lambda, \Gamma', \mu', \kappa'\} \{\sigma\} \mathbf{skip} @e_0}$$

EXR-ASSIGN-EXPR

$$\frac{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} e \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} e'}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} x := e @e_0 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} \{\sigma\} x := e' @e_0}$$

EXR-ASSIGN-VAL

$$\frac{\mu' = \mu[x \mapsto v] \quad \kappa' = \kappa \triangleright \mathbf{store}}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} x := v @e_0 \longrightarrow \{\Lambda, \Gamma, \mu', \kappa'\} \{\sigma\} \mathbf{skip} @e_0}$$

EXR-ARR-ASSIGN-EXPR-L

$$\frac{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} e_1 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} e'_1}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} a[e_1] := e_2 @e_0 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} \{\sigma\} a[e'_1] := e_2 @e_0}$$

EXR-ARR-ASSIGN-EXPR-R

$$\frac{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} e_2 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} e'_2}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} a[v_1] := e_2 @e_0 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} \{\sigma\} a[v_1] := e'_2 @e_0}$$

EXR-ARR-ASSIGN-VAL

$$\frac{\Gamma' = \Gamma[a \mapsto \Gamma(a)[v_1 \mapsto v_2]] \quad \kappa' = \kappa \triangleright \mathbf{store}}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} a[v_1] := v_2 @e_0 \longrightarrow \{\Lambda, \Gamma', \mu, \kappa'\} \{\sigma\} \mathbf{skip} @e_0}$$

EXR-FOR

$$\frac{v_1 < v_2 \quad v'_1 = v_1 + 1 \quad \kappa' = \kappa \triangleright \mathbf{store}}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} \mathbf{for} x \mathbf{from} v_1 \mathbf{to} v_2 : s @e_0 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} \{\sigma\} (\{x/v_1\} s); \mathbf{for} x \mathbf{from} v'_1 \mathbf{to} v_2 : s @e_0}$$

EXR-ADD-SUBST

$$\frac{\begin{array}{l} \{\sigma_1\} \cap \{\sigma_2\} = \{\} \\ \{\sigma_3\} = \{\sigma_1\} \cup \{\sigma_2\} \end{array}}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma_1\} (\{\sigma_2\} s) @e_0 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} \{\sigma_3\} s @e_0}$$

EXR-FOR-BASE

$$\frac{v_1 = v_2}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} \mathbf{for} x \mathbf{from} v_2 \mathbf{to} v_2 : s @e_0 \longrightarrow \{\Lambda, \Gamma, \mu', \kappa\} \{\sigma\} \mathbf{skip} @e_0}$$

### A.3 CONSTANC Grammar

$\ominus_h$	$::=$ $ $ $!$ $ $ $\sim$	unary operations logical not bitwise not
$\oplus_h$	$::=$ $ $ $+$ $ $ $-$ $ $ $*$ $ $ $<<$ $ $ $>>$ $ $ $\&$ $ $ $ $ $ $ $\&\&$ $ $ $  $ $ $ $==$ $ $ $!=$ $ $ $>$ $ $ $<$ $ $ $>=$ $ $ $<=$	binary operations integer addition integer subtraction integer multiplication bitshift left bitshift right bitwise and bitwise or logical and logical or equals not equals greater than less than greater than or equals less than or equals
$e_h$	$::=$ $ $ $b_h$ $ $ $c_h$ $ $ $a$ $ $ $x$ $ $ $a[e_h]$ $ $ $\ominus_h e_h$ $ $ $e_{h1} \oplus e_{h2}$ $ $ $f_h(e_{h1}, \dots, e_{hn})$	expressions boolean value numeric value bytearray variable array access unary operation binary operation function application
$s_h$	$::=$ $ $ <b>skip</b> <sub><math>h</math></sub> $ $ $s_{h1}; s_{h2}$ $ $ <b>def</b> <sub><math>h</math></sub> $x := e_h$ $ $ <b>adef</b> <sub><math>h</math></sub> $x := a$ $ $ $x := e_h$ $ $ $a[e_{h1}] := e_{h2}$ $ $ <b>for</b> <sub><math>h</math></sub> $x$ <b>from</b> $v_{h1}$ <b>to</b> $v_{h2}$ $ $ <b>if</b> <sub><math>h</math></sub> $e_h$ <b>then</b> $s_{h1}$ <b>else</b> $s_{h2}$ $ $ <b>return</b> <sub><math>h</math></sub> $e_h$	statements skip sequence variable declaration array declaration variable assignment array assignment for loop conditional branch return
$hfval$	$::=$ $ $ $(x_1, \dots, x_n) : s_h$	function spec
$hfndef$	$::=$ $ $ <b>fdef</b> <sub><math>h</math></sub> $f_h$ $hfval$	function definition
$hprogram$	$::=$ $ $ $hfndef_1; \dots; hfndef_n; \textbf{expose}$ $hfndef$	program list of fdefs



## A.4 Transformations from CONSTANC to CONSTCORE

$\llbracket \ominus_h \rrbracket_t = \ominus$	$(\ominus_h \text{ is transformed to } \ominus)$					
UNOPT-LNOT			UNOPT-UNOP			
$\frac{}{\llbracket ! \rrbracket_t = \sim}$			$\frac{}{\llbracket \ominus_h \rrbracket_t = \ominus}$			
$\llbracket \oplus_h \rrbracket_t = \oplus$	$(\oplus_h \text{ is transformed to } \oplus)$					
BINOPT-LAND	BINOPT-LOR	BINOPT-EQ	BINOPT-NEQ	BINOPT-GT	BINOPT-LT	BINOPT-GTE
$\frac{}{\llbracket \&\& \rrbracket_t = \&}$	$\frac{}{\llbracket \mid \mid \rrbracket_t = \mid}$	$\frac{}{\llbracket == \rrbracket_t = ==_s}$	$\frac{}{\llbracket != \rrbracket_t = !=_s}$	$\frac{}{\llbracket > \rrbracket_t = >_s}$	$\frac{}{\llbracket < \rrbracket_t = <_s}$	$\frac{}{\llbracket >= \rrbracket_t = >=_s}$
BINOPT-LTE			BINOPT-BINOP			
$\frac{}{\llbracket <= \rrbracket_t = <=_s}$			$\frac{}{\llbracket \oplus_h \rrbracket_t = \oplus}$			
$\llbracket e_h \rrbracket_t = e$	$(e_h \text{ is transformed to } e)$					
EXT-VAL	EXT-VAR	EXT-ARR	EXT-ARR-GET	EXT-FN-CALL		
$\frac{v \equiv \llbracket v_h \rrbracket_{int}}{\llbracket v_h \rrbracket_t = v}$	$\frac{}{\llbracket x \rrbracket_t = x}$	$\frac{}{\llbracket a \rrbracket_t = a}$	$\frac{}{\llbracket a[e_h] \rrbracket_t = a[e]}$	$\frac{\begin{array}{l} \llbracket \mathbf{fdef}_h f_h hfval \rrbracket_t = \mathbf{fdef} f fval \\ \llbracket e_{h1} \rrbracket_t = e_1 \quad \dots \quad \llbracket e_{hk} \rrbracket_t = e_k \end{array}}{\llbracket f_h(e_{h1}, \dots, e_{hk}) \rrbracket_t = f(e_1, \dots, e_k)}$		
$\llbracket s_h \rrbracket_{ctx} = s$	$(s_h \text{ is transformed to } s)$					
STT-SKIP	STT-SEQ		STT-VAR-DEC			
$\frac{}{\llbracket \mathbf{skip}_h \rrbracket_{ctx} = \mathbf{skip}}$	$\frac{\llbracket s_{h1} \rrbracket_{ctx} = s_1 \quad \llbracket s_{h2} \rrbracket_{ctx} = s_2}{\llbracket s_{h1}; s_{h2} \rrbracket_{ctx} = s_1; s_2}$		$\frac{}{\llbracket e_h \rrbracket_t = e}$			$\frac{}{\llbracket \mathbf{def}_h x := e_h \rrbracket_{ctx} = \mathbf{def} x := e}$
STT-ARR-DEC	STT-VAR-ASSIGN		STT-ARR-ASSIGN			
$\frac{}{\llbracket \mathbf{adef}_h x := a \rrbracket_{ctx} = \mathbf{adef} x := a}$	$\frac{\begin{array}{l} \llbracket e_h \rrbracket_t = e \quad e' = ctx \& rnset \\ e'' = e \& e' \quad e''' = x \& (\sim e') \end{array}}{\llbracket x := e_h \rrbracket_{ctx} = x := (e'' \mid e''')}$		$\frac{\begin{array}{l} \llbracket e_{h1} \rrbracket_t = e_1 \\ \llbracket e_{h2} \rrbracket_t = e_2 \quad e' = ctx \& rnset \\ e'' = e_2 \& e' \quad e''' = a[e_1] \& (\sim e') \end{array}}{\llbracket a[e_{h1}] := e_{h2} \rrbracket_{ctx} = a[e_1] := (e'' \mid e''')}$			
STT-FOR	STT-IF		STT-RET			
$\frac{\begin{array}{l} \llbracket v_{h1} \rrbracket_t = v_1 \\ \llbracket v_{h2} \rrbracket_t = v_2 \quad \llbracket s_h \rrbracket_{ctx} = s \end{array}}{\llbracket \mathbf{for}_h x \mathbf{from} v_{h1} \mathbf{to} v_{h2} \rrbracket_{ctx} = \mathbf{for} x \mathbf{from} v_1 \mathbf{to} v_2 : s}$	$\frac{\begin{array}{l} \llbracket e_h \rrbracket_t = e \quad e' = e \& (ctx \& rnset) \\ e'' = e' \mid rval := (e' \mid rval); rnset := (rnset \& (\sim ctx)) \end{array}}{\llbracket \mathbf{return}_h e_h \rrbracket_{ctx} = rval := (e' \mid rval); rnset := (rnset \& (\sim ctx))}$		$\frac{\begin{array}{l} \llbracket e_h \rrbracket_t = e \quad \llbracket s_{h1} \rrbracket_{(ctx' \& ctx)} = s_1 \\ \llbracket s_{h2} \rrbracket_{(ctx' \& ctx)} = s_2 \end{array}}{\llbracket \mathbf{if}_h e_h \mathbf{then} s_{h1} \mathbf{else} s_{h2} \rrbracket_{ctx} = \mathbf{def} ctx' := e; s_1; ctx' := (\sim ctx'); s_2}$			
$\llbracket hfndef \rrbracket_t = fndef$	$(hfndef \text{ is transformed to } fndef)$					
FDEFT-FDEF	$\frac{\llbracket s_h \rrbracket_{\text{TRUE}} = s}{\llbracket \mathbf{fdef}_h f_h (x_1, \dots, x_k) : s_h \rrbracket_t = \mathbf{fdef} f (x_1, \dots, x_k) : \mathbf{def} rval := \text{FALSE}; \mathbf{def} rnset := \text{TRUE}; s @ rval}$					