

# It's About Time: A Language-Based Approach to Constant-Time Programming

Sunjay Cauligi    Brian Johannismeyer    Ariana Mirian    Gary Soeller  
University of California, San Diego  
{scauligi, bjohanne, amirian, gsoeller}@cs.ucsd.edu

## ABSTRACT

**TK** when rest of paper is done

## 1. INTRODUCTION

Timing side channels are an important class of security bugs whereby an attacker is able use the timing of events in a system to discern secret data. One well-known timing attack was the “Lucky 13” attack against various SSL implementations [2], which used minute differences in the duration between a malicious request to a server and the resulting error message to recover an SSL session key.

Due to the sensitive nature of data that can be leaked from timing attacks, developers must take extra care to defend against such attacks. The current state-of-the-art used in many cryptographic libraries is constant-time programming. The idea behind constant-time programming is to structure code so that the time a program takes to run is not influenced by sensitive data. There are three main issues with constant-time programming: First, it restricts how the developer can use high level constructs like if-statements and loops, making the code difficult to read and write. This level of complexity can easily cause the developer to write buggy code if it is not tested properly. Second, since the code is written in a high level language, a developer may be using operations that are not actually constant time (e.g. floating point division). Finally, the compiler may optimize constant-time code to form a program that is not actually constant time.

We address the need to easily write verified constant-time code by making several contributions to constant-time programming: We formalize a core language, **CONSTCORE**, built using constant-time low-level primitives; we formalize code transformations from our high-level language, **CONSTANC**, to our core language; we build a compiler for **CONSTANC** that produces constant-time programs; and finally, we implement cryptographic functions in **CONSTANC** and verify they run in constant time with *ct-verif* tool [3].

## 2. LANGUAGE DESIGN

The **CONSTANC** system is comprised of two distinct languages: **CONSTANC** proper, which end-users will develop programs in, and **CONSTCORE**, an internal language with

specific timing guarantees. Before diving into the languages, we briefly outline our threat model.

### 2.1 Threat Model

For **CONSTANC**, we assume an adversary has remote access to the machine running the code. The adversary has the ability to measure the amount of time it takes for **CONSTANC** code to run. We also assume the caller has not been compromised and that they are invoking **CONSTANC** functions with valid values (as opposed to null values).

### 2.2 Constanc

The **CONSTANC** language is the language we intend end-users of our system to program in. As **CONSTANC** is intended to replace low-level code, the language is designed to feel very similar to C, to make it more accessible for developers.

There are, however, some necessary restrictions on what can be expressed in **CONSTANC**. For example, recursion of any sort is disallowed. Loop constructs are restricted to for-loops, and loop bounds can only be constants. While these would be onerous restrictions for a general programming language, **CONSTANC** is intended for a very specific domain where these features of a language are generally unnecessary.

The language supports 32-bit signed integers, boolean values, and arrays of bytes. Most standard operations on these values are supported.

We do not prove timing guarantees directly in **CONSTANC**; instead, we show that the **CONSTCORE** language has the security guarantees we require, and transform all programs in **CONSTANC** to functionally equivalent programs in **CONSTCORE**.

The full grammar of **CONSTANC** can be found in Appendix A.3.

### 2.3 ConstCore

The **CONSTCORE** language is a **WHILE**-like language [?] with highly limited control flow. Notably, there are no conditional branching instructions, and the only loop construct is a for loop with static loop bounds. The type system is also more restrictive than that of **CONSTANC**: we support only 32-bit signed integers and byte arrays.

Comparison operators in **CONSTCORE** evaluate integer-width bitmasks of either all high bits (if the comparison is

$$\begin{array}{c}
\text{STT-VAR-ASSIGN} \\
\frac{\llbracket e_h \rrbracket_t = e \quad e' = \text{ctx} \& \text{rnset} \quad e'' = e \& e' \quad e''' = x \& (\sim e')}{\llbracket x := e_h \rrbracket_{\text{ctx}} = x := (e'' \mid e''')}
\end{array}
\qquad
\begin{array}{c}
\text{STT-IF} \\
\frac{\llbracket e_h \rrbracket_t = e \quad \llbracket s_{h1} \rrbracket_{(\text{ctx}' \& \text{ctx})} = s_1 \quad \llbracket s_{h2} \rrbracket_{(\text{ctx}' \& \text{ctx})} = s_2}{\llbracket \text{if}_h e_h \text{ then } s_{h1} \text{ else } s_{h2} \rrbracket_{\text{ctx}} = \text{def ctx}' := e; s_1; \text{ctx}' := (\sim \text{ctx}'); s_2}
\end{array}$$

$$\begin{array}{c}
\text{STT-RET} \\
\frac{\llbracket e_h \rrbracket_t = e \quad e' = e \& (\text{ctx} \& \text{rnset})}{\llbracket \text{return}_h e_h \rrbracket_{\text{ctx}} = \text{rval} := (e' \mid \text{rval}); \text{rnset} := (\text{rnset} \& (\sim \text{ctx}))}
\end{array}$$

Figure 1: Excerpt of semantic transformations from CONSTANC to CONSTCORE. Full rule set can be found in Appendix A.4.

true) or all low bits (if false). The resulting bitmasks are then typically used to mask expressions that depend on the conditional result. For example, if a variable  $x$  was to be incremented by 3 only if the variable  $y$  was greater than 5, we could express that as the following statement:

$$x := x + ((y > 5) \& 3)$$

The result of the expression  $y > 5$  will either be 0b1111... if true or 0b0000... if false. The  $\&$  operator performs a bitwise masking operation, either leaving the value 3 as-is (if the bitmask was all 1s) or zeroing it out (if the bitmask was all 0s). Thus  $x$  gets incremented only if the condition holds.

To show the constant-time nature of CONSTCORE, we adopt the *program-transcript security model* from Molnar et al. [12], keeping a log of executed instructions as part of our small-step semantics (see Appendix A). We then show that every function in CONSTCORE has the following property: For every function  $f$ , there exists a transcript  $f_\kappa$  such that for any input  $x_1, \dots, x_n$  to  $f$  and current transcript  $\kappa$ , the transcript  $\kappa'$  when  $f$  returns is exactly  $\kappa + f_\kappa$ . That is to say, the program path through any function in CONSTCORE does not depend on the inputs to the function.

However, even if a function always follows the same program transcript, it might still take varying amounts of real-world time, as physical instructions themselves may have timing variations depending on input registers [7]. Our compiler currently targets LLVM IR, but with Intel x86 assembly in mind; it is currently difficult which instructions in this architecture are truly constant time without support from Intel. However, certain instructions, such as integer division (`idiv`), are known to have timing variations based on the input values [6]. Our mitigation strategy is to restrict ourselves to instructions that, to the best of our knowledge, run in constant time with respect to arguments. These include basic arithmetic (excluding division and modulo) and comparison operators, as well as simple loads, stores, and calls. The constant-time nature of compiled CONSTCORE is thus reduced to the timing properties of the set of chosen instructions.

## 2.4 Transformations

If we can transform any program in CONSTANC to an equivalent program in CONSTCORE, then we can compile any

CONSTANC program to a program with the timing guarantees shown in CONSTCORE.

Since the inputs and outputs of both CONSTCORE and CONSTANC are restricted to function parameters, return values, and memory access, we simply need to show that for every function  $f$  in CONSTANC, the corresponding transformed function  $f_{\text{core}}$  returns an equivalent value and sets equivalent memory values when given equivalent arguments and starting memory state.

We ensure this equivalence by keeping a “context”, which is a bitmask representing the control flow of the function at the current statement. This bitmask is either *high* (all 1s) or *low* (all 0s). Any variable assignment in CONSTANC is transformed via the semantic transformation rule STT-VAR-ASSIGN shown in Figure 1, where  $\text{ctx}$  is the context bitmask and  $\&$ ,  $\mid$ ,  $\sim$  represent bitwise *and*, *or*, and *not* operations respectively. The  $\text{rnset}$  variable is an additional bitmask used for tracking early function return and is described below. With this transformation, variables are only updated to new values if the context at the time of execution indicates that the original program control flow would have made it to that statement.

Conditional branches are transformed by executing the statements in both branches. However, before each block is executed, the context bitmask is updated with the branch condition. Thus the statement is transformed via the rule STT-IF in Figure 1, where  $\langle s_1 \rangle$  and  $\langle s_2 \rangle$  are the transformations of  $s_1$  and  $s_2$ , and  $b$  is a fresh temporary variable for each instance of a branching statement. This ensures that nested conditionals still function as expected.

Return statements in CONSTANC are dealt with by constructing two additional variables for every function:  $\text{rval}$  (initialized low) and  $\text{rnset}$  (initialized high). A return statement in CONSTANC is translated to an assignment to  $\text{rval}$ , gated by the context as above. Additionally,  $\text{rnset}$  (“return value not set”) is updated to reflect the current return status. In this way,  $\text{rnset}$  remains high until a “return statement” is executed under an active control flow path, at which point it is set low and remains low for the rest of the function. Since all variable assignments are gated with  $\text{rnset}$  in addition to the context, no further variable assignments will cause updates.

Label	Supported Operations
Types	Int/Bool/ByteArr
Statements	VarDec/Assign/ArrAssign/If/For/Return
Expressions	VarExp/ArrExp/Unop/Binop/Primitive/CallExp
Unary Op	Bitwise Not
Binary Op	Plus/Minus/GT/Bitwise And/Bitwise Or

Figure 2: **Supported Language**—We show the different types, statements, expressions, and operators our language supports.

The formal transformation can be seen in the rule STT-RET in Figure 1.

### 3. COMPILER

We implemented a compiler for the CONSTANC language in OCaml, targeting LLVM bytecode. As syntax design is largely unimportant, we left the lexer and parser for future work. This allowed for fast iteration on the AST with minimal code changes. There are four main parts of the CONSTANC compiler: the *driver*, *type system*, *transformer*, and *IR generator*.

**Driver.** The driver controls the compilation process and is the interface between the developer and the compiler. It is a standard compiler driver, so we will omit all other details about it.

**Type System.** Although CONSTANC is a statically typed language, the type system is rather primitive: the only types are booleans, ints, and byte arrays. We guarantee type safety by type checking the CONSTANC AST. Types are checked again during IR generation, as LLVM IR is also typed. At the moment, we do not have support for structs or other record types; we leave this for future work.

CONSTANC supports a *for* loop, but ensures that it runs a constant number of cycles. Furthermore, CONSTANC only allows byte arrays to be indexed by constants or loop indices. The type checker ensures this property, guaranteeing that array accesses do not depend on secret values. The type checker also prevents out-of-bounds errors by checking the index against the byte array size, which can be done at compile time.

**Transformer.** The transformer converts the CONSTANC AST to CONSTCORE following the rules formally defined in Appendix A.4. Since CONSTCORE is built using known constant-time primitives, we can generate our LLVM IR using this language, ensuring our output runs in constant time.

**IR Generator.** The IR generator takes a CONSTCORE program and produces LLVM IR. The primitives used in the IR are critical to the resulting code running in constant time. To the best of our knowledge, all of the allowed operations run in constant time with 32 bit integers.

### 4. EVALUATION

We have implemented functions from OpenSSL in our language, to show that our system can be practical. We implemented `ssl3_cbc_remove_padding.c`. Our comparison shows up in Section 4.

**ssl3\_cbc\_remove\_padding** We implemented the `ssl3_cbc_remove_padding` in our language to show that more implementations are possible. Since our language is not fully fleshed out with all the desired features, we ran into a few problems when replicating this function. The original function utilizes structs — instead, we broke up the structs into individual variables, and any mutable objects that needed to be modified were passed in and modified in a byte array. Since we do not support memory referencing or public labels, we had to hardcode the size of the arrays. Moreover, we do not support unsigned types in our language, so wherever an unsigned was used in the original function, we used an int in our code.

We were not able to evaluate our implementation due to a variety of reasons.

[3]

Moreover, we were not able to perform microbenchmarks on this function because it would not have been fair. We only implemented one function that cannot stand on its own — in order to truly test this, we would like to implement the functions needed to encrypt and decrypt, so that we can test the function in its full form.

### 5. FUTURE WORK

Our goal for this quarter was to create a few implementations in our language to show that this concept is feasible and worth further research. As such, we have a plethora of future work that we would like to explore in the months to follow. The simplest to tackle is the creation of a syntax for our language — currently we must write programs directly in the AST. Having a corresponding syntax is key to stabilizing our language.

Another useful feature will be the concept of public and private labels on data. Our language currently treats all values as private — we would like to augment it to be able to relax restrictions on data that does not need protection. Along these lines, we also would like to expand the notion of our side channel safety to domains beyond timing, such as memory safety and prevention against caching side channels.

Finally, an important goal of this project is to determine a method of usability testing for programming languages. A huge impediment in this task is that there are currently no meaningful ways of testing programming languages at scale — it is not a task that one can easily part out, such as with Amazon Mechanical Turk [1]. One needs to be able to find a key group of users and be able to test in-depth at a large scale in order to get proper data. This is an open question that we are hoping to address in the following months.

### 6. RELATED WORK

The security community has been aware of side channel attacks for over 40 years [4, 8, 9, 18]. Recently, side channels

have been shown to leak information via power analysis [16]. Furthermore, timing side channels have been shown to leak information from the cache [10] or other aspects of processor architecture [17].

Due to the rise in the number of side channel attacks, there has much work in creating defenses. [11, 13] use information flow control to prevent information leaks on encrypted data. [14] uses instruction-based scheduling to address cache based timing attacks. [5, 15] address timing attacks in LIO, a concurrent information flow control system for Haskell. Most closely related to CONSTANC, [12] uses C to C code transformations to avoid timing side channels.

## 7. CONCLUSION

In this paper, we introduced CONSTANC, a high level language that can be used for constant-time programming. We formalized the semantics of CONSTANC and CONSTCORE and the transformation from CONSTANC to CONSTCORE. With this formalization, we built a CONSTANC compiler which we successfully used to write various cryptographic functions. Our evaluation is limited to the success of writing these functions. In the future, we plan to evaluate CONSTANC based on performance and usability. Furthermore, we plan to create a syntax for CONSTANC and implement a lexer and parser for the compiler. Finally, we plan to add public labels to the type system allowing for more flexibility for the developer and better performance.

## 8. REFERENCES

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## APPENDIX

### A. SEMANTICS OF `CONSTANC`

#### A.1 `CONSTCORE` Grammar

$\ominus$	$::=$ $  \sim$	unary operations bitwise not
$\oplus$	$::=$ $  +$ $  -$ $  *$ $  \ll$ $  \gg$ $  \&$ $   $ $  ==_s$ $  !=_s$ $  >_s$ $  <_s$ $  >=_s$ $  <=_s$	binary operations      bitwise and bitwise or equals (sign extended)
$e$	$::=$ $  \text{TRUE}$ $  \text{FALSE}$ $  c$ $  a$ $  x$ $  a[e]$ $  \ominus e$ $  e_1 \oplus e_2$ $  f(e_1, \dots, e_n)$	expressions bitmask true (0b1111...) bitmask false (0b0000...) numeric value bytearray variable array access unary operation binary operation function application
$s$	$::=$ $  \text{skip}$ $  s_1; s_2$ $  \text{def } x := e$ $  \text{a def } x := a$ $  x := e$ $  a[e_1] := e_2$ $  \text{for } x \text{ from } v_1 \text{ to } v_2 : s$	statements skip sequence variable declaration array declaration variable assignment array assignment for loop
$fval$	$::=$ $  (x_1, \dots, x_n) : s @ e$	function spec
$fndef$	$::=$ $  \text{f def } fval$	function definition
$program$	$::=$ $  fndef_1; \dots; fndef_n; \text{expose } fndef$	program list of fdefs

## A.2 CONSTCORE Small-Step Semantics

$\Lambda$	$::=$	function store
	$\emptyset_\Lambda$	empty function store
	$\Lambda[f \mapsto fval]$	define function
$\Gamma$	$::=$	global memory
	$\emptyset_\Gamma$	
	$\Gamma[a \mapsto []]$	new array
	$\Gamma[a \mapsto \Gamma(a)[v_1 \mapsto v_2]]$	array update
$\mu$	$::=$	local memory
	$\emptyset_\mu$	empty memory
	$\mu[x \mapsto v]$	add/update variable
	$\mu_1 \triangleright \mu_2$	push stack frame
$\{\sigma\}$	$::=$	variable substitution
	$\{x_1/v_1, \dots, x_k/v_k\}$	

$$\boxed{\{\Lambda, \Gamma, \mu, \kappa\} e \longrightarrow \{\Lambda', \Gamma', \mu', \kappa'\} e'} \quad (e \text{ reduces to } e')$$

$$\frac{\text{EXR-VAR} \quad \mu = \mu'[x \mapsto v] \quad \kappa' = \kappa \triangleright \mathbf{load}}{\{\Lambda, \Gamma, \mu, \kappa\} x \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} v}$$

$$\frac{\text{EXR-ARR-GET-EXPR} \quad \{\Lambda, \Gamma, \mu, \kappa\} e \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} e'}{\{\Lambda, \Gamma, \mu, \kappa\} a[e] \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} a[e']}$$

$$\frac{\text{EXR-ARR-GET-VAL} \quad v' = \Gamma(a)[v] \quad \kappa' = \kappa \triangleright \mathbf{load}}{\{\Lambda, \Gamma, \mu, \kappa\} a[v] \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} v'}$$

$$\frac{\text{EXR-UNOP-EXPR} \quad \{\Lambda, \Gamma, \mu, \kappa\} e \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} e'}{\{\Lambda, \Gamma, \mu, \kappa\} \ominus e \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} \ominus e'}$$

$$\frac{\text{EXR-UNOP-VAL} \quad v' \equiv \llbracket \ominus v \rrbracket \quad \kappa' = \kappa \triangleright \ominus}{\{\Lambda, \Gamma, \mu, \kappa\} \ominus v \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} v'}$$

$$\frac{\text{EXR-BINOP-L} \quad \{\Lambda, \Gamma, \mu, \kappa\} e_1 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} e'_1}{\{\Lambda, \Gamma, \mu, \kappa\} e_1 \oplus e_2 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} e'_1 \oplus e_2}$$

$$\frac{\text{EXR-BINOP-R} \quad \{\Lambda, \Gamma, \mu, \kappa\} e_2 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} e'_2}{\{\Lambda, \Gamma, \mu, \kappa\} v \oplus e_2 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} v \oplus e'_2}$$

$$\frac{\text{EXR-BINOP-VAL} \quad v_3 \equiv \llbracket v_1 \oplus v_2 \rrbracket \quad \kappa' = \kappa \triangleright \oplus}{\{\Lambda, \Gamma, \mu, \kappa\} v_1 \oplus v_2 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} v_3}$$

$$\frac{\text{EXR-SUBST-EMPTY}}{\{\Lambda, \Gamma, \mu, \kappa\} \{\} e \longrightarrow \{\Lambda, \Gamma, \mu, \kappa\} e}$$

$$\frac{\text{EXR-SUBST-EXPR} \quad \{\Lambda, \Gamma, \mu, \kappa\} e \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} e'}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} e \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} \{\sigma\} e'}$$

$$\frac{\text{EXR-SUBST-VAR} \quad \kappa' = \kappa \triangleright \mathbf{load}}{\{\Lambda, \Gamma, \mu, \kappa\} \{x_1/v_1, \dots, x_k/v_k\} x_i \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} v_i}$$

$$\frac{\text{EXR-SUBST-NO-VAR}}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} x \longrightarrow \{\Lambda, \Gamma, \mu, \kappa\} x}$$

$$\frac{\text{EXR-SUBST-VAL}}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} v \longrightarrow \{\Lambda, \Gamma, \mu, \kappa\} v}$$

$$\frac{\text{EXR-FN-EXPR} \quad \{\Lambda, \Gamma, \mu, \kappa\} e_1 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} e'_1}{\{\Lambda, \Gamma, \mu, \kappa\} f(v_1, \dots, v_k, e_1, e_2, \dots, e_n) \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} f(v_1, \dots, v_k, e'_1, e_2, \dots, e_n)}$$

$$\frac{\text{EXR-FN-SUBST} \quad \kappa' = \kappa \triangleright \mathbf{store}}{\{\Lambda, \Gamma, \mu, \kappa\} f(x'_1/v'_1, \dots, x'_k/v'_k, v_1, v_2, \dots, v_n) \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} f(x'_1/v'_1, \dots, x'_k/v'_k, x_1/v_1, v_2, \dots, v_n)}$$

$$\frac{\text{EXR-FN-CALL} \quad \Lambda = \Lambda'[f \mapsto (x_1, \dots, x_k) : s @ e] \quad \mu' = \mu \triangleright \emptyset_\mu \quad \kappa' = \kappa \triangleright f}{\{\Lambda, \Gamma, \mu, \kappa\} f(x_1/v_1, \dots, x_k/v_k) \longrightarrow \{\Lambda, \Gamma, \mu', \kappa'\} \{x_1/v_1, \dots, x_k/v_k\} s @ e}$$

EXR-SKIP-EXPR

$$\frac{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} e \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} e'}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} \mathbf{skip} @e \longrightarrow \{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} \mathbf{skip} @e'}$$

EXR-SKIP-VAL

$$\frac{\mu = \mu_1 \triangleright \mu_2 \quad \kappa' = \kappa \triangleright \mathbf{ret}}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} \mathbf{skip} @v \longrightarrow \{\Lambda, \Gamma, \mu_1, \kappa'\} v}$$

EXR-SEQ

$$\frac{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} s_1 @e_0 \longrightarrow \{\Lambda, \Gamma, \mu', \kappa'\} \{\sigma'\} s'_1 @e_0}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} s_1; s_2 @e_0 \longrightarrow \{\Lambda, \Gamma, \mu', \kappa'\} \{\sigma'\} s'_1; s_2 @e_0}$$

EXR-SEQ-SKIP

$$\frac{}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} \mathbf{skip}; s @e_0 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} s @e_0}$$

EXR-DEF-EXPR

$$\frac{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} e \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} e'}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} \mathbf{def} x := e @e_0 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} \{\sigma\} \mathbf{def} x := e' @e_0}$$

EXR-DEF-VAL

$$\frac{\mu' = \mu[x \mapsto v] \quad \kappa' = \kappa \triangleright \mathbf{store}}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} \mathbf{def} x := v @e_0 \longrightarrow \{\Lambda, \Gamma, \mu', \kappa'\} \{\sigma\} \mathbf{skip} @e_0}$$

EXR-DEF-ARR

$$\frac{\Gamma' = \Gamma[a \mapsto []] \quad \mu' = \mu[x \mapsto a] \quad \kappa' = \kappa \triangleright \mathbf{store}}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} \mathbf{adef} x := a @e_0 \longrightarrow \{\Lambda, \Gamma', \mu', \kappa'\} \{\sigma\} \mathbf{skip} @e_0}$$

EXR-ASSIGN-EXPR

$$\frac{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} e \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} e'}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} x := e @e_0 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} \{\sigma\} x := e' @e_0}$$

EXR-ASSIGN-VAL

$$\frac{\mu' = \mu[x \mapsto v] \quad \kappa' = \kappa \triangleright \mathbf{store}}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} x := v @e_0 \longrightarrow \{\Lambda, \Gamma, \mu', \kappa'\} \{\sigma\} \mathbf{skip} @e_0}$$

EXR-ARR-ASSIGN-EXPR-L

$$\frac{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} e_1 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} e'_1}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} a[e_1] := e_2 @e_0 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} \{\sigma\} a[e'_1] := e_2 @e_0}$$

EXR-ARR-ASSIGN-EXPR-R

$$\frac{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} e_2 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} e'_2}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} a[v_1] := e_2 @e_0 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} \{\sigma\} a[v_1] := e'_2 @e_0}$$

EXR-ARR-ASSIGN-VAL

$$\frac{\Gamma' = \Gamma[a \mapsto \Gamma(a)[v_1 \mapsto v_2]] \quad \kappa' = \kappa \triangleright \mathbf{store}}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} a[v_1] := v_2 @e_0 \longrightarrow \{\Lambda, \Gamma', \mu, \kappa'\} \{\sigma\} \mathbf{skip} @e_0}$$

EXR-FOR

$$\frac{v_1 < v_2 \quad v'_1 = v_1 + 1 \quad \kappa' = \kappa \triangleright \mathbf{store}}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} \mathbf{for} x \mathbf{from} v_1 \mathbf{to} v_2 : s @e_0 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} \{\sigma\} (\{x/v_1\} s); \mathbf{for} x \mathbf{from} v'_1 \mathbf{to} v_2 : s @e_0}$$

EXR-ADD-SUBST

$$\frac{\begin{array}{l} \{\sigma_1\} \cap \{\sigma_2\} = \{\} \\ \{\sigma_3\} = \{\sigma_1\} \cup \{\sigma_2\} \end{array}}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma_1\} (\{\sigma_2\} s) @e_0 \longrightarrow \{\Lambda, \Gamma, \mu, \kappa'\} \{\sigma_3\} s @e_0}$$

EXR-FOR-BASE

$$\frac{v_1 = v_2}{\{\Lambda, \Gamma, \mu, \kappa\} \{\sigma\} \mathbf{for} x \mathbf{from} v_2 \mathbf{to} v_2 : s @e_0 \longrightarrow \{\Lambda, \Gamma, \mu', \kappa\} \{\sigma\} \mathbf{skip} @e_0}$$

### A.3 CONSTANC Grammar

$\ominus_h$	$::=$ $ $ $!$ $ $ $\sim$	unary operations logical not bitwise not
$\oplus_h$	$::=$ $ $ $+$ $ $ $-$ $ $ $*$ $ $ $\ll$ $ $ $\gg$ $ $ $\&$ $ $ $\&\&$ $ $ $  $ $ $ $==$ $ $ $!=$ $ $ $>$ $ $ $<$ $ $ $>=$ $ $ $<=$	binary operations
$e_h$	$::=$ $ $ $b_h$ $ $ $c_h$ $ $ $a$ $ $ $x$ $ $ $a[e_h]$ $ $ $\ominus_h e_h$ $ $ $e_{h1} \oplus e_{h2}$ $ $ $f_h(e_{h1}, \dots, e_{hn})$	expressions boolean value numeric value bytearray variable array access unary operation binary operation function application
$s_h$	$::=$ $ $ <b>skip</b> <sub><math>h</math></sub> $ $ $s_{h1}; s_{h2}$ $ $ <b>def</b> <sub><math>h</math></sub> $x := e_h$ $ $ <b>adef</b> <sub><math>h</math></sub> $x := a$ $ $ $x := e_h$ $ $ $a[e_{h1}] := e_{h2}$ $ $ <b>for</b> <sub><math>h</math></sub> $x$ <b>from</b> $v_{h1}$ <b>to</b> $v_{h2}$ $ $ <b>if</b> <sub><math>h</math></sub> $e_h$ <b>then</b> $s_{h1}$ <b>else</b> $s_{h2}$ $ $ <b>return</b> <sub><math>h</math></sub> $e_h$	statements skip sequence variable declaration array declaration variable assignment array assignment for loop conditional branch return
$hfval$	$::=$ $ $ $(x_1, \dots, x_n) : s_h$	function spec
$hfndef$	$::=$ $ $ <b>fdef</b> <sub><math>h</math></sub> $f_h$ $hfval$	function definition
$hprogram$	$::=$ $ $ $hfndef_1; \dots; hfndef_n; \textbf{expose}$ $hfndef$	program list of fdefs



#### A.4 Transformations from CONSTANC to CONSTCORE

$$\boxed{[\![\Theta_h]\!]_t = \Theta} \quad (\Theta_h \text{ is transformed to } \Theta)$$

UNOPT-LNOT	UNOPT-UNOP
$\overline{[\![!]\!]}_t = \sim$	$\overline{[\![\ominus h]\!]}_t = \ominus$

$$\boxed{[\oplus_h]_t = \oplus} \quad (\oplus_h \text{ is transformed to } \oplus)$$

BINOPT-LAND	BINOPT-LOR	BINOPT-EQ	BINOPT-NEQ	BINOPT-GT	BINOPT-LT	BINOPT-GTE
$\overline{\llbracket \&\& \rrbracket_t = \&}$	$\overline{\llbracket     \rrbracket_t =  }$	$\overline{\llbracket == \rrbracket_t = ==_s}$	$\overline{\llbracket != \rrbracket_t = !=_s}$	$\overline{\llbracket > \rrbracket_t = >_s}$	$\overline{\llbracket < \rrbracket_t = <_s}$	$\overline{\llbracket >= \rrbracket_t = >= _s}$
		BINOPT-LTE		BINOPT-BINOP		
		$\overline{\llbracket <= \rrbracket_t = <= _s}$		$\overline{\llbracket \oplus_h \rrbracket_t = \oplus}$		

$$\boxed{[e_h]_t = e} \quad (e_h \text{ is transformed to } e)$$

$\frac{\text{EXT-VAL}}{v \equiv \llbracket v_h \rrbracket_{int}} \quad \frac{}{\llbracket v_h \rrbracket_t = v}$	$\frac{\text{EXT-VAR}}{} \quad \frac{}{\llbracket x \rrbracket_t = x}$	$\frac{\text{EXT-ARR}}{} \quad \frac{}{\llbracket a \rrbracket_t = a}$	$\frac{\text{EXT-ARR-GET}}{\llbracket e_h \rrbracket_t = e} \quad \frac{}{\llbracket a[e_h] \rrbracket_t = a[e]}$	$\frac{\text{EXT-FN-CALL}}{\llbracket \mathbf{fdef}_h f_h hfval \rrbracket_t = \mathbf{fdef} f hfval} \quad \frac{}{\llbracket e_h \rrbracket_t = e_1 \quad \dots \quad \llbracket e_h \rrbracket_t = e_k} \quad \frac{}{\llbracket f_h(e_{h1}, \dots, e_{hk}) \rrbracket_t = f(e_1, \dots, e_k)}$
--	--	--	---	--

$$\boxed{\llbracket s_h \rrbracket_{ctx} = s} \quad (s_h \text{ is transformed to } s)$$

STT-SKIP	STT-SEQ	STT-VAR-DEC
$\frac{}{\llbracket \mathbf{skip}_h \rrbracket_{ctx} = \mathbf{skip}}$	$\frac{\llbracket s_{h1} \rrbracket_{ctx} = s_1 \quad \llbracket s_{h2} \rrbracket_{ctx} = s_2}{\llbracket s_{h1}; s_{h2} \rrbracket_{ctx} = s_1; s_2}$	$\frac{\llbracket e_h \rrbracket_t = e}{\llbracket \mathbf{def}_h x := e_h \rrbracket_{ctx} = \mathbf{def} x := e}$
STT-ARR-DEC	STT-VAR-ASSIGN	STT-ARR-ASSIGN
$\frac{}{\llbracket \mathbf{adef}_h x := a \rrbracket_{ctx} = \mathbf{adef} x := a}$	$\frac{\llbracket e_h \rrbracket_t = e \quad e' = ctx \& mset \quad e'' = e \& e' \quad e''' = x \& (\sim e')}{\llbracket x := e_h \rrbracket_{ctx} = x := (e'' \mid e''')}$	$\frac{\llbracket e_{h1} \rrbracket_t = e_1 \quad \llbracket e_{h2} \rrbracket_t = e_2 \quad e' = ctx \& mset \quad e'' = e_2 \& e' \quad e''' = a[e_1] \& (\sim e')}{\llbracket a[e_{h1}] := e_{h2} \rrbracket_{ctx} = a[e_1] := (e'' \mid e''')}$

STT-FOR	STT-IF
$\frac{\begin{array}{l} \llbracket v_{h1} \rrbracket_t = v_1 \\ \llbracket v_{h2} \rrbracket_t = v_2 \quad \llbracket s_h \rrbracket_{ctx} = s \end{array}}{\llbracket \text{for } x \text{ from } v_{h1} \text{ to } v_{h2} \rrbracket_{ctx} = \text{for } x \text{ from } v_1 \text{ to } v_2 : s}$	$\frac{\begin{array}{l} \llbracket e_h \rrbracket_t = e \quad \llbracket s_{h1} \rrbracket_{(ctx' \& ctx)} = s_1 \\ \llbracket s_{h2} \rrbracket_{(ctx' \& ctx)} = s_2 \end{array}}{\llbracket \text{if } e_h \text{ then } s_{h1} \text{ else } s_{h2} \rrbracket_{ctx} = \text{def } ctx' := e; s_1; ctx' := (\sim ctx'); s_2}$

$$\frac{\text{STT-RET} \quad \llbracket e_h \rrbracket_t = e \quad e' = e \& (ctx \& rnsset)}{\llbracket \mathbf{return}_h e_h \rrbracket_{ctx} = rval := (e' \mid rval); rnsset := (rnsset \& (\sim ctx))}$$

$$\boxed{\llbracket hfn\!def \rrbracket_t = fn\!def} \qquad (hfn\!def \text{ is transformed to } fn\!def)$$

$$\frac{\llbracket S_h \rrbracket_{\text{TRUE}} = s}{\llbracket \mathbf{fdef}_h f_h(x_1, \dots, x_k) : s_h \rrbracket_t = \mathbf{fdef} f(x_1, \dots, x_k) : \mathbf{def} rval := \text{FALSE}; \mathbf{def} rnsel := \text{TRUE}; s @ rval}$$