SPEC® CINT2006 Result

Tyan

Tyan Thunder KKQS Pro (S4882)

SPECint®2006 = Not Run

SPECint_base2006 = 0.00

CPU2006 license: 0		**Test date:**	Jul-2021
Test sponsor:	Turbo Computers	**Hardware Availability:**	Dec-9999
Tested by:	Turbo Computers	**Software Availability:**	Dec-9999

400.perlbench

401.bzip2

403.gcc

429.mcf

445.gobmk

456.hmmer

458.sjeng

462.libquantum 65.7

464.h264ref

471.omnetpp

473.astar

483.xalancbmk

Hardware

CPU Name:	AMD Opteron 256
CPU Characteristics:	
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	1 core, 1 chip, 1 core/chip
CPU(s) orderable:	1 chip
Primary Cache:	64 KB I + 64 KB D on chip per chip
Secondary Cache:	1 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2 x 1GB DDR333 CL2.5)
Disk Subsystem:	SATA
Other Hardware:	--

Software

Operating System:	SUSE Linux 10.1 (for i386)
Compiler:	gcc , g++ & gfortran 4.2 Snapshot 20060715 (for i386)
Auto Parallel:	No
File System:	ext3
System State:	runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	None

Errors

'reportable' flag not set during run
471.omnetpp (base) did not have enough runs!
456.hmmer (base) did not have enough runs!

Continued on next page

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Errors (Continued)

445.gobmk (base) did not have enough runs!
458.sjeng (base) did not have enough runs!
429.mcf (base) did not have enough runs!
473.astar (base) did not have enough runs!
483.xalancbmk (base) did not have enough runs!
400.perlbench (base) did not have enough runs!
464.h264ref (base) did not have enough runs!
462.libquantum (base) did not have enough runs!
401.bzip2 (base) did not have enough runs!
403.gcc (base) did not have enough runs!
471.omnetpp (base) had invalid runs!
456.hmmer (base) had invalid runs!
458.sjeng (base) had invalid runs!
473.astar (base) had invalid runs!
483.xalancbmk (base) had invalid runs!
Run of 456.hmmer (base) was not valid; status is CE
Run of 458.sjeng (base) was not valid; status is RE
Run of 471.omnetpp (base) was not valid; status is RE
Run of 473.astar (base) was not valid; status is CE
Run of 483.xalancbmk (base) was not valid; status is CE
Unknown flags were used! See
    http://www.spec.org/cpu2006/Docs/runspec.html#flagsurl
for information about how to get rid of this error.

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench												
401.bzip2												
403.gcc												
429.mcf												
445.gobmk												
456.hmmer	0.00	0.00										
458.sjeng	0.0871139000											
462.libquantum	**315**	**65.7**										
464.h264ref												
471.omnetpp	0.0200312000											
473.astar	0.00	0.00										
483.xalancbmk	0.00	0.00										

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

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CPU2006 license: 0	**Test date:** Jul-2021
Test sponsor: Turbo Computers	**Hardware Availability:** Dec-9999
Tested by: Turbo Computers	**Software Availability:** Dec-9999

General Notes

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
C base flags: -O2
C++ base flags: -O2
Fortran base flags: -O2
```

Base Unknown Flags

458.sjeng: "/home/shr/Code/zerocost/Sandboxing_NaCl/native_client/toolchain/linux_x86/pnacl_newlib_raw/bin/clang -m32 -Wl,--allow-multiple-definition -DNACL32BUILD -target i686-nacl" (in CC)

"/home/shr/Code/zerocost/Sandboxing_NaCl/native_client/toolchain/linux_x86/pnacl_newlib_raw/bin/clang -m32 -Wl,--allow-multiple-definition -DNACL32BUILD -target i686-nacl" (in

"-O2" (in COPTIMIZE)

462.libquantum: "/home/shr/Code/zerocost/Sandboxing_NaCl/native_client/toolchain/linux_x86/pnacl_newlib_raw/bin/clang -m32 -Wl,--allow-multiple-definition -DNACL32BUILD -target i686-nacl" (in CC)

"/home/shr/Code/zerocost/Sandboxing_NaCl/native_client/toolchain/linux_x86/pnacl_newlib_raw/bin/clang -m32 -Wl,--allow-multiple-definition -DNACL32BUILD -target i686-nacl" (in

"-O2" (in COPTIMIZE)

471.omnetpp: "/home/shr/Code/zerocost/Sandboxing_NaCl/native_client/toolchain/linux_x86/pnacl_newlib_raw/bin/clang++ -m32 -Wl,--allow-multiple-definition -DNACL32BUILD -target i686-nacl" (in CXX)

"/home/shr/Code/zerocost/Sandboxing_NaCl/native_client/toolchain/linux_x86/pnacl_newlib_raw/bin/clang++ -m32 -Wl,--allow-multiple-definition -DNACL32BUILD -target i686-nacl" (in

"-O2" (in CXXOPTIMIZE)

Base Portability Flags

462.libquantum: -DSPEC_CPU_LINUX

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For other inquiries, please contact webmaster@spec.org.

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Page 3