Project Configuration Report wifi_http_client_soc

Target Device:

Target Part: SIWG917M111MGTBA

Boards:

- SiWG917 Dev Kit Board (BRD2605A Rev A02)

Target SDK

Simplicity SDK Suite v2025.6.1: Bluetooth 10.1.0, Bluetooth Mesh 9.0.1, Connect 4.1.1, EmberZNet 8.2.1.0, Micrium OS Kernel 5.18.01, OpenThread 2.7.1.0 (GitHub-fb0446f53), Platform 5.2.1.0, RAIL 2.19.1, USB 1.5.1.0, Wi-Fi SDK 3.5.1, Wi-SUN 2.7.0, Z-Wave SDK 7.24.1.0 SDK Version: 2025.6.1

Extensions

WiSeConnect 3 3.5.1

Project Generators

• Simplicity IDE Project

Installed Software Components:

- 802.15.4
 - Radio

Radio Priority Configurator

Configurable: true

Configs: priority_config.prioconf

- Board
 - Radio Board

BRD2605A

Configurable: false

- Device
 - Si91x
 - MCU
 - Common

Debug UC

Configurable: true

Configs: sl_si91x_debug_uc_1_config.h, sl_si91x_debug_uc_2_config.h

Debug Unit

Sources: rsi_debug.c Configurable: false

System Calls

Sources: syscalls.c Configurable: false

Device Part

• SIWG917

SIWG917M111MGTBA

Sources: system_si91x.c, startup_si91x.c

Configurable: false

Hardware

Si70xx Humidity and Temperature Sensor

Sources: sl_si91x_si70xx.c

Configurable: true

Configs: sl_si91x_si70xx_config.h

Peripheral

I2C

Configurable: true

Configs: sl_si91x_i2c_init_i2c2_config.h, sl_si91x_i2c_init_i2c0_config.h

• Service

IO Stream: SI91X Debug

Sources: sl_si91x_iostream_debug.c, sl_si91x_iostream_swo_itm_8.c

Configurable: false

IO Stream: SI91X VUART

Sources: sl_si91x_iostream_vuart.c, sl_si91x_iostream_swo_itm_8.c

Configurable: true

Configs: sl_si91x_iostream_vuart_config.h

Memory Configuration

Default RAM Memory Configuration

Configurable: false

Wireless

• Buffer Manager

Memory Pool buffers With Quota

Sources: mem_pool_buffer_quota.c

Configurable: false

Platform

- Security

- Mbed TLS
 - Utilities

Debug Helpers

Sources: debug.c Configurable: false

• Service

HTTP Server

Sources: sl_http_server.c

Configurable: false

MDNS

Sources: sl_mdns.c Configurable: false

• Services

- IO Stream

IO Stream

Sources: sl_iostream.c Configurable: false

Pins:

Peripherals:

Peripheral	Software Component	Custom Peripheral Name
I2C0	I2C (i2c0)	
I2C1		
ULP_I2C	I2C (i2c2)	
USART0		
UART1		
QEI		
OPAMP1		
OPAMP2		
OPAMP3		
ULP_UART		
SSI_MASTER		
SSI_SLAVE		
HSPI_SECONDARY		
ULP_SSI		
GSPI_MASTER		
I2S0		
ULP_I2S		
SCT		
PWM_CH0		
PWM_CH1		
PWM_CH2		
PWM_CH3		
PWM		
SDC_CH1		

SDC_CH2 SDC_CH3 SDC_CH4 ADC_CH1 ADC_CH2 ADC_CH3 ADC_CH3 ADC_CH4 ADC_CH5 ADC_CH6 ADC_CH7 ADC_CH8 ADC_CH9 ADC_CH10 ADC_CH11 ADC_CH12 ADC_CH13 ADC_CH14 ADC_CH16 COMP1 COMP2 DAC0 DAC1 SYSRTC UULP_VBAT_GPIO		
SDC_CH4 ADC_CH1 ADC_CH2 ADC_CH3 ADC_CH3 ADC_CH4 ADC_CH6 ADC_CH6 ADC_CH7 ADC_CH7 ADC_CH8 ADC_CH9 ADC_CH10 ADC_CH10 ADC_CH11 ADC_CH12 ADC_CH12 ADC_CH13 ADC_CH14 ADC_CH16 ADC_CH16 COMP1 COMP1 DAC0 DAC1 SYSRTC UULP_VBAT_GPIO Image: Company of the company o	SDC_CH2	
ADC_CH1 ADC_CH2 ADC_CH3 ADC_CH4 ADC_CH5 ADC_CH6 ADC_CH6 ADC_CH7 ADC_CH8 ADC_CH9 ADC_CH10 ADC_CH10 ADC_CH11 ADC_CH11 ADC_CH12 ADC_CH12 ADC_CH13 ADC_CH14 ADC_CH16 COMP1 COMP2 DACO DAC1 SYSRTC UULP_VBAT_GPIO	SDC_CH3	
ADC_CH2 ADC_CH3 ADC_CH4 ADC_CH5 ADC_CH6 ADC_CH6 ADC_CH7 ADC_CH8 ADC_CH9 ADC_CH10 ADC_CH11 ADC_CH11 ADC_CH12 ADC_CH12 ADC_CH13 ADC_CH14 ADC_CH15 ADC_CH16 COMP1 COMP1 COMP2 DAC0 DAC1 SYSRTC UULP_VBAT_GPIO	SDC_CH4	
ADC_CH3 ADC_CH4 ADC_CH5 ADC_CH6 ADC_CH7 ADC_CH8 ADC_CH9 ADC_CH9 ADC_CH10 ADC_CH11 ADC_CH12 ADC_CH12 ADC_CH13 ADC_CH14 ADC_CH15 ADC_CH16 COMP1 COMP2 DAC0 DAC1 SYSRTC UULP_VBAT_GPIO	ADC_CH1	
ADC_CH4 ADC_CH5 ADC_CH6 ADC_CH7 ADC_CH8 ADC_CH9 ADC_CH10 ADC_CH11 ADC_CH12 ADC_CH12 ADC_CH13 ADC_CH14 ADC_CH15 ADC_CH16 COMP1 COMP2 DAC0 DAC1 SYSRTC UULP_VBAT_GPIO	ADC_CH2	
ADC_CH5 ADC_CH6 ADC_CH7 ADC_CH8 ADC_CH9 ADC_CH10 ADC_CH11 ADC_CH12 ADC_CH13 ADC_CH14 ADC_CH15 ADC_CH16 COMP1 COMP2 DAC0 DAC1 SYSRTC UULP_VBAT_GPIO	ADC_CH3	
ADC_CH6 ADC_CH7 ADC_CH8 ADC_CH9 ADC_CH10 ADC_CH11 ADC_CH11 ADC_CH12 ADC_CH13 ADC_CH14 ADC_CH15 ADC_CH16 COMP1 COMP2 DAC0 DAC1 SYSRTC UULP_VBAT_GPIO	ADC_CH4	
ADC_CH7 ADC_CH8 ADC_CH9 ADC_CH10 ADC_CH11 ADC_CH12 ADC_CH13 ADC_CH14 ADC_CH15 ADC_CH16 COMP1 COMP2 DAC0 DAC1 SYSRTC UULP_VBAT_GPIO	ADC_CH5	
ADC_CH8 ADC_CH10 ADC_CH11 ADC_CH11 ADC_CH12 ADC_CH13 ADC_CH14 ADC_CH15 ADC_CH16 COMP1 COMP2 DAC0 DAC1 SYSRTC UULP_VBAT_GPIO	ADC_CH6	
ADC_CH9 ADC_CH10 ADC_CH11 ADC_CH12 ADC_CH13 ADC_CH14 ADC_CH15 ADC_CH16 COMP1 COMP2 DAC0 DAC1 SYSRTC UULP_VBAT_GPIO	ADC_CH7	
ADC_CH10 ADC_CH11 ADC_CH12 ADC_CH13 ADC_CH14 ADC_CH15 ADC_CH16 COMP1 COMP2 DAC0 DAC1 SYSRTC UULP_VBAT_GPIO	ADC_CH8	
ADC_CH11 ADC_CH12 ADC_CH13 ADC_CH14 ADC_CH15 ADC_CH16 COMP1 COMP2 DAC0 DAC1 SYSRTC UULP_VBAT_GPIO	ADC_CH9	
ADC_CH12 ADC_CH13 ADC_CH14 ADC_CH15 ADC_CH15 ADC_CH16 COMP1 COMP2 DAC0 DAC1 SYSRTC UULP_VBAT_GPIO	ADC_CH10	
ADC_CH13 ADC_CH14 ADC_CH15 ADC_CH16 COMP1 COMP2 DAC0 DAC1 SYSRTC UULP_VBAT_GPIO	ADC_CH11	
ADC_CH14 ADC_CH15 ADC_CH16 COMP1 COMP2 COMP2 DAC0 DAC1 SYSRTC UULP_VBAT_GPIO	ADC_CH12	
ADC_CH15 ADC_CH16 COMP1 COMP2 DAC0 DAC1 SYSRTC UULP_VBAT_GPIO	ADC_CH13	
ADC_CH16 COMP1 COMP2 COMP2 DAC0 COMP2 DAC1 COMP2 SYSRTC COMP2 UULP_VBAT_GPIO COMP2	ADC_CH14	
COMP1 COMP2 DAC0 COMP2 DAC1 COMP2 SYSRTC COMP2 UULP_VBAT_GPIO COMP2	ADC_CH15	
COMP2	ADC_CH16	
DAC0	COMP1	
DAC1 SYSRTC UULP_VBAT_GPIO	COMP2	
SYSRTC UULP_VBAT_GPIO UULP_VBAT_GPIO	DAC0	
UULP_VBAT_GPIO	DAC1	
	SYSRTC	
GPIO PIO	UULP_VBAT_GPIO	
	GPIO	