**MIDDLE EAST TECHNICAL UNIVERSITY**

**ELECTRICAL AND ELECTRONICS ENGINEERING DEPARTMENT**



**EE462 UTILIZATION OF ELECTRICAL ENERGY**

**EE464 STATIC POWER CONVERSION – II**

**SOFTWARE PROJECT**

**REPORT**

**Design of a SM-PMSM Variable Frequency Drive with Matlab/Simulink**

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# INTRODUCTION

In this project, we are asked to design a SM-PMSM Variable Frequency Drive using Matlab/Simulink. The available supply is a three-phase AC source (50 Hz, 400Vl-l) and the PM is a surface-mount motor.

The motor ratings of the surface mount PM synchronous machine (SM-PMSM);

* 𝑃𝑛𝑜𝑚𝑖𝑛𝑎𝑙=80 𝑘𝑊
* 𝑇𝑛𝑜𝑚𝑖𝑛𝑎𝑙=300 𝑁𝑚
* 𝑛𝑚𝑎𝑥 =7000 𝑟𝑝𝑚
* Pole number: p=8
* 𝐹𝑙𝑢𝑥 𝑙𝑖𝑛𝑘𝑎𝑔𝑒: 𝜆𝑃𝑀= 0.2 Vs (Wb-t)
* 𝐿𝑑=𝐿𝑞= 500 µH
* 𝐼𝑛𝑜𝑚𝑖𝑛𝑎𝑙= 250 𝐴 (peak)
* Phase resistance: 𝑅𝑠=50 mOhm
* Equivalent inertia of the system: 𝐽𝑒𝑞= 10 kg m2

3-phase full-bridge diode rectifier is connected to the grid. The 3-phase motor drive inverter is

connected to the diode rectifier as shown in Figure 1.

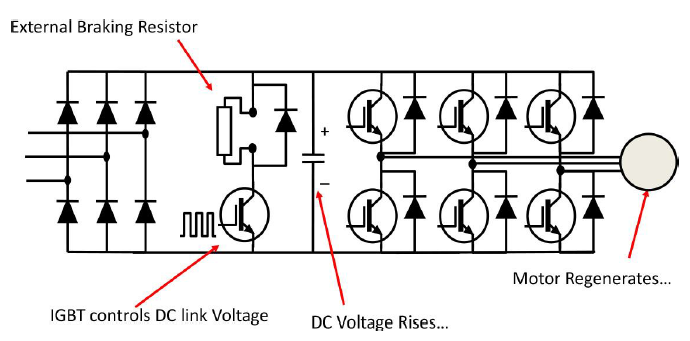


Figure 1: The model used in overall schematic of the Motor Drive in Open-Loop configuration

Starting with pre-design, rated values are to be calculated. Then, a suitable DC-Link capacitor is chosen according to created Simulink model illustrated in Figure 3.

Later on, Sinusoidal PWM method with current and speed controller using id-iq parameters are implemented with our own designed Clarke-Park transformation subcircuits. Then, some analysis using the data obtained from Voltage, Current, Speed etc. are performed. In these analysis, different load-characteristics and speed requirements are implemented.

After finishing the Sinusoidal PWM analysis, Space Vector PWM method is applied with readily available blocks. Then, 2 methods are compared and differences are discussed.

Finally, component selection and verification part is finished. Designed system is realized using the commercially available components. While choosing the components, application notes are used heavily. The characteristics for each component are analyzed and components are chosen accordingly. Efficiency is calculated and the drive is completed.

# PART A: Pre-design Stage

1. Rated torque of the motor is Tnominal , 300Nm. Rated speed of the motor is found by (1) and (2).
2. The maximum applied electrical frequency is found by (3).

In LV applications where the inverter output is in between 380-460Vrms, IGBT voltage class in low-level inverter topology is 1200V. In Figure A.1, total semiconductor losses as a function of carrier frequency can be seen. [1]. Choosing FF200R12KS4 as our IGBT and 2000W as a reasonable loss we decided to use 5000Hz. In this case mf is given by (4). Since we are operating below mf =21, mf should be an odd integer. To have mf approximately equal to 11, we chose fs as **5130Hz.**

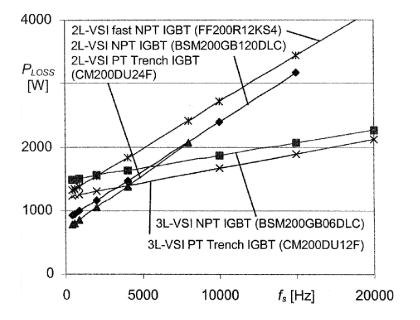


Figure 2: Total semiconductor loss as a function of carrier frequency

1. In this part we are required to find the suitable DC link capacitor for the rectifier output so that the DC input of the inverter will be 540V. Equivalent resistance at the rated current is 2.16Ω. The rectifying circuit and voltage output waveform can be seen in Figure A.2 and 3, respectively. DC link capacitor is 1mF as it can be seen output voltage is oscillating around 540V. However, 1mF is already is a big value.

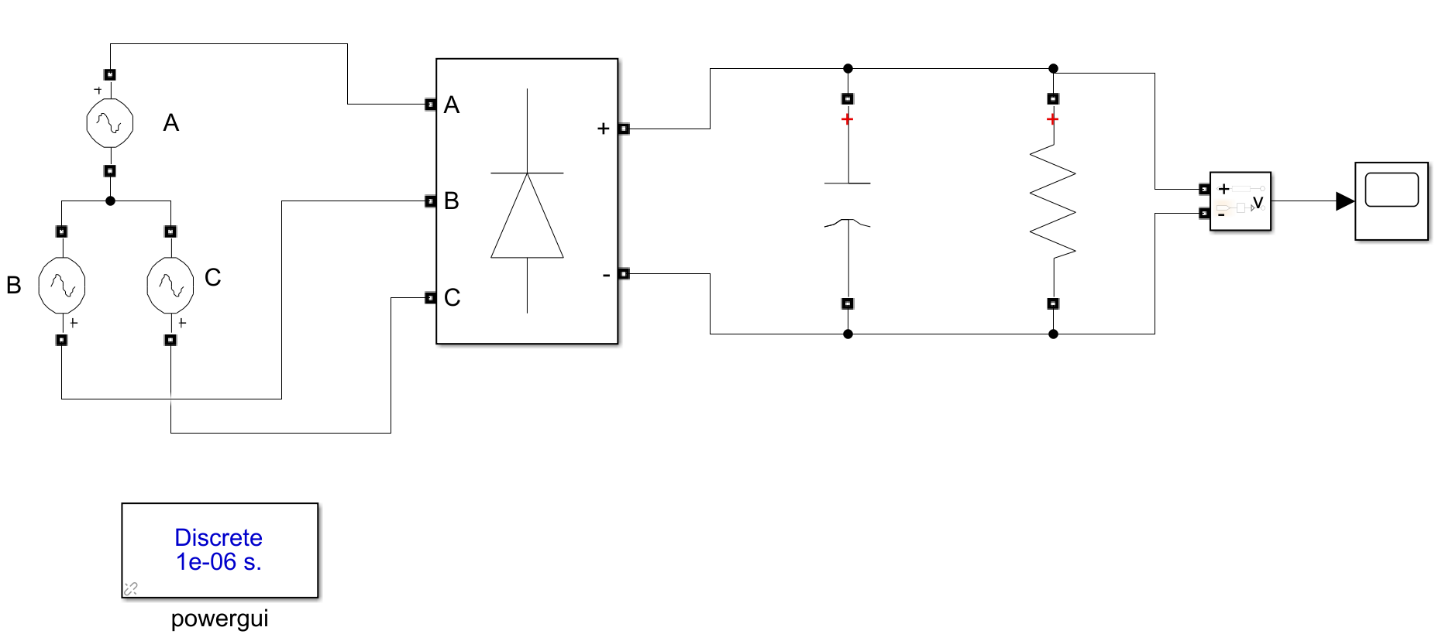


Figure 3: Simulink circuit of the rectifier

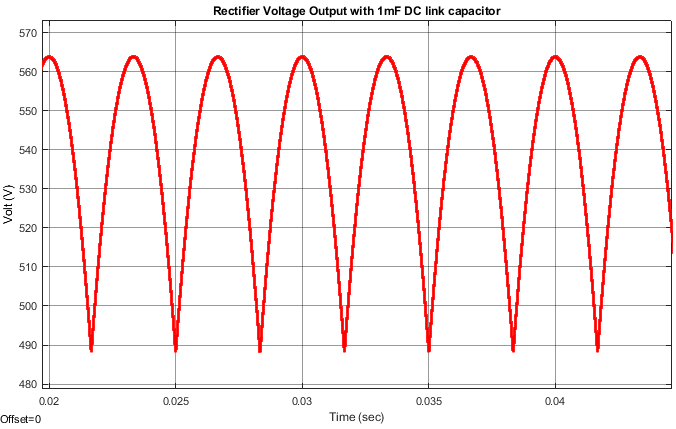


Figure 4: Output voltage of the rectifying unit

# PART B: Sinusoidal PWM

In this part, we are asked to implement a SMPMSM motor drive using sinusoidal PWM scheme. The controller must adjust the rotor speed according to the reference value and set a current limit to the nominal value using id, iq parameters. This motor drive can be seen in Figure B.1. Subsystem details in the given Simulink model can be observed in the slx. files uploaded to ODTUClass.

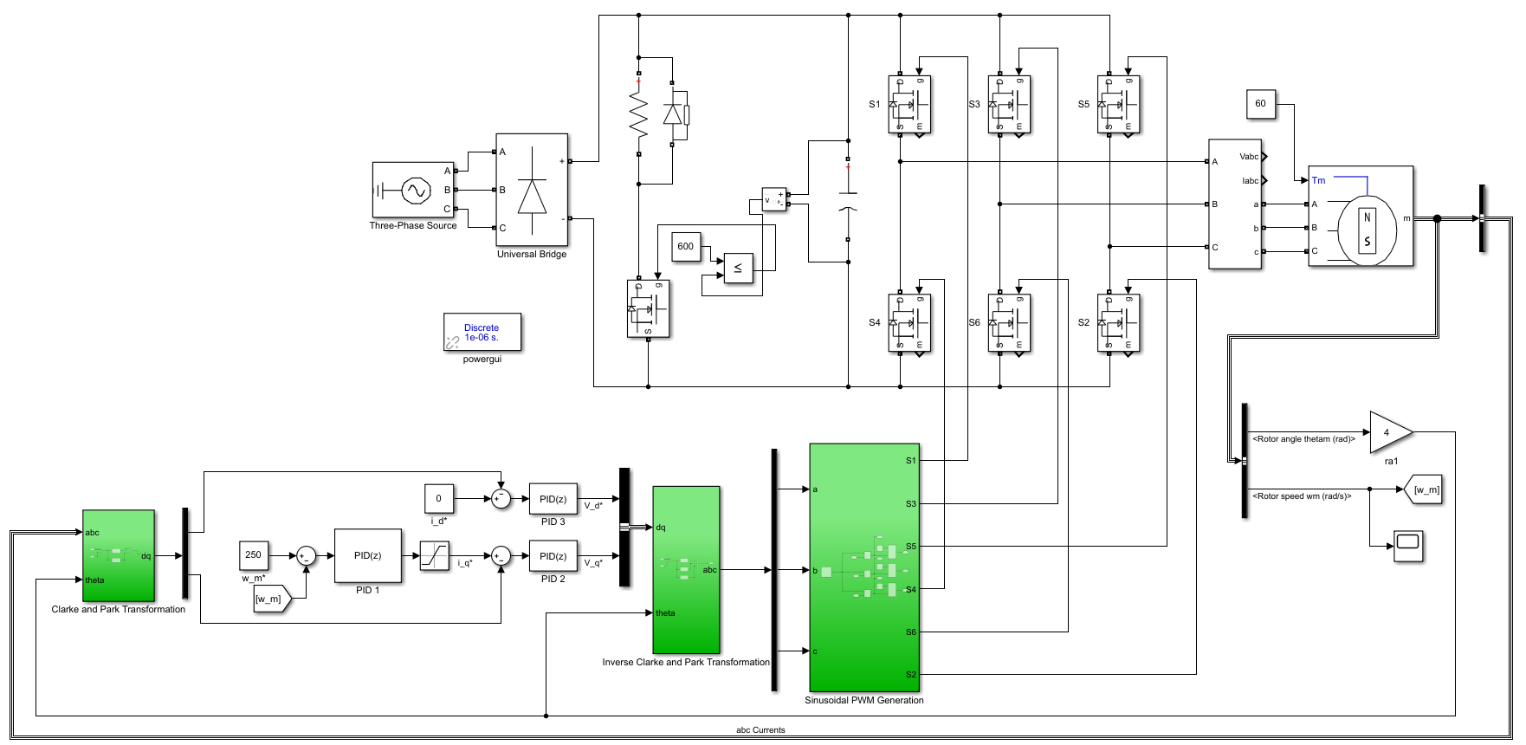


Figure 5: Detailed Simulink model of the SPWM motor drive

1. %90

# PART C: Space Vector PWM (SVPWM)

Since in this part we are allowed to use readily available Simulink blocks implementing SVPWM is quite easy. Without changing control loops, SVPWM Generator (2-level) block, which can be seen in Figure 6, is added which uses inverse Clarke’s transformation instead of Sinusoidal PWM Generator subsystem in Figure 5.

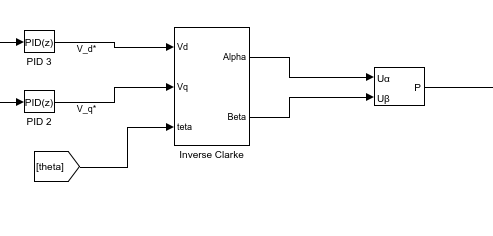


Figure : SVPWM Simulink model

1. In this part, we are asked to repeat part B using a Space Vector PWM algorithm.

Speed versus time plot of SVPWM scheme from %90 of the rated speed to rated speed can be seen in Figure 7. Also, motor 3-phase line-to-line voltages and 3-phase line currents vs. time plots are in Figure 8. Plots of d and q currents are in Figure 9. Finally, since in this part we are driving constant torque, its plot is not added.

Figure : Rotor speed versus time plot from %90 of the rated speed to rated speed

# PART D: Component selection and verification

# CONCLUSION

# References