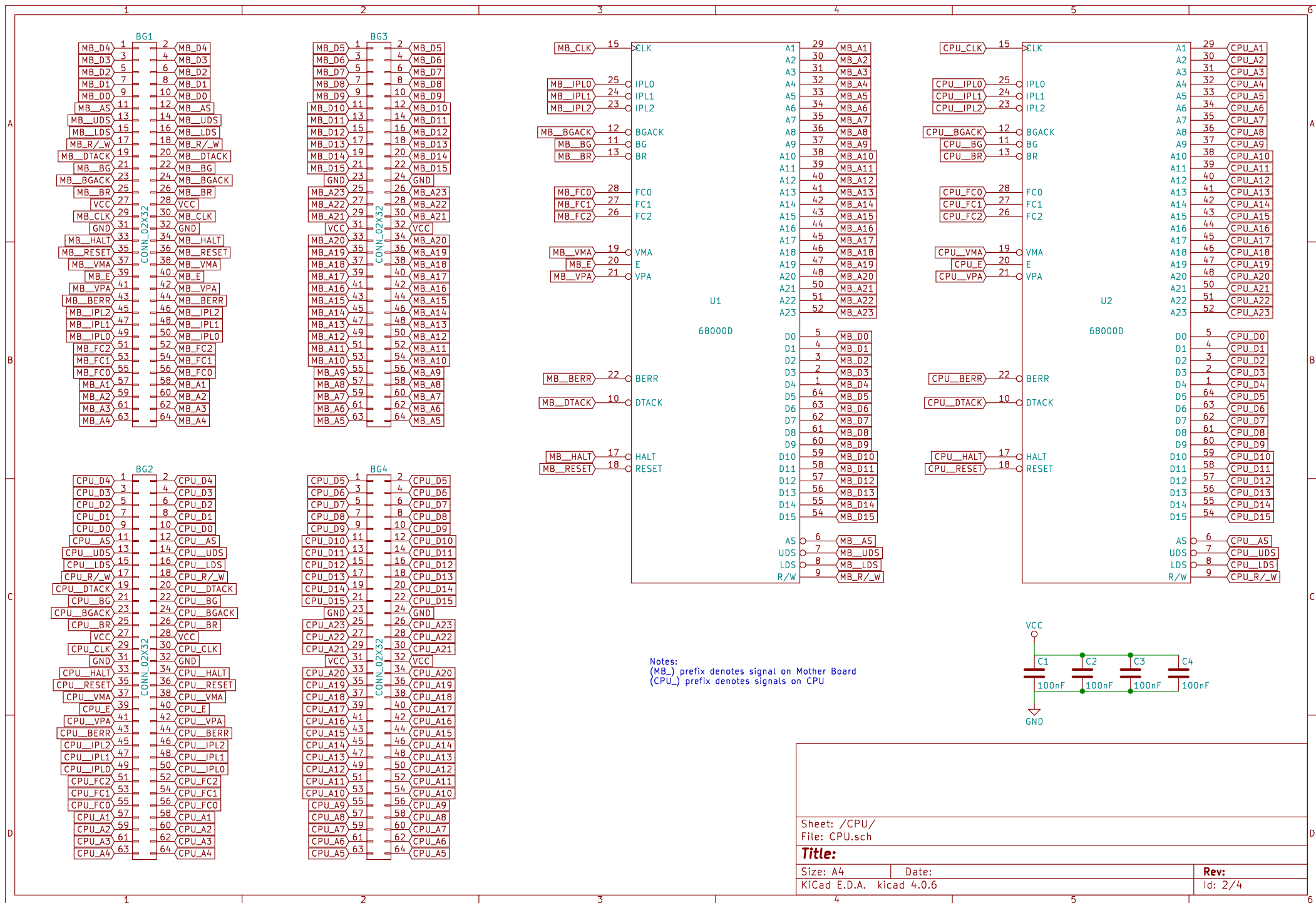
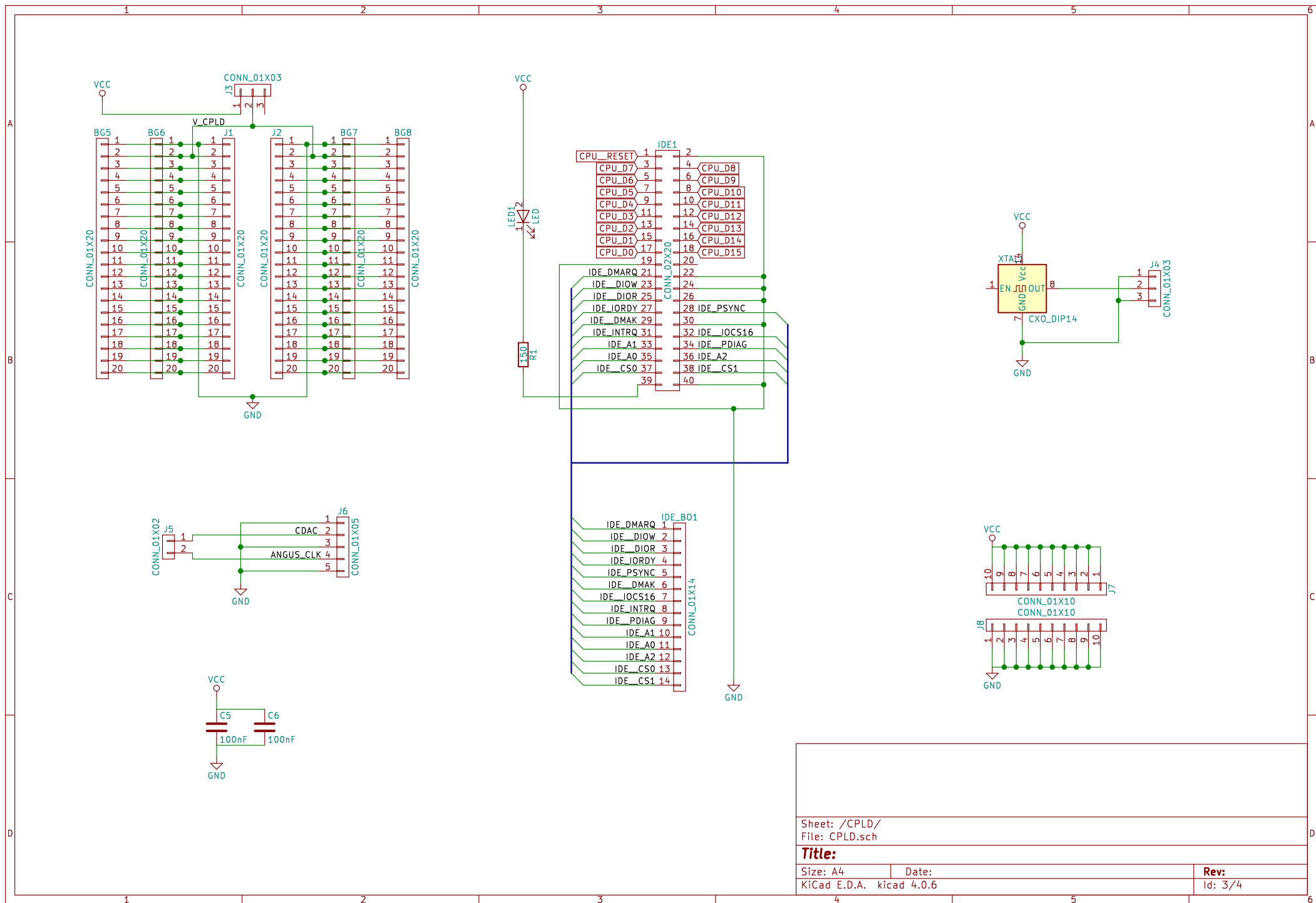


	1	2	3	4	5	6
A			Sheet: MEM			A
B			File: MEM.sch			B
C		Sheet: CPU	Sheet: CPLD			C
D		File: CPU.sch	File: CPLD.sch			D
	1	2	3	4	5	6

Sheet: /		
File: A500_ACCEL_RAM_IDE.sch		
Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad 4.0.6		Id: 1/4





Sheet: /CPLD/
File: CPLD.sch

Title:

Size: A4

Date:

KiCad E.D.A. kicad 4.0.6

Rev:

Id: 3/4

