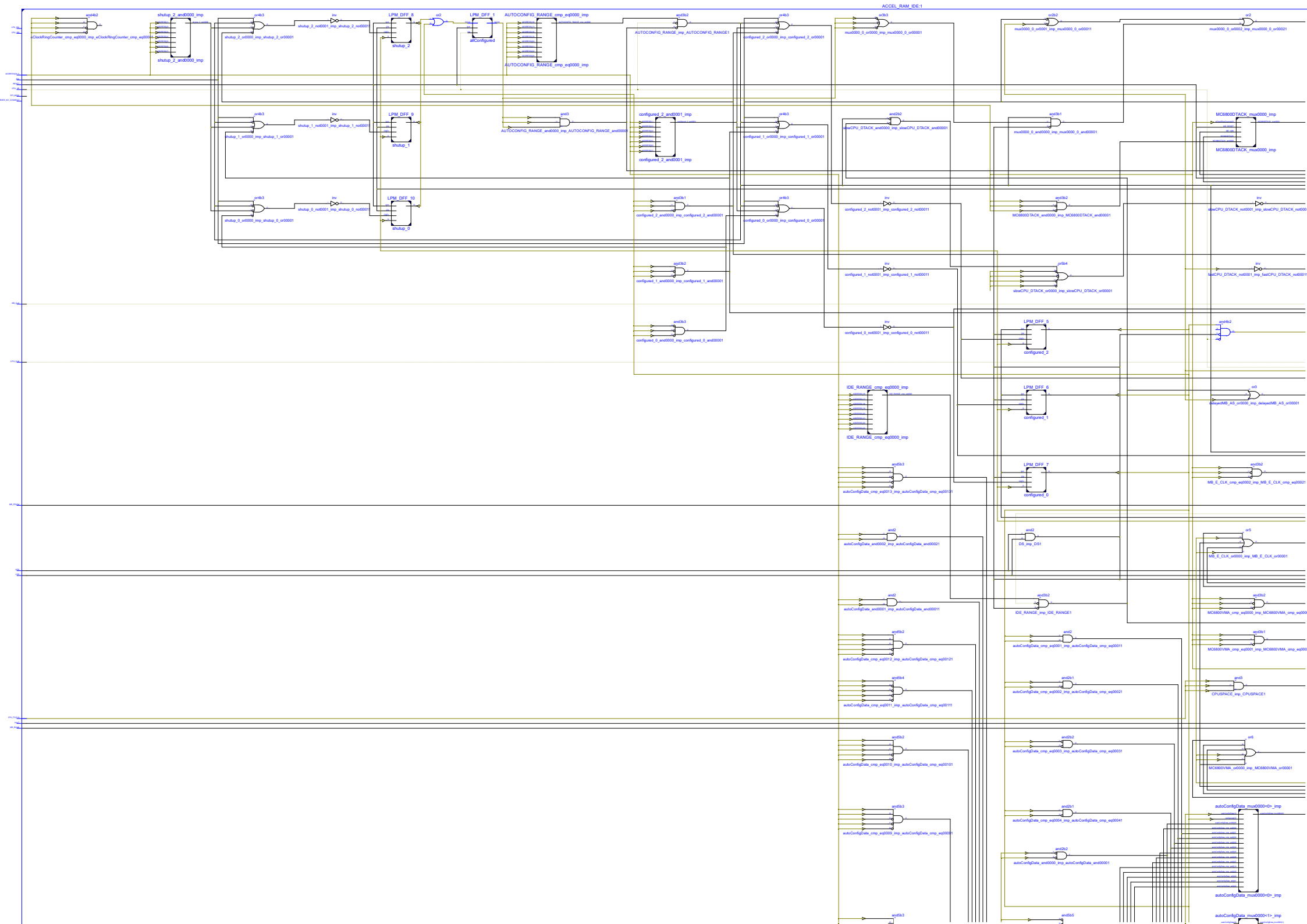
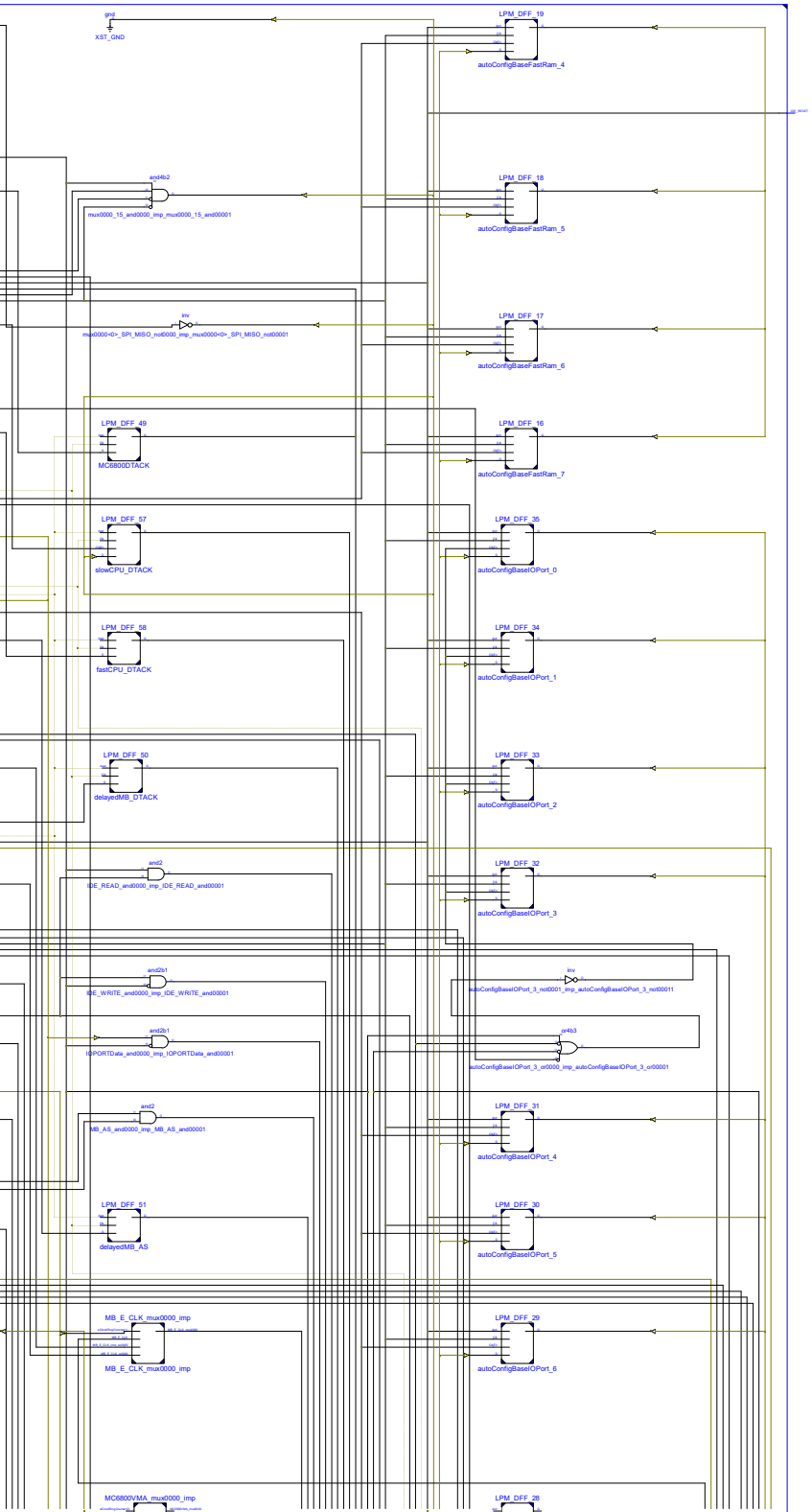
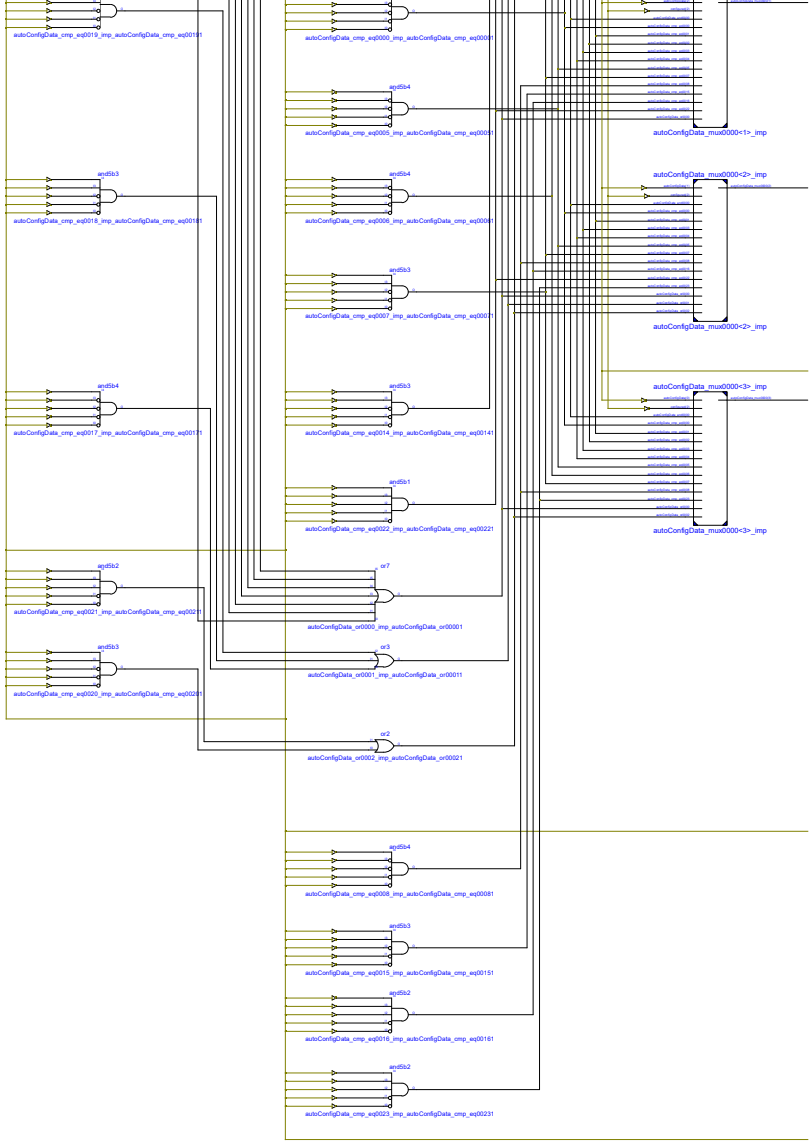
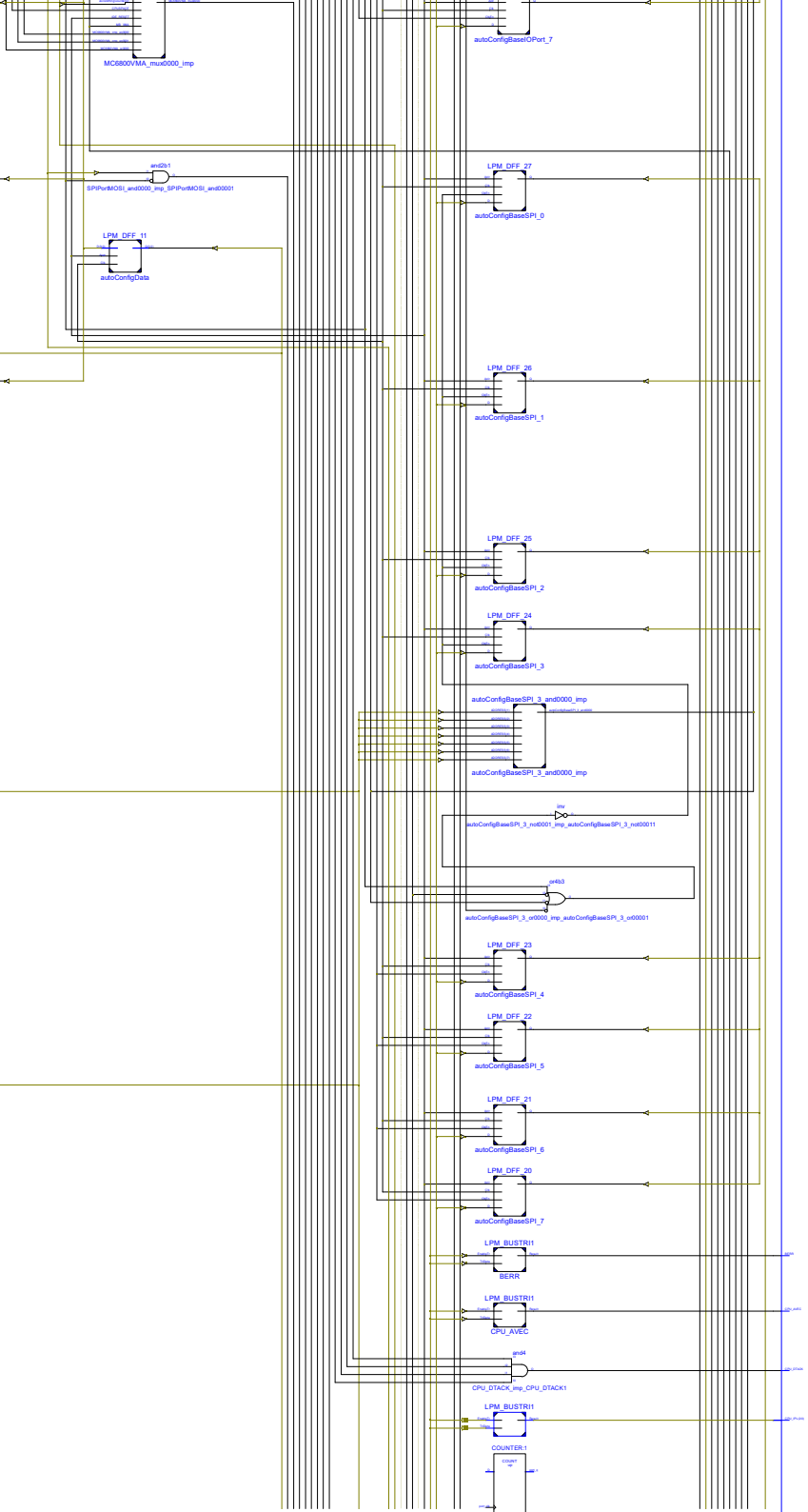
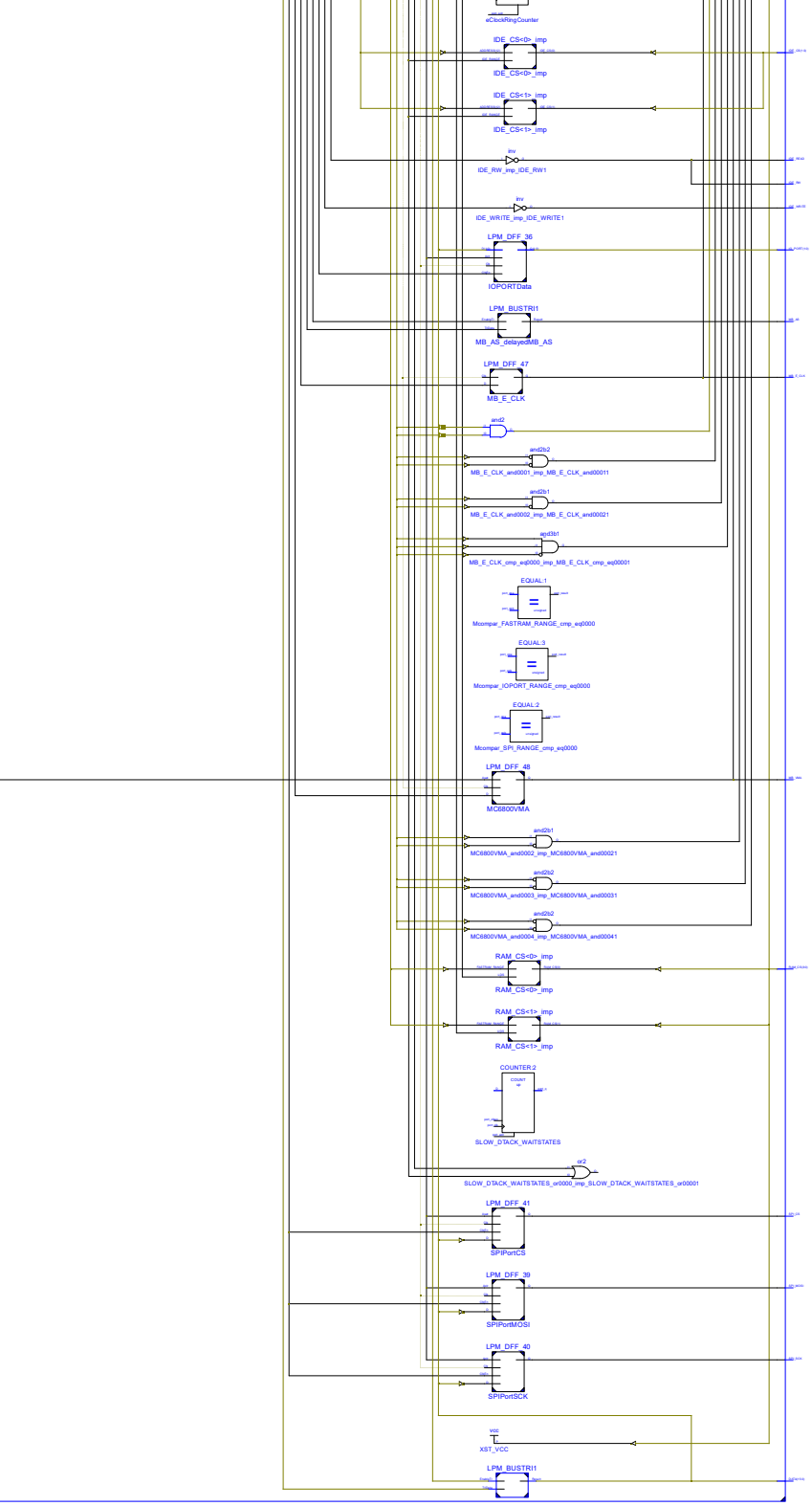

ACCEL_RAM_IDE:1
and4b2
eClockRingCounter_cmp_eq0000_imp_eClockRingCounter_cmp_eq00001
shutup_2_and0000_imp
or4b3
shutup_2_or0000_imp_shutup_2_or00001
inv
shutup_2_not0001_imp_shutup_2_not00001
LPM_DFF_8
shutup_2
or2
LPM_DFF_1
allConfigured
AUTOCONFIG_RANGE_cmp_eq0000_imp
and3b2
AUTOCONFIG_RANGE_imp_AUTOCONFIG_RANGE1
or4b3
configured_2_or0000_imp_configured_2_or00001
or3b3
mux0000_0_or0000_imp_mux0000_0_or00001
or2b2
mux0000_0_or0001_imp_mux0000_0_or00011
or2
mux0000_0_or0002_imp_mux0000_0_or00021
or4b3
shutup_1_or0000_imp_shutup_1_or00001
inv
shutup_1_not0001_imp_shutup_1_not00001
LPM_DFF_9
shutup_1
and3
AUTOCONFIG_RANGE_and0000_imp_AUTOCONFIG_RANGE_and00001
configured_2_and0001_imp
or4b3
configured_1_or0000_imp_configured_1_or00001
and3b2
slowCPU_DTACK_and0000_imp_slowCPU_DTACK_and00001
and3b1
mux0000_0_and0000_imp_mux0000_0_and00001
MC6800DTACK_mux0000_imp
or4b3
shutup_0_or0000_imp_shutup_0_or00001
inv
shutup_0_not0001_imp_shutup_0_not00001
LPM_DFF_10
shutup_0
and3b1
configured_2_and0000_imp_configured_2_and00001
or4b3
configured_0_or0000_imp_configured_0_or00001
inv
configured_2_not0001_imp_configured_2_not00011
and3b2
MC6800DTACK_and0000_imp_MC6800DTACK_and00001
inv
slowCPU_DTACK_not0001_imp_slowCPU_DTACK_not000
and3b2
configured_1_and0000_imp_configured_1_and00001
inv
configured_1_not0001_imp_configured_1_not00011
or5b4
slowCPU_DTACK_or0000_imp_slowCPU_DTACK_or00001
inv
fastCPU_DTACK_not0001_imp_fastCPU_DTACK_not00011
and3b3
configured_0_and0000_imp_configured_0_and00001
inv
configured_0_not0001_imp_configured_0_not00011
LPM_DFF_5
configured_2
and4b2
IDE_RANGE_cmp_eq0000_imp
IDE_RANGE_cmp_eq0000_imp
LPM_DFF_6
configured_1
or3
delayedMB_AS_or0000_imp_delayedMB_AS_or00001
and5b3
autoConfigData_cmp_eq0013_imp_autoConfigData_cmp_eq00111
LPM_DFF_7
configured_0
and3b2
MB_E_CLK_cmp_eq0002_imp_MB_E_CLK_cmp_eq00021
and2
autoConfigData_and0002_imp_autoConfigData_and00021
and2
DS_imp_DS1
or5
MB_E_CLK_or0000_imp_MB_E_CLK_or00001
and2
autoConfigData_and0001_imp_autoConfigData_and00011
and3b2
IDE_RANGE_imp_IDE_RANGE1
and3b2
MC6800VMA_cmp_eq0000_imp_MC6800VMA_cmp_eq000
and5b2
autoConfigData_cmp_eq0012_imp_autoConfigData_cmp_eq00121
and2
autoConfigData_cmp_eq0001_imp_autoConfigData_cmp_eq00011
and3b1
MC6800VMA_cmp_eq0001_imp_MC6800VMA_cmp_eq000
and5b4
autoConfigData_cmp_eq0011_imp_autoConfigData_cmp_eq00111
and2b1
autoConfigData_cmp_eq0002_imp_autoConfigData_cmp_eq00021
and3
CPUSPACE_imp_CPUSPACE1
and5b2
autoConfigData_cmp_eq0010_imp_autoConfigData_cmp_eq00101
and2b2
autoConfigData_cmp_eq0003_imp_autoConfigData_cmp_eq00031
or6
MC6800VMA_or0000_imp_MC6800VMA_or00001
and5b3
autoConfigData_cmp_eq0009_imp_autoConfigData_cmp_eq00091
and2b1
autoConfigData_cmp_eq0004_imp_autoConfigData_cmp_eq00041
autoConfigData_mux0000<0>_imp
and3b2
autoConfigData_and0000_imp_autoConfigData_and00001
autoConfigData_mux0000<0>_imp
autoConfigData_mux0000<1>_imp
and5b3
and5b5


gnd
XST_GND
LPM_DFF_19
autoConfigBaseFastRam_4
MB_RESET
and4b2
mux0000_15_and0000_imp_mux0000_15_and00001
LPM_DFF_18
autoConfigBaseFastRam_5
inv
mux0000<0>_SPI_MISO_not0000_imp_mux0000<0>_SPI_MISO_not00001
LPM_DFF_17
autoConfigBaseFastRam_6
LPM_DFF_49
MC6800DTACK
LPM_DFF_16
autoConfigBaseFastRam_7
LPM_DFF_57
slowCPU_DTACK
LPM_DFF_35
autoConfigBaseIOPort_0
LPM_DFF_58
fastCPU_DTACK
LPM_DFF_34
autoConfigBaseIOPort_1
LPM_DFF_50
delayedMB_DTACK
LPM_DFF_33
autoConfigBaseIOPort_2
and2
IDE_READ_and0000_imp_IDE_READ_and00001
LPM_DFF_32
autoConfigBaseIOPort_3
and2b1
IDE_WRITE_and0000_imp_IDE_WRITE_and00001
inv
autoConfigBaseIOPort_3_not0001_imp_autoConfigBaseIOPort_3_not00011
and2b1
IOPORTData_and0000_imp_IOPORTData_and00001
or4b3
autoConfigBaseIOPort_3_or0000_imp_autoConfigBaseIOPort_3_or00001
and2
MB_AS_and0000_imp_MB_AS_and00001
LPM_DFF_31
autoConfigBaseIOPort_4
LPM_DFF_51
delayedMB_AS
LPM_DFF_30
autoConfigBaseIOPort_5
MB_E_CLK_mux0000_imp
MB_E_CLK_mux0000_imp
LPM_DFF_29
autoConfigBaseIOPort_6
MC6800VMA_mux0000_imp
LPM_DFF_28


autoConfigData_cmp_eq0019_imp_autoConfigData_cmp_eq00191
autoConfigData_cmp_eq0006_imp_autoConfigData_cmp_eq00061
autoConfigData_cmp_eq0005_imp_autoConfigData_cmp_eq00051
autoConfigData_mux0000<1>_imp
autoConfigData_cmp_eq0018_imp_autoConfigData_cmp_eq00181
autoConfigData_cmp_eq0008_imp_autoConfigData_cmp_eq00081
autoConfigData_mux0000<2>_imp
autoConfigData_cmp_eq0007_imp_autoConfigData_cmp_eq00071
autoConfigData_mux0000<2>_imp
autoConfigData_cmp_eq0017_imp_autoConfigData_cmp_eq00171
autoConfigData_cmp_eq0014_imp_autoConfigData_cmp_eq00141
autoConfigData_mux0000<3>_imp
autoConfigData_cmp_eq0022_imp_autoConfigData_cmp_eq00221
autoConfigData_mux0000<3>_imp
autoConfigData_cmp_eq0021_imp_autoConfigData_cmp_eq00211
autoConfigData_or0000_imp_autoConfigData_or00001
autoConfigData_cmp_eq0020_imp_autoConfigData_cmp_eq00201
autoConfigData_or0001_imp_autoConfigData_or00011
autoConfigData_or0002_imp_autoConfigData_or00021
autoConfigData_cmp_eq0008_imp_autoConfigData_cmp_eq00081
autoConfigData_cmp_eq0015_imp_autoConfigData_cmp_eq00151
autoConfigData_cmp_eq0016_imp_autoConfigData_cmp_eq00161
autoConfigData_cmp_eq0023_imp_autoConfigData_cmp_eq00231


MC6800VMA_mux0000_imp
autoConfigBaseIOPort_7
and3b1
SPI\PortMOSI_and0000_imp_SPI\PortMOSI_and00001
LPM_DFF_27
autoConfigBaseSPI_0
LPM_DFF_11
autoConfigData
LPM_DFF_26
autoConfigBaseSPI_1
LPM_DFF_25
autoConfigBaseSPI_2
LPM_DFF_24
autoConfigBaseSPI_3
autoConfigBaseSPI_3_and0000_imp
autoConfigBaseSPI_3_and0000_imp
inv
autoConfigBaseSPI_3_not0001_imp_autoConfigBaseSPI_3_not00011
or4b3
autoConfigBaseSPI_3_or0000_imp_autoConfigBaseSPI_3_or00001
LPM_DFF_23
autoConfigBaseSPI_4
LPM_DFF_22
autoConfigBaseSPI_5
LPM_DFF_21
autoConfigBaseSPI_6
LPM_DFF_20
autoConfigBaseSPI_7
LPM_BUSTRI1
BERR
LPM_BUSTRI1
CPU_AVEC
and4
CPU_DTACK_imp_CPU_DTACK1
LPM_BUSTRI1
COUNTER:1


aClockRingCounter
IDE_CS<0>_imp
IDE_CS<0>_imp
IDE_CS<1>_imp
IDE_CS<1>_imp
inv
IDE_RW_imp_IDE_RW1
inv
IDE_WRITE_imp_IDE_WRITE1
LPM_DFF_36
IOPORTData
LPM_BUSTRI1
MB_AS_delayedMB_AS
LPM_DFF_47
MB_E_CLK
and2
and2b2
MB_E_CLK_and0001_imp_MB_E_CLK_and00011
and2b1
MB_E_CLK_and0002_imp_MB_E_CLK_and00021
ajd3b1
MB_E_CLK_cmp_eq0000_imp_MB_E_CLK_cmp_eq00001
EQUAL:1
Mcompar_FASTRAM_RANGE_cmp_eq0000
EQUAL:3
Mcompar_IOPORT_RANGE_cmp_eq0000
EQUAL:2
Mcompar_SPI_RANGE_cmp_eq0000
LPM_DFF_48
MC6800VMA
and2b1
MC6800VMA_and0002_imp_MC6800VMA_and00021
and2b2
MC6800VMA_and0003_imp_MC6800VMA_and00031
and2b2
MC6800VMA_and0004_imp_MC6800VMA_and00041
RAM_CS<0>_imp
RAM_CS<0>_imp
RAM_CS<1>_imp
RAM_CS<1>_imp
COUNTER:2
SLOW_DTACK_WAITSTATES
or2
SLOW_DTACK_WAITSTATES_or0000_imp_SLOW_DTACK_WAITSTATES_or00001
LPM_DFF_41
SPIPortCS
LPM_DFF_39
SPIPortMOSI
LPM_DFF_40
SPIPortSCK
vcc
XST_VCC
LPM_BUSTRI1