	Name: Alok Bhawan Kar			
	ROTI no : PAOG	Page No.		
(1) 4 11 1/4 1 1/4 1/4 1/4 1/4 1/4 1/4 1/4		SSC		
omendario y sico da Referiga meso meso	ASSIGNIZIENT -2	9	opposed the second second second	
	H55 JGNINERI	Annual Control of the	general and the second second second	elan, gera en terrese religioloris, de
and the second second second			A STATE OF THE STA	none and soldhood
at the continue to a large	TITLE		earnesservice and extensive	
	Design of Pass 2 of two pas	3 0.35	(cmb)	122
managali ny istra na miningalisarana.	AIM:	1		Addition to the same and the sa
	Design swhable data structure and	d ime	oleme	'nt
	pass 2 of two pass assembles &	rendo	maic	bine
	pass 7 or two pass assessed t			
The local control of the local	MOTCETTUC:			
	OBJECTIVE: Design truitable data structure ar	al in	nalon	acnt
Alle di Sirina, uleanini ini di si				
PARTY OF A PARTY OF THE	pass 2 of two pass assembles y			
	machine subset should consist of			
-	instruction from each category a	nd	1cm	Same distribution (Secretary)
	assembles directives.			pag demili ti in di kemanjan pangan pangan meng
	THEORY:		- The second	
The state of the s			and the second distribution of the second distri	managed above to another the second
	Design of a two Pass Assembler			
	Two Pass Assembles			an Colonyal an aproprior for the control
			ed description than the second trans	
and property of the second section of the second	Pass I Pass 2			
	A second	Sun th	isic	
	Perform analysis Perform		and the same throat of the same	
*		the second secon	and the second s	at valence and any or in the same of the party of the same of the
	Data Structure		we produce the control of the control of	erani ar meneralisepani sepanju magamentum
50	urce Program		the transfer and discount for	a salamandense (neil in tend, ser olare tend
			> Ta	LRSIEF
	1 2030 1	152 -	Pro	ogra n
	> Intermediate code	11 12		0

ALGORITHM For Pass II (0) code area adress - address of code area: loc cola = 0 20) While next statement is not an END sterkment as clear machine - code buffer b) III a start or origin sterement then i) loc colo = value specifical in operand field. ii) Size = Size of memory area elequiered by DC PS c) If an impenative stakment i) Get operand address from SYMITAB OR LITTAB ii) size Assemble instruction in machine code buffer. iii) Size = Size of instruction; d) IF size i = 0 then, i) More content of muchine code buffer to address code. ariaadviss to locanter. ii) Locenter = locenter + size 30) Write code area input output file IMPUT: Symbol table and Intermediak code generated by Pass I

	The set of					
and the same of th		and the second s				
	QUTPUT:					
	I) final output CAfter Pass II)					
	Address	op wde	op and I	operand 2		
or followers the second	CLC Value		Crahu / Address	Value Addres		
and the first and the state of						
	Conduston:					
			unction of Pass	II in an		
ter three are builty, it of science and you can	assembler are studied					
	PLATFORM:					
salt epiter di trant di me te fini co in filiami, consecuence con						
	1-	IMUX JAVI	4			