



The 27th IEEE Pacific Rim International Symposium on Dependable Computing (PRDC 2022), Industry Track

Tsinghua University, Beijing, China + (Virtual options)

November 28-December 1, 2022

<http://prdc.dependability.org/PRDC2022/cfp-industry.html>

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Industry-Track Call for papers

IEEE PRDC 2022 is the twenty-seventh event in a series of symposia started in 1989 devoted to dependable and fault-tolerant computing. PRDC is recognized as the main event in the Pacific area that covers many dimensions of dependability and fault tolerance, encompassing fundamental theoretical approaches, practical experimental projects, and commercial components and systems. As applications of computing systems have permeated into all aspects of daily life, the dependability of computing systems has become increasingly critical. This symposium provides a forum for countries around the Pacific Rim and other areas of the world to exchange ideas for improving the dependability of computing systems.

The Industry Track provides a forum for researchers and practitioners from industry to present and debate R&D challenges, practical solutions, case studies, and share field reliability data. The topics of interest include several aspects of dependable computing such as the reliability, availability, safety, security, and measurement techniques for both computer hardware and software. The Industry Track seeks to promote and foster discussion on the state-of-the-art in an industrial context, feedback from experiments and field measurements, applicability and scalability issues regarding recent techniques, and novel technological challenges.

Submission and Publication Information

Manuscripts may be submitted in IEEE double-column format (<https://www.ieee.org/conferences/publishing/templates.html>) to one of the following two categories: (i) Regular Paper and (ii) Position paper.

- **Regular paper** submission consisting of 6 pages (including references).
- **Position paper** submission consisting of 2 pages (including references).

All submissions must be made electronically (in PDF format) through the submission website (<https://easychair.org/conferences/?conf=prdc2022industry>). All accepted papers/abstracts will be published in the PRDC volume and made available in IEEE Xplore (EI Indexed). Accepted materials will be presented in dedicated sessions. All materials must be written in English, and be formatted according to the IEEE double-column format. Contributions must be in PDF. Submissions will be reviewed by the Industry Track Program Committee and evaluated for their relevance to PRDC, the technical soundness, the potential impact (e.g., adoption), and their potential for stimulating further research. Submission of a contribution to the track indicates agreement to have one author present the work at the conference, if accepted. **In light of the ongoing COVID-19 pandemic, virtual presentation and attendance options will be available to accommodate all participants.**

The Industry Track will include Best Industry Paper Award. It will be awarded to the best paper among the regular papers accepted to appear in the PRDC 2022 Industry Track. A few excellent papers will be recommended to have their extended versions submitted to IEEE Transactions on Dependable and Secure Computing (TDSC) or a Special Issue of the Software Testing, Verification and Reliability (STVR) journal.

Important Dates for Industry Track

Submission: 15 August 2022 (Anywhere on Earth, AoE time)

Notification of Acceptance: 12 September 2022

Keynote Speakers:



Kishor S. Trivedi, Hudson Chair Professor in the Department of Electrical and Computer Engineering at Duke University. He is IEEE Fellow, IEEE Life Fellow, and Golden Core Member of the IEEE Computer Society. He is the author of the well-known textbook entitled, *Probability and Statistics with Reliability, Queuing and Computer Science Applications*. His software tools SHARPE (Symbolic Hierarchical Automated Reliability and Performance Evaluator), SPNP (Stochastic Petri Net Package), and SREPT (Software Reliability Estimation and Prediction Tool) have been used in 500+ academic/industrial laboratories.



Paulo E. Verissimo, a Professor at KAUST and Director of the Resilient Computing and Cybersecurity Center (RC3) at KAUST. Previously, he was a professor and FNR PEARL Chair at the University of Luxembourg FSTM, and Head of the CritiX Research Lab at SnT center at the same University. He is both IEEE Fellow and ACM Fellow. He was the representative of UNILU-SnT in European Cyber Security Organization, Chair of the IFIP WG 10.4 on Dependable Computing and Fault-Tolerance, vice-Chair of the Steering Committee of DSN, and associate editor of IEEE TETC.



Zhendong Su, a Professor in the Department of Computer Science at ETH Zurich. He had been a full professor in Computer Science and a Chancellor's Fellow at UC Davis until June 2019. He is an elected member of the Academia Europaea (the Academy of Europe). He served on the steering committees of ISSTA and ESEC/FSE, served as an Associate Editor for ACM TOSEM, co-chaired SAS 2009, program chaired ISSTA 2012, and program co-chaired SIGSOFT FSE 2016. His research spans programming languages and compilers, software engineering, computer security, deep learning and education technologies.



Tao Xie, a Peking University Chair Professor. He is a Deputy Director of the Key Lab of High Confidence Software Technologies (PKU), Ministry of Education, and the Deputy Secretary General of the Emerging Engineering Development Committee of Peking University. He was a Full Professor at the Department of Computer Science, University of Illinois at Urbana-Champaign (UIUC), USA. He is a Fellow of ACM, IEEE, AAAS, and CCF. He also serves as a Deputy Director of CCF TCSE. His main research interests include software engineering, system software, software security, and trustworthy AI.

Industry Track Program Committee:

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