



# 第 27 届电气和电子工程师协会 (IEEE) 可信计算环太平洋 国际研讨会 (PRDC 2022) 工业赛道

北京, 清华大学 (线上与线下混合模式)

2022 年 11 月 28 日-12 月 1 日召开

<http://prdc.dependability.org/PRDC2022/cfp-industry.html>



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## 工业赛道征稿通知

电气和电子工程师协会可信计算环太平洋国际研讨会 (IEEE Pacific Rim International Symposium on Dependable Computing, PRDC) 专注于可信的 (dependable) 软件与系统, 涵盖基础理论、经验研究和工业实践在内等多方面的相关研究工作。自首届会议于 1989 年召开以来, PRDC 已成为环太平洋区域可信与容错计算的最主要学术会议 (入选澳大利亚的 CORE Ranking 会议列表)。随着计算系统与应用渗透到日常生活的方方面面, 计算系统的可信变得越来越高。PRDC 研讨会为环太平洋地区和世界其他地区的专家学者提供了一个为提高计算系统可靠性与安全性而充分交流的论坛。PRDC2022 大会将采用线上与线下相结合的混合会议模式。

本次会议工业赛道环节为来自工业界的研究人员和从业者提供一个论坛, 交流与讨论工业界可信计算领域面临的挑战、实用解决方案、案例研究并分享现场可靠性数据。本次会议以系统可信计算为主题, 包括系统的可靠性、可用性、安全性, 以及网络安全性和可测试性技术。我们致力于为学术界和工业界创建一个平台, 希望能够帮助参会者更好地了解工业界可信计算技术的挑战与未来, 在可信计算理论研究与实际应用之间搭起一座桥梁。本次会议将邀请工业界相关的专家学者共同参加, 诚邀广大工业界可信计算从业者积极投稿。

## 投稿指南

所有稿件须以 IEEE 双列格式 (<https://www.ieee.org/conferences/publishing/templates.html>) 的 PDF 文件通过提交网站提交 (<https://easychair.org/conferences/?conf=prdc2022industry>)。

- 常规论文: 6 页正文 (包括参考文献)
- 意见文章 (Position Paper): 2 页正文 (包括参考文献)

所有接收论文将由 IEEE 计算机学会出版社 (EI 索引) 出版。此外, 提交文稿即表示文章被接收后会由一位作者在会议上作报告。鉴于新冠疫情, 会议将提供线上与线下参加和作报告的选项。

## 优秀论文奖励

会议将评选一篇常规论文获最佳论文奖。多篇接收的优秀论文将被推荐其扩展版本给 IEEE Transactions on Dependable and Secure Computing (TDSC), 或 Software Testing, Verification and Reliability (STVR) 期刊的特刊。

## 重要日期

全文提交截止日期: 2022 年 8 月 15 日 (Anywhere on Earth, AoE time)

录用通知日期: 2022 年 9 月 12 日

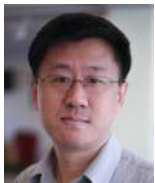
## 大会主题报告人:



**Kishor S. Trivedi**, Hudson Chair Professor in the Department of Electrical and Computer Engineering at Duke University. He is IEEE Fellow, IEEE Life Fellow, and Golden Core Member of the IEEE Computer Society. He is the author of the well-known textbook entitled, *Probability and Statistics with Reliability, Queuing and Computer Science Applications*. His software tools SHARPE (Symbolic Hierarchical Automated Reliability and Performance Evaluator), SPNP (Stochastic Petri Net Package), and SREPT (Software Reliability Estimation and Prediction Tool) have been used in 500+ academic/industrial laboratories.



**Paulo E. Verissimo**, a Professor at KAUST and Director of the Resilient Computing and Cybersecurity Center (RC3) at KAUST. Previously, he was a professor and FNR PEARL Chair at the University of Luxembourg FSTM, and Head of the CritiX Research Lab at SnT center at the same University. He is both IEEE Fellow and ACM Fellow. He was the representative of UNILU-SnT in European Cyber Security Organization, Chair of the IFIP WG 10.4 on Dependable Computing and Fault-Tolerance, vice-Chair of the Steering Committee of DSN, and associate editor of IEEE TETC.



**Zhendong Su**, a Professor in the Department of Computer Science at ETH Zurich. He had been a full professor in Computer Science and a Chancellor's Fellow at UC Davis until June 2019. He is an elected member of the Academia Europaea (the Academy of Europe). He served on the steering committees of ISSTA and ESEC/FSE, served as an Associate Editor for ACM TOSEM, co-chaired SAS 2009, program chaired ISSTA 2012, and program co-chaired SIGSOFT FSE 2016. His research spans programming languages and compilers, software engineering, computer security, deep learning and education technologies.



**Tao Xie**, a Peking University Chair Professor. He is a Deputy Director of the Key Lab of High Confidence Software Technologies (PKU), Ministry of Education, and the Deputy Secretary General of the Emerging Engineering Development Committee of Peking University. He was a Full Professor at the Department of Computer Science, University of Illinois at Urbana-Champaign (UIUC), USA. He is a Fellow of ACM, IEEE, AAAS, and CCF. He also serves as a Deputy Director of CCF TCSE. His main research interests include software engineering, system software, software security, and trustworthy AI.

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