第 27 届电气和电子工程师协会（IEEE）可信赖计算环太平洋国际研讨会（PRDC 2022）

北京，清华大学（线上与线下混合模式）

2022 年 11 月 28 日-12 月 1 日召开

http://prdc.dependability.org/PRDC2022/

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征稿通知

电气和电子工程师协会可信赖计算环太平洋国际研讨会(IEEE Pacific Rim International Symposium on Dependable Computing, PRDC)专注于可信赖的(dependable)软件与系统，涵盖基础理论、经验研究和工业实践在内等多方面的相关研究工作。自首届会议于 1989 年召开以来，PRDC 已成为环太平洋区域可信赖与容错计算的最主要学术会议（入选澳大利亚的 CORE Ranking 会议列表）。随着计算系统与应用渗透到日常生活的方方面面，计算系统的可信赖变得越来越重要。PRDC 研讨会为环太平洋地区和世界其他地区的专家学者提供了一个为提高计算系统可靠性与安全性而充分交流的论坛。PRDC2022 大会将采用线上与线下相结合的混合会议模式。

征稿范围包括（但不限于）如下主题：
- 软件和硬件的可靠性、安全性、测试和验证等
- 可信赖计算的度量 、建模、评估及相关工具
- 自愈系统、自保护系统和容错系统
- 面向可信赖计算的架构和系统设计
- 复杂系统的故障预测
- 复杂系统的可靠性分析
- 容错算法和协议
- 云计算的可靠性、安全性和隐私
- 软件定义网络的架构和协议
- 各类计算系统的可信赖性问题（如计算机网络和通讯、高性能计算系统、实时系统、存储和数据库系统、物理信息系统、社会技术系统、区块链和智能合约等）
- 新兴技术的可靠性与安全性问题（如自动驾驶系统、机器学习系统、智能设备和物联网等）

投稿指南

PRDC2022 大会征集原创未发表的研究论文，包含常规论文和实践经验报告两类。论文的评审将根据每个类别的标准进行。每篇投稿将由相关领域的多名国际专家学者从学术原创性、问题和结果重要性、相关性和论文写作质量等多方面进行评审。
- 常规论文：10 页正文 + 参考文献和附录，描述原创研究
- 实践经验报告：6 页正文 + 参考文献和附录，描述经验或案例研究，例如真实系统的设计和部署，故障和修复的真实数据等。

未被接收为常规论文的投稿可能会作为实践经验报告被接收；此时作者可在收到录取通知时选择是发表全文（10 页+参考文献）还是缩写版（6 页+参考文献）。

所有稿件须以 IEEE 双列格式 (https://www.ieee.org/conferences/publishing/templates.html) 的 PDF 文件通过提交网站(https://easychair.org/conferences/?conf=prdc2022)提交。所有稿件将经过双盲审核，请确保您已删除任何可能导致稿件作者身份泄露的参考资料，否则可能导致论文被拒。所有接收论文将由 IEEE 计算机学会出版社(EI 索引)出版。此外，提交文稿即表示文章被接收后会由一位作者在会议上作报告。鉴于新冠疫情，会议将提供线上与线下参加和作报告的选项。

优秀论文奖励

会议将评选一篇论文获最佳论文奖，评选一篇学生为一作的论文最佳学生论文奖。多篇接收的优秀论文将被推荐其扩展版本给 IEEE Transactions on Dependable and Secure Computing（TDSC），或 Software Testing, Verification and Reliability (STVR)期刊的特刊。

重要日期

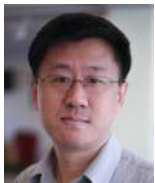全文提交截止日期：2022 年 7 月 18 日(Anywhere on Earth, AoE time)
录用通知日期：2022 年 8 月 22 日

大会主题报告人:

Kishor S. Trivedi, Hudson Chair Professor in the Department of Electrical and Computer Engineering at Duke University. He is IEEE Fellow, IEEE Life Fellow, and Golden Core Member of the IEEE Computer Society. He is the author of the well-known textbook entitled, *Probability and Statistics with Reliability, Queuing and Computer Science Applications*. His software tools SHARPE (Symbolic Hierarchical Automated Reliability and Performance Evaluator), SPNP (Stochastic Petri Net Package), and SREPT (Software Reliability Estimation and Prediction Tool) have been used in 500+ academic/industrial laboratories.

Paulo E. Verissimo, a Professor at KAUST and Director of the Resilient Computing and Cybersecurity Center (RC3) at KAUST. Previously, he was a professor and FNR PEARL Chair at the University of Luxembourg FSTM, and Head of the CritiX Research Lab at SnT center at the same University. He is both IEEE Fellow and ACM Fellow. He was the representative of UNILU-SnT in European Cyber Security Organization, Chair of the IFIP WG 10.4 on Dependable Computing and Fault-Tolerance, vice-Chair of the Steering Committee of DSN, and associate editor of IEEE TETC.

Zhendong Su, a Professor in the Department of Computer Science at ETH Zurich. He had been a full professor in Computer Science and a Chancellor's Fellow at UC Davis until June 2019. He is an elected member of the Academia Europaea (the Academy of Europe). He served on the steering committees of ISSTA and ESEC/FSE, served as an Associate Editor for ACM TOSEM, co-chaired SAS 2009, program chaired ISSTA 2012, and program co-chaired SIGSOFT FSE 2016. His research spans programming languages and compilers, software engineering, computer security, deep learning and education technologies.

Tao Xie, a Peking University Chair Professor. He is a Deputy Director of the Key Lab of High Confidence Software Technologies (PKU), Ministry of Education, and the Deputy Secretary General of the Emerging Engineering Development Committee of Peking University. He was a Full Professor at the Department of Computer Science, University of Illinois at Urbana-Champaign (UIUC), USA. He is a Fellow of ACM, IEEE, AAAS, and CCF. He also serves as a Deputy Director of CCF TCSE. His main research interests include software engineering, system software, software security, and trustworthy AI.

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