# **DATA MEMORY REGISTER MAP**

### DECISION MAKING CONTROL REGISTER

Address Offset 0x00
Reset Value 0x0000 0002

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
							RESE	RVED							
N/A	N/A	N/A	N/A	N/A	N/A	N/A	N/A	N/A	N/A	N/A	N/A	N/A	N/A	N/A	N/A
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
			RESERVED				I2C_BUSY	MLP_BUSY	•	MLP_F	RESULT		DMC_DONE	DMC_RT	DMC_START
N/A	N/A	N/A	N/A	N/A	N/A	N/A	R	R	R/W				R	R/W	R/W

### SENSOR DATA 0 REGISTER

Address Offset 0x04
Reset Value 0x0000 0002

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
							SENSOR DA	ATA 0 [31:16]							
							R	/W							
15	14	10	10	44	10	0	0	7		-	4	2	0	4	
15	14	13	12	11	10	9	8	/	ь	5	4	3	2	1	U
							SENSOR D	ATA 0 [15:0]							
							R	ΛM							

## SENSOR DATA 1 REGISTER

Address Offset 0x08

Reset Value 0x0000 0002

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
							SENSOR DA	ATA 1 [31:16]							
							R	/W							
15	1/	13	12	11	10	۵		7	6	5	4	2	2	1	0
15	14	13	12		10	9	SENSOR D	ATA 1 [15:0]	0	J J	4	3			0
							R	/W							

#### SENSOR DATA 2 REGISTER

Address Offset 0x0C Reset Value 0x0000 0002

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
							SENSOR DA	ATA 2 [31:16]							
	R/W														
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
							SENSOR D	ATA 2 [15:0]							
							R	W							

### SENSOR DATA 3 REGISTER

Address Offset 0x10

Reset Value 0x0000 0002

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
							SENSOR DA	ATA 3 [31:16]							
	R/W														
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
							SENSOR D	ATA 3 [15:0]							
		•	•		•	•	R/	W			•				

## SENSOR DATA 4 REGISTER

Address Offset 0x14 Reset Value 0x0000 0002

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
							SENSOR DA	ATA 4 [31:16]							
							R	/W							
•															
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
							SENSOR D	ATA 4 [15:0]							
							R	/W							

## SENSOR DATA 5 REGISTER

Address Offset 0x18
Reset Value 0x0000 0002

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
							SENSOR DA	ATA 5 [31:16]							
							R	/W							
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
							SENSOR D	ATA 5 [15:0]							
							R	/W							

## SENSOR DATA 6 REGISTER

Address Offset 0x1C Reset Value 0x0000 0002

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
							SENSOR DA	ATA 6 [31:16]							
							R	/W							
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
							SENSOR D	ATA 6 [15:0]							
				•	•	•	R	/W	•	•	•	•			

## SENSOR DATA 7 REGISTER

Address Offset 0x20
Reset Value 0x0000 0002

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
							SENSOR DA	ATA 7 [31:16]							
	R/W														
•															
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
							SENSOR D	ATA 7 [15:0]							
							R	ΛW							

### SENSOR DATA 8 REGISTER

Address Offset 0x24

Reset Value 0x0000 0002

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16
							SENSOR DA	ATA 8 [31:16]							
	R/W														
															<u>-</u>
15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
							SENSOR D	ATA 8 [15:0]							
							R	/W							