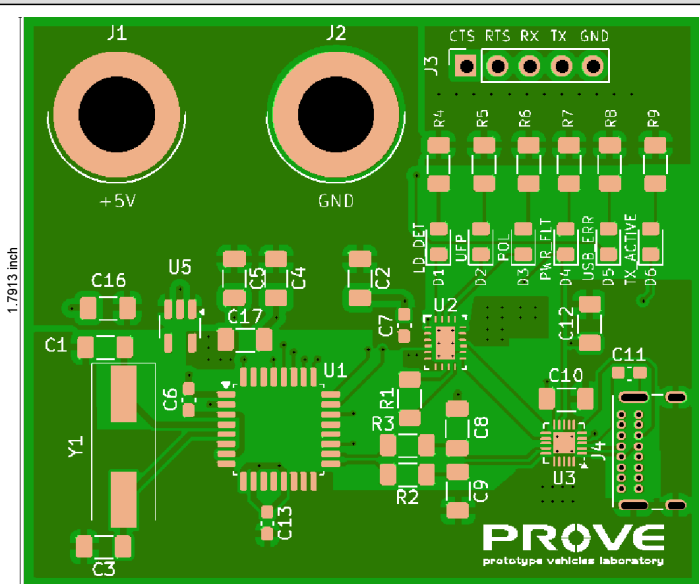
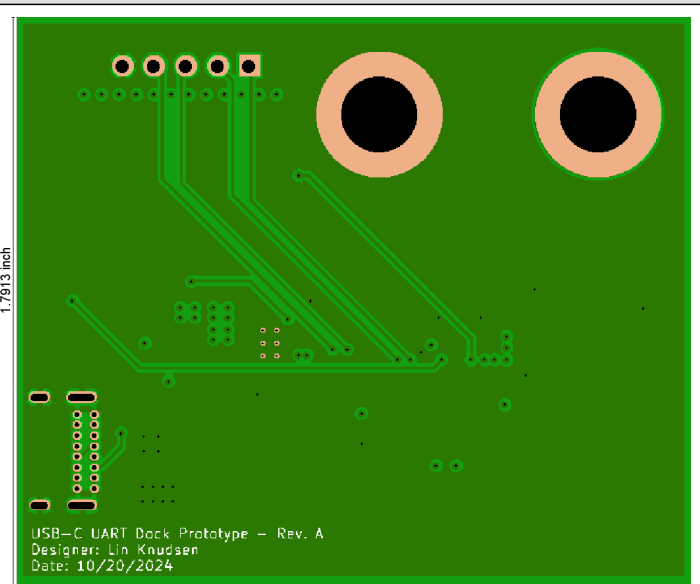


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Single PCB View - Original

Top View	Bottom View
	

Summary - General - Original

PCB Size	2.128 inch x 1.791 inch	Customer Panel Size	
PCB Thickness	62.787 mil	Max. Aspect Ratio on PTH	7.9
Copper Layers	4	Pressing Stages	1
Surface Finish	None	Drill Hole Density	26 Holes/inch ²
Solder Mask	Both	Testable Points	211
Solder Mask Color	R128G128B128	Min. SMD/BGA Size	7.87 mil
Legend	Both	Via in Pad	Yes
Legend Color	White	Stacked Vias	
Edge Connector Area	0 inch ²	Castellated	No
Peeloff Mask	No	Anomalies	Yes
Carbon Mask	No		

Summary - Copper Layer Minima - Original

Type	Copper Width	Critical Copper Width	Trace Width	Critical Trace Width	Copper to Copper Clr.	Trace to Trace Clr.	Same Net Clr.	Ring	Copper to Plated Clr.	Copper to NPTH Clr.	Copper to Outline Clr.
	mil	mil	mil	mil	mil	mil	mil	mil	mil	mil	mil
Outer	¹ 7.87	² 7.87	³ 7.87	⁴ 7.87	⁵ 5.90	⁶ 7.87	⁷ 5.56	⁸ 5.84	⁹ 11.84	¹⁰ 19.70	
Inner	¹¹ 5.32	¹² 8.25	¹³ 11.81	¹⁴ >16.00	¹⁵ 5.90	¹⁶ >32.00	¹⁷ 0.79	¹⁸ 5.84	¹⁹ 11.84	²⁰ 19.70	

Summary - Sequences - Original

Type	Sequences	Tools	Min. End Dia.	Max. End Dia.	Holes	Routs	Ring on Outer	Ring on Inner	Hole to Copper Clr.
			mil	mil			mil	mil	mil
PTH	1	7	7.90	240.20	99	4	5.84	5.84	11.84
Total	1	7	7.90	240.20	99	4	5.84	5.84	11.84

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Solder Mask - Original

Side	Mask to Mask Clr.	Web	Ring on Cu Defined Pads	Ring on SM Defined Pads	Mask to Copper Clr.	Mask Opening	Fully Covered Via Holes	Partly Covered Via Holes	One Side Covered Vias	Both Sides Covered Vias	No Side Covered Vias
	mil	mil	mil	mil	mil	mil					
Top	5.90	7.87	>10.00	>10.00	5.90	7.87	Yes	No			
Bottom	5.90	>10.00	>10.00	>10.00	5.90	19.69	Yes	No			
Both	5.90	7.87	>10.00		5.90	7.87	Yes	No	Yes	Yes	Yes

Stackup - Original

legend

soldermask

1

2

3

4

soldermask

legend

USBC_UART_Dock_Prototype-F_Silkscreen_gbr

USBC_UART_Dock_Prototype-F_Mask_gbr

2.098 USBC_UART_Dock_Prototype-F_Cu_gbr

7.5 FR4

1.402 USBC_UART_Dock_Prototype-In1_Cu_gbr

40 FR4

1.402 USBC_UART_Dock_Prototype-In2_Cu_gbr

7.5 FR4

2.098 USBC_UART_Dock_Prototype-B_Cu_gbr

USBC_UART_Dock_Prototype-B_Mask_gbr

USBC_UART_Dock_Prototype-B_Silkscreen_gbr

USBC_UART_Dock_Prototype-PTH_drl

Pressing Stages

1

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Summary Minimum Design Characteristics - Locations - Original

1



USBC_UART_Dock_Prototype-F_Cu_gbr
⊕ x 5805.97 mil
y -3342.03 mil

**Copper Width
Outer Layers**
7.87 mil



100 mil

2



USBC_UART_Dock_Prototype-F_Cu_gbr
⊕ x 6139.1 mil
y -3706.58 mil

**Critical Copper Width
Outer Layers**
7.87 mil



100 mil

3



USBC_UART_Dock_Prototype-F_Cu_gbr
⊕ x 6183.43 mil
y -3688.98 mil

**Trace Width
Outer Layers**
7.87 mil



100 mil

4



USBC_UART_Dock_Prototype-F_Cu_gbr
⊕ x 6307.46 mil
y -3720.47 mil

**Critical Trace Width
Outer Layers**
7.87 mil



100 mil

5



USBC_UART_Dock_Prototype-F_Cu_gbr
⊕ x 6410.43 mil
y -3814.47 mil

**Copper to Copper Clr.
Outer Layers**
5.90 mil



100 mil

6



USBC_UART_Dock_Prototype-F_Cu_gbr
⊕ x 6155.46 mil
y -3712.6 mil

**Trace to Trace Clr.
Outer Layers**
7.87 mil



100 mil

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7

USBC_UART_Dock_Prototype-B_Cu_gbr

⊕ x 5751.97 mil
y -3409.45 mil

Same Net Clr.
Outer Layers
5.56 mil

100 mil

8

USBC_UART_Dock_Prototype-F_Cu_gbr

⊕ x 5834.65 mil
y -3412.89 mil

Ring
Outer Layers
5.84 mil

50 mil

9

USBC_UART_Dock_Prototype-F_Cu_gbr

⊕ x 6410.43 mil
y -3811.5 mil

Copper to Plated Clr.
Outer Layers
11.84 mil

100 mil

10

USBC_UART_Dock_Prototype-F_Cu_gbr

⊕ x 5153.43 mil
y -4124.01 mil

Copper to Outline Clr.
Outer Layers
19.70 mil

100 mil

11

USBC_UART_Dock_Prototype-In1_Cu_gbr

⊕ x 6463.58 mil
y -3572.43 mil

Copper Width
Inner Layers
5.32 mil

100 mil

12

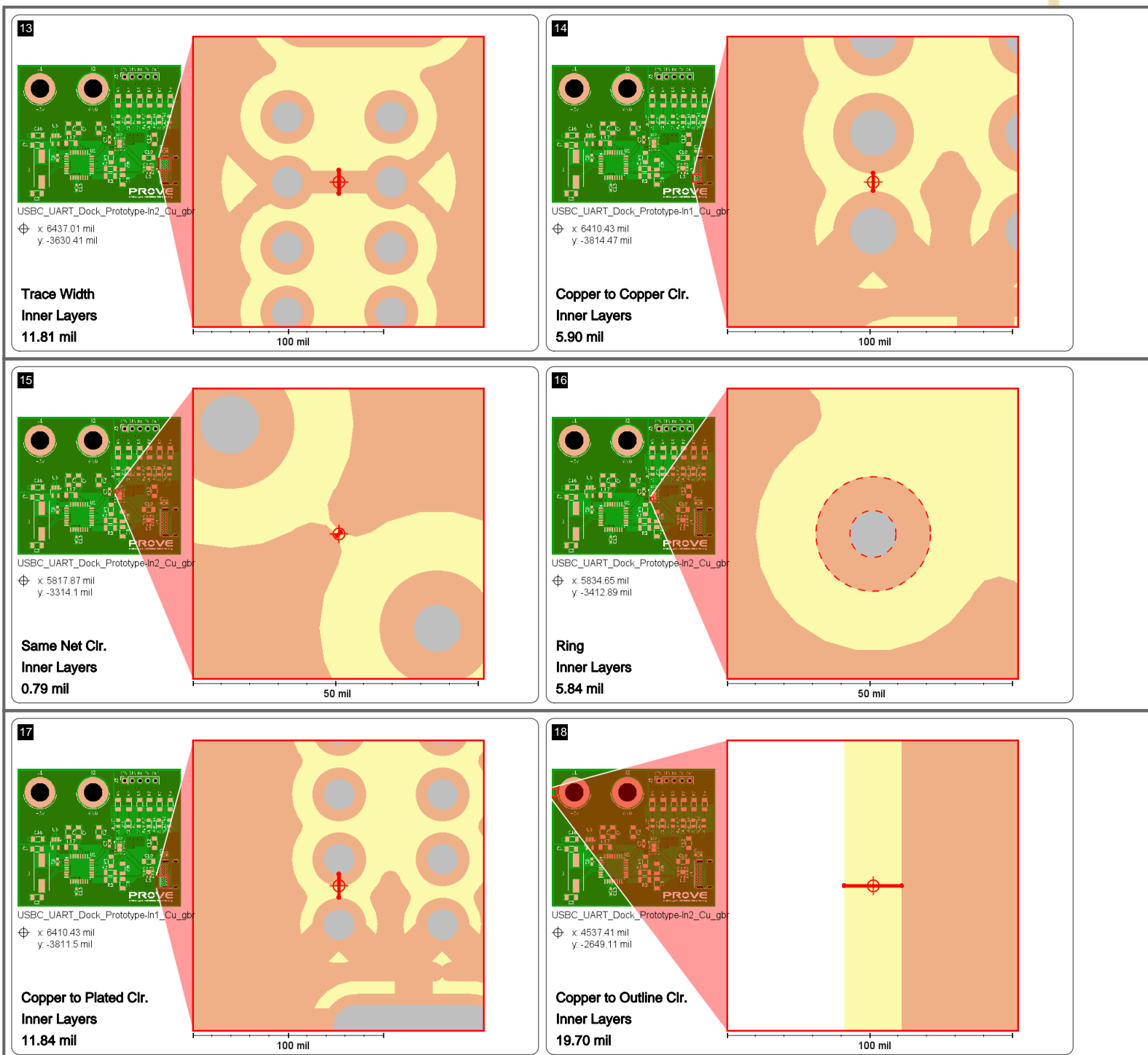
USBC_UART_Dock_Prototype-In2_Cu_gbr

⊕ x 6390.12 mil
y -3784.49 mil

Critical Copper Width
Inner Layers
8.25 mil

100 mil

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Copper Layer Minima & Area - Original

File	Pos.	Copper Width	Critical Copper Width	Trace Width	Critical Trace Width	Copper to Copper Clr.	Same Net Clr.	Copper Area	
		mil	mil	mil	mil	mil	mil	inch ²	%
USBC_UART_Dock_Prototype-F_Cu_gbr	1	7.87	7.87	7.87	7.87	5.90	8.38	2.6044	68
USBC_UART_Dock_Prototype-In1_Cu_gbr	2	5.32	>16.00	>16.00	>16.00	5.90	5.56	3.5656	94
USBC_UART_Dock_Prototype-In2_Cu_gbr	3	8.25	8.25	11.81	>16.00	5.90	0.79	3.5216	92
USBC_UART_Dock_Prototype-B_Cu_gbr	4	10.00	10.00	10.00	10.00	5.90	5.56	3.4046	89

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Copper Layer Minima - Copper to Drill Minima - Original






File	Pos.	Ring					Copper to Drill Clr.		Copper to Outline Clr.			
		Overall	Via	Laser Via	Comp.	Mech.	Plated	NPTH	Overall	to Pad	to Trace	to Region
		mil	mil	mil	mil	mil	mil	mil	mil	mil	mil	mil
USBC_UART_Dock_Prototype-F_Cu_gbr	1	5.84	5.84		5.85		11.84		19.70	39.96	>64.00	19.70
USBC_UART_Dock_Prototype-In1_Cu_gbr	2	5.85	6.00		5.85		11.84		19.70	39.96	>64.00	19.70
USBC_UART_Dock_Prototype-In2_Cu_gbr	3	5.84	5.84		5.85		11.84		19.70	39.96	>64.00	19.70
USBC_UART_Dock_Prototype-B_Cu_gbr	4	5.85	6.00		5.85		11.84		19.70	39.96	>64.00	19.70

Drill Tools - Original

File	Tool Nr.	Span	Type	Function	Method	Filled Via	Counter	Dia.	Tol. -	Tol. +	Holes in PCB	Routs in PCB	Double Hits	Predrill Hits
								mil	mil	mil				
USBC_UART_Dock_Prototype-PTH_drl	1	1-4	PTH	via	mech.	unknown	unknown	7.90	0.00	0.00	4	0	0	0
USBC_UART_Dock_Prototype-PTH_drl	2	1-4	PTH	via	mech.	unknown	unknown	8.00	0.00	0.00	6	0	0	0
USBC_UART_Dock_Prototype-PTH_drl	3	1-4	PTH	via	mech.	unknown	unknown	10.00	0.00	0.00	66	0	0	0
USBC_UART_Dock_Prototype-PTH_drl	4	1-4	PTH	comp.	mech.	unknown	unknown	15.70	0.00	0.00	16	0	0	0
USBC_UART_Dock_Prototype-PTH_drl	5	1-4	PTH	comp.	mech.	unknown	unknown	23.60	0.00	0.00	0	4	0	0
USBC_UART_Dock_Prototype-PTH_drl	6	1-4	PTH	comp.	mech.	unknown	unknown	39.40	0.00	0.00	5	0	0	0
USBC_UART_Dock_Prototype-PTH_drl	7	1-4	PTH	comp.	mech.	unknown	unknown	240.20	0.00	0.00	2	0	0	0

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Drill Tools - Drill vs Copper - Original

File	Tool Nr.	Span	Type	Function	Method	Dia.	Ring on Outer	Ring on Inner	Min. Pad Size	Via in Pad	Plated to Copper Clr. 			
											Overall	to Pad	to Trace	to Region
						mil	mil	mil	mil		mil	mil	mil	mil
USBC_UART_Dock_Prototype-PTH_drl	1	1-4	PTH	via	mech.	7.90	5.89	5.89	19.68	4	14.63	14.63	26.76	15.91
USBC_UART_Dock_Prototype-PTH_drl	2	1-4	PTH	via	mech.	8.00	5.84	5.84	19.68	6	15.86	18.84	25.38	15.86
USBC_UART_Dock_Prototype-PTH_drl	3	1-4	PTH	via	mech.	10.00	6.00	6.00	22.00	0	13.89	16.00	16.00	13.89
USBC_UART_Dock_Prototype-PTH_drl	4	1-4	PTH	comp.	mech.	15.70	5.93	5.93	27.56		11.84	11.84	15.93	15.95
USBC_UART_Dock_Prototype-PTH_drl	5	1-4	PTH	comp.	mech.	23.60	unknown	unknown	> 87.60		unknown	unknown	unknown	unknown
USBC_UART_Dock_Prototype-PTH_drl	6	1-4	PTH	comp.	mech.	39.40	13.76	13.76	66.92		23.78	>32.00	>32.00	23.78
USBC_UART_Dock_Prototype-PTH_drl	7	1-4	PTH	comp.	mech.	240.20	>32.00	>32.00	> 304.20		>32.00	>32.00	>32.00	>32.00

Sequences - Original

Span	Type	Tools	Min. End Dia.	Max. End Dia.	Holes	Ring on Outer	Ring on Inner	Hole to Copper Clr.	Hole to Outline Clr.	Slot to Outline Clr.
			mil	mil		mil	mil	mil	mil	mil
1-4	PTH	7	7.90	240.20	99	5.84	5.84	11.84	133.35	45.91
All	All	7	7.90	240.20	99	5.84	5.84	11.84	133.35	45.91

Rout Tools - Original

File	Tool Nr.	Type	Tool Dia.	End Dia.	Rout Length	Nibble Count
			mil	mil	mil	
USBC_UART_Dock_Prototype-PTH_drl	5	PTH	0.00	23.60	181.00	100

Routed Holes - Original

File	Hole Nr.	Instances	X Size	Y Size	Rout Length	Nibble Count
			mil	mil	mil	
USBC_UART_Dock_Prototype-PTH_drl	1	2	82.60	23.60	59.00	33
USBC_UART_Dock_Prototype-PTH_drl	2	2	55.10	23.60	31.50	17
All		4			90.50	50

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Files - Original

Initial	Renamed	Function	Position	Color	Thickness	
					Base	Finished
					mil	mil
USBC_UART_Dock_Prototype-F_Paste.gbr		paste	top			
USBC_UART_Dock_Prototype-F_Silkscreen.gbr		silk	top	white	unknown	unknown
USBC_UART_Dock_Prototype-F_Mask.gbr		mask	top	r128g128b128	unknown	unknown
USBC_UART_Dock_Prototype-F_Cu.gbr		outer	1		unknown	unknown
USBC_UART_Dock_Prototype-In1_Cu.gbr		inner	2		unknown	unknown
USBC_UART_Dock_Prototype-In2_Cu.gbr		inner	3		unknown	unknown
USBC_UART_Dock_Prototype-B_Cu.gbr		outer	4		unknown	unknown
USBC_UART_Dock_Prototype-B_Mask.gbr		mask	bottom	r128g128b128	unknown	unknown
USBC_UART_Dock_Prototype-B_Silkscreen.gbr		silk	bottom	white	unknown	unknown
USBC_UART_Dock_Prototype-PTH.drl		plated	1-4			
USBC_UART_Dock_Prototype-B_Paste.gbr		empty	none			
USBC_UART_Dock_Prototype-Edge_Cuts.gbr		cad_outline	none			
USBC_UART_Dock_Prototype-NPTH.drl		empty	none			

Input Remarks - Original

Gerber import: Invalid coincident draw, continuing without cleanup 'USBC_UART_Dock_Prototype-B_Cu.gbr'
Gerber import: Invalid contour, continuing with an interpretation. Cannot be cleaned up automatically. Must be cleaned up manually. 'USBC_UART_Dock_Prototype-B_Cu.gbr' (at line 3138)
Gerber import: Invalid coincident draw, continuing without cleanup 'USBC_UART_Dock_Prototype-F_Cu.gbr'
Gerber import: Self-intersecting contours are detected, continuing with an interpretation of the contours. 'USBC_UART_Dock_Prototype-F_Cu.gbr' (at line 2076)
Gerber import: Invalid contour, continuing with an interpretation. Cannot be cleaned up automatically. Must be cleaned up manually. 'USBC_UART_Dock_Prototype-F_Cu.gbr' (at line 3225)
Gerber import: Invalid coincident draw, continuing without cleanup 'USBC_UART_Dock_Prototype-In1_Cu.gbr'
Gerber import: Invalid contour, continuing with an interpretation. Cannot be cleaned up automatically. Must be cleaned up manually. 'USBC_UART_Dock_Prototype-In1_Cu.gbr' (at line 3328)
Gerber import: Invalid coincident draw, continuing without cleanup 'USBC_UART_Dock_Prototype-In2_Cu.gbr'
Gerber import: Self-intersecting contours are detected, continuing with an interpretation of the contours. 'USBC_UART_Dock_Prototype-In2_Cu.gbr' (at line 3020)
External import: Empty image generated. 'USBC_UART_Dock_Prototype-NPTH.drl' (at line 13)
DISCREPANCY: Extra bottom layers mismatch between Gerber Job File and current job stackup.
OMITTED: \$.MaterialStackup[11] not added to layer attributes because corresponding layer could not be found.

Todo's - Original

Please check the image size of drill layer 'USBC_UART_Dock_Prototype-NPTH.drl'
Please check the image size of drill layer 'USBC_UART_Dock_Prototype-PTH.drl'

Comments - Original

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