

# P.SAI VENKAT RAMANA

Bachelor of Technology - ECE

+91-+91 9182709036 , DOB : 12/02/2005

PUSARLA SAI VENKAT RAMANA

psvr120205@gmail.com

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## EDUCATION

<b>Bachelor of Technology in Electronics and Communication Engineering ( VLSI )</b>	2023 - 2027
Koneru Lakshmaiah University, Vaddeswaram, Vijayawada, CGPA 9.72	
<b>Bachelor of Business Administration</b>	2024 - 2027
Koneru Lakshmaiah University, Vaddeswaram, Vijayawada, CGPA 9.00	
<b>Higher Secondary Education in MPC</b>	2021 - 2023
Tirumala Junior College Tallavalasa, Visakhapatnam 96.2%	
<b>Secondary School Education (10th Standard)</b>	2020 - 2021
Keshava Reddy Concept School Etcherla, Srikakulam 100%	

## PROJECTS

### •Interactive Arduino Games

Designing Engaging Hardware-Based Games Using Arduino.

- Developed interactive games using Arduino, integrating LEDs, buzzers, buttons, LCDs, and sensors for real-time user interaction.

### •SPI Slave Implementation

Verilog-Based SPI Slave Module for Reliable FPGA Communication.

- Designed and implemented an SPI slave module using Verilog HDL for FPGA-based systems.

### •Increasing the voltage levels of Read / Write data in SRAM

Increase SRAM VDD in Cadence, verify model compatibility, and adjust timing parameters.

- Increase SRAM VDD in Cadence and ensure that SRAM model supports higher voltage.
- Recalculate setup, hold, and access times.

### •8-Bit Adder Using SystemVerilog

Digital Arithmetic Design Using SystemVerilog.

- Designed an 8-bit binary adder using SystemVerilog for digital logic applications.
- Verified functionality through simulation and testbench validation..

## CERTIFICATIONS

### •Embedded Systems & IoT Programming

TARAS Solutions

- Gained practical experience in embedded system development and IoT-based application design.
- Worked with sensors, microcontrollers, and communication protocols for real-time systems.

### •Salesforce AI Associate – Certified

Salesforce

- Learned fundamentals of Artificial Intelligence within the Salesforce ecosystem.
- Understood ethical AI, data handling, and AI-driven business use cases.

### •Spectre Simulator Fundamentals S1: Spectre Basics (vSPECTRE23.1)

Cadence

- Learned analog circuit simulation using Cadence Spectre Simulator.
- Gained understanding of simulation setup, analysis types, and waveform interpretation.

## •Artificial Intelligence and Machine Learning Using Python (Industrial Automation – L1)

- Applied Python-based AI and ML techniques for industrial automation scenarios.
- Learned basics of data analysis, model training, and automation workflows.

## •Proficient in MS Office & C Language Basics

- Skilled in documentation, data analysis, and presentations using MS Office tools.
- Strong foundation in C programming, including control structures, arrays, and functions.

## INTERNSHIP & TRAINING

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### •SystemVerilog Training

Abhyasa

Recent  
Training

- Built and verified an 8-bit adder using UVM methodology.
- Worked with SystemVerilog testbench design and verification concepts.
- Hands-on experience with Linux commands and Verilog-based design flow.

### •Verilog Design Training

Codetech IT Solutions

Recent  
Training

- Completed intermediate-level training in digital design using Verilog HDL.
- Gained practical knowledge of combinational and sequential circuit design.

## TECHNICAL SKILLS

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**Programming Languages:** C, Java(Basics), Verilog, SystemVerilog,

**Tools & :** Cadence, Multisim, Xilinx Vivado, Arduino IDE, EDA Playground

## POSITIONS

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### •Technical Team Member PULSE ECE, KL University

July 2024 – Present

- Led and supported technical sessions focused on IoT and Embedded Systems projects.
- Mentored peers and junior students, promoting collaborative learning and innovation.
- Organized and assisted in technical workshops to enhance hands-on engineering skills.