

# Pitta Venkateshwara Sai Kiran Reddy



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## PERSONAL DETAILS

Current Location	Hyderabad / Secunderabad
Date of birth	April 21, 2001
Gender	Male



## EDUCATION

Graduation	BE / B.Tech ( Electrical and Electronics ) Anurag Group of Institutions, Hyderabad with Score 7.47/10 Graduating in Jul 2022
Class XII	Other with 95.1% in 2018
Class X	Other with 83.6% in 2016
Certification Course	Introduction to Machine learning by andrew ng ETAP Power System Analysis For Electrical Engineers Digital Systems From Logic Gates to Processors



## INTERNSHIPS AND PROJECTS

Projects	<b>Pycompiler</b> This project is an attempt to make a python compiler for multi platform using python .I written this app using python. kivy, kivymd ,pygment libraries are used. It is a personal project for fun and to gain practice on OOp. Skills used - Python,OOPS,GUI <b>A Multiport Bidirectional DC to DC Converter</b> A bidirectional four port DC to DC converter is designed and simulated using MATLAB for integration of the hybrid renewable energy system to a DC micro grid. The converter uses the least number of semiconductor devices as possible. Skills used - Electronics,MATLAB,Simulink,PSpice,Electrical Engineering
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## SKILLS AND ACHIEVEMENTS

Skills	python,communication skills,problem solving,Analytical Skills,C++,Programming,Convincing Power,Hvdc,Python,MATLAB,Simulink,Excel,Word
Language	English ( Both ), Telugu ( Both ), Hindi ( Read/Write )