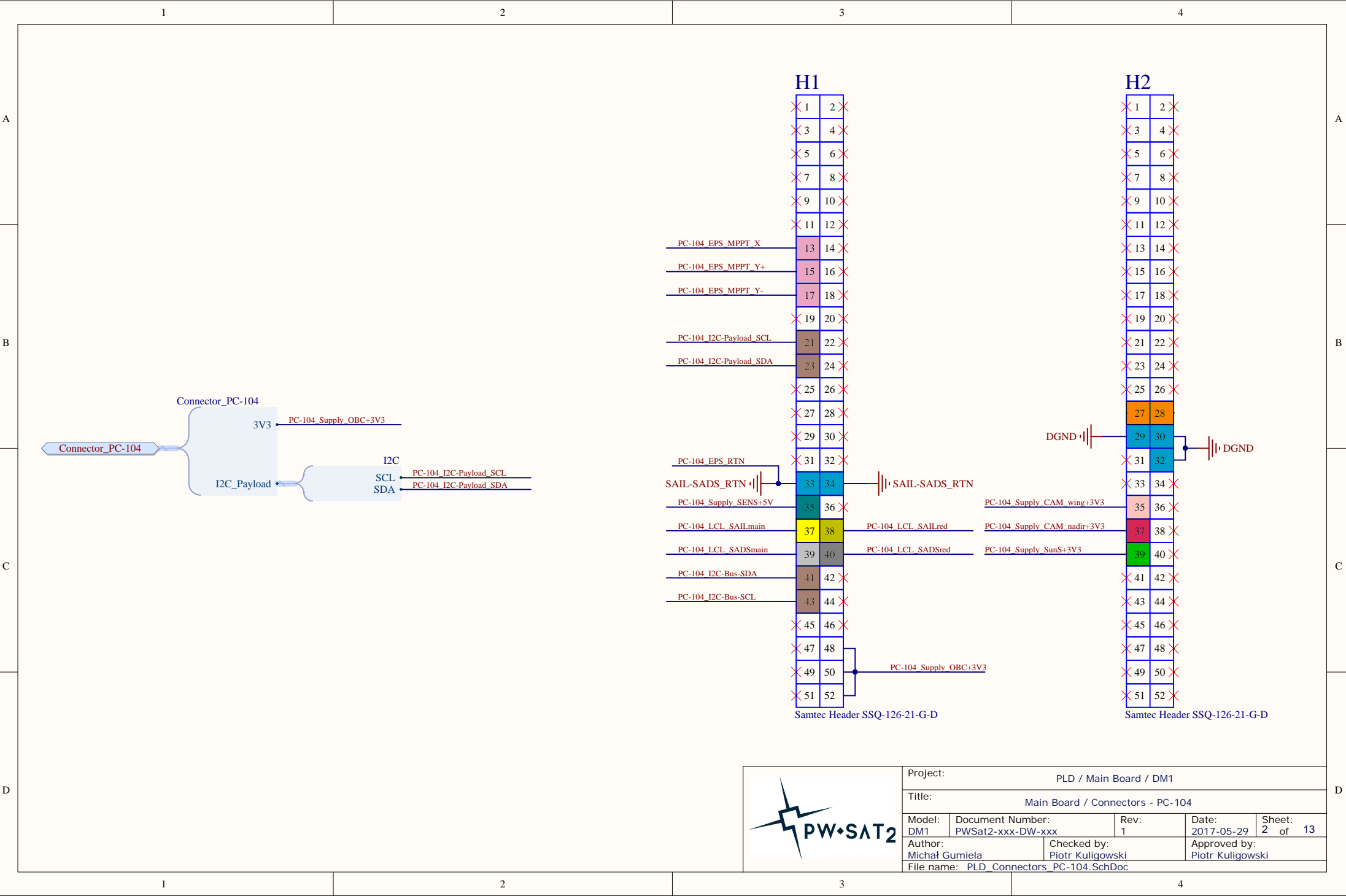
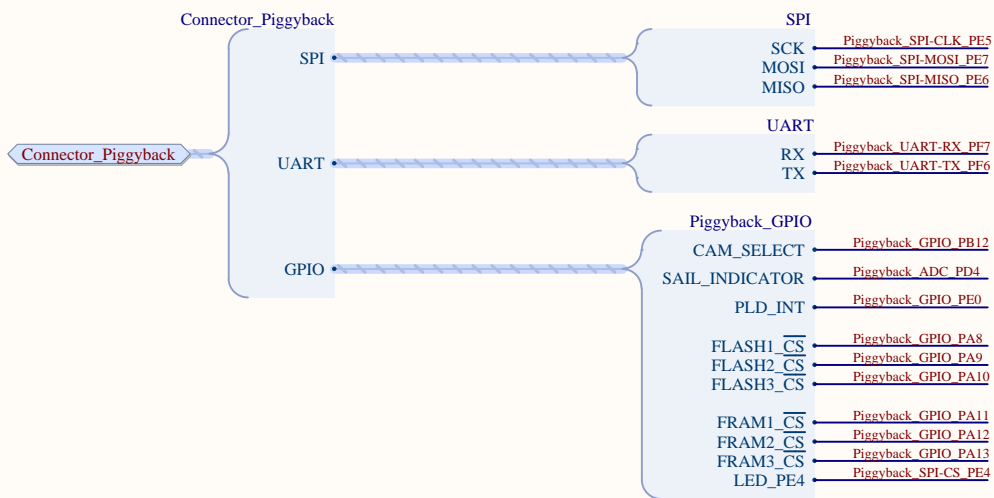


Project: PLD / Main Board / DM1					
Title: Main Board / Connectors					
Model: DM1	Document Number: PWSat2-xxx-DW-xxx	Rev: 1	Date: 2017-05-29	Sheet: 1 of 13	
Author: Michał Gumiela		Checked by: Piotr Kuligowski		Approved by: Piotr Kuligowski	
File name: PLD_Connectors.SchDoc					



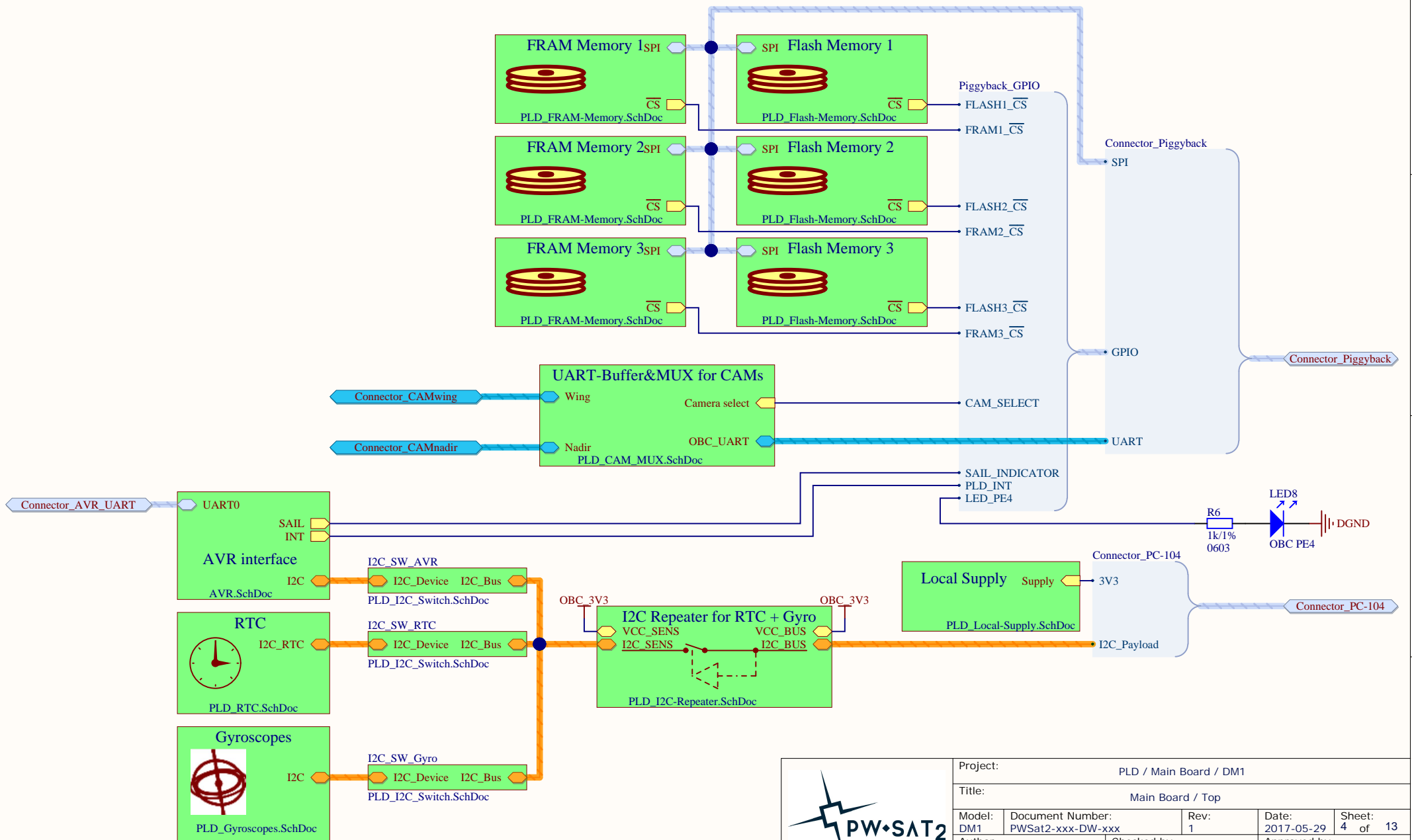


Piggyback_H1 Samtec Header TSW-115-xx-x-D			
×	1	2	×
×	3	4	×
×	5	6	×
Piggyback_UART-RX_PE7	7	8	×
Piggyback_UART-TX_PE6	9	10	×
Piggyback_SPI-CLK_PE5	11	12	Piggyback_GPIO_PA8
Piggyback_SPI-CS_PE4	13	14	Piggyback_GPIO_PA9
Piggyback_SPI-MOSI_PE7	15	16	Piggyback_GPIO_PA10
Piggyback_SPI-MISO_PE6	17	18	Piggyback_GPIO_PA11
×	19	20	Piggyback_GPIO_PA12
Piggyback_GPIO_PA13	21	22	Piggyback_GPIO_PB12
×	23	24	Piggyback_GPIO_PE0
Piggyback_ADC_PD4	25	26	Piggyback_ADC_PD5
×	27	28	×
×	29	30	×

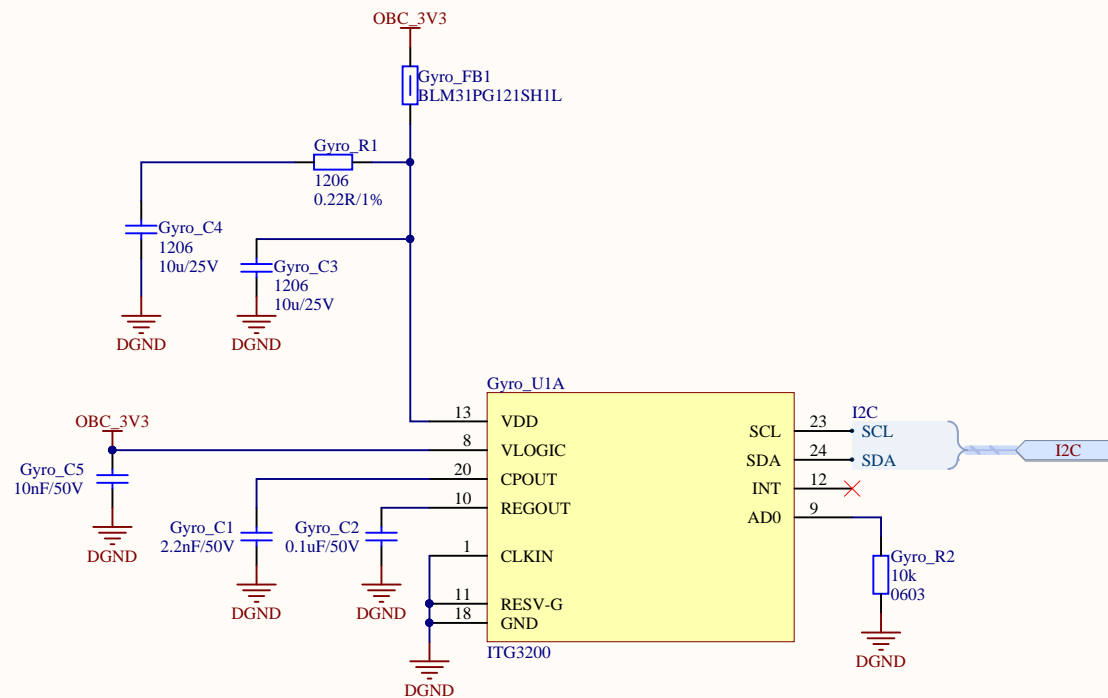
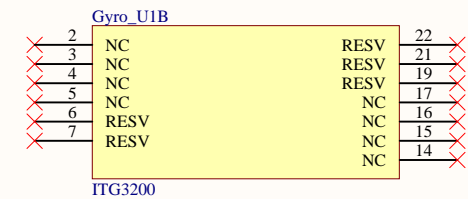
https://github.com/PW-Sat2/Documentation/raw/master/obc/obc_datasheet_esl_cubecomputer.pdf



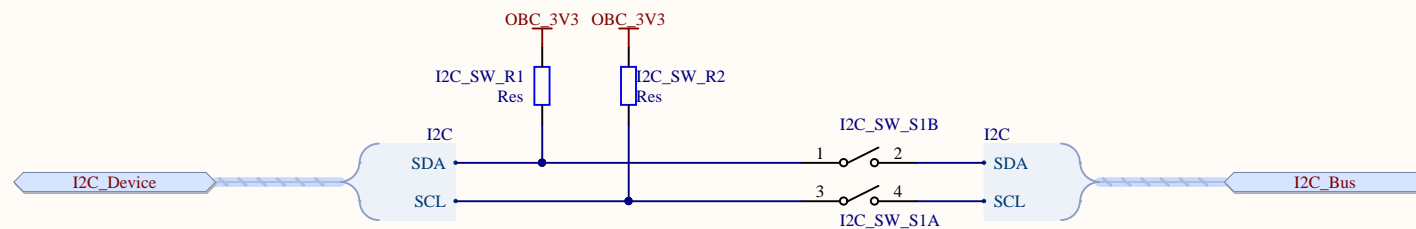
Project: PLD / Main Board / DM1			
Title: Main Board / Connectors - Piggyback			
Model: DM1	Document Number: PWSat2-xxx-DW-xxx	Rev: 1	Date: 2017-05-29
Author: Michał Gumieła		Checked by: Piotr Kuligowski	Sheet: 3 of 13
Approved by: Piotr Kuligowski		File name: PLD_Connectors_Piggyback.SchDoc	



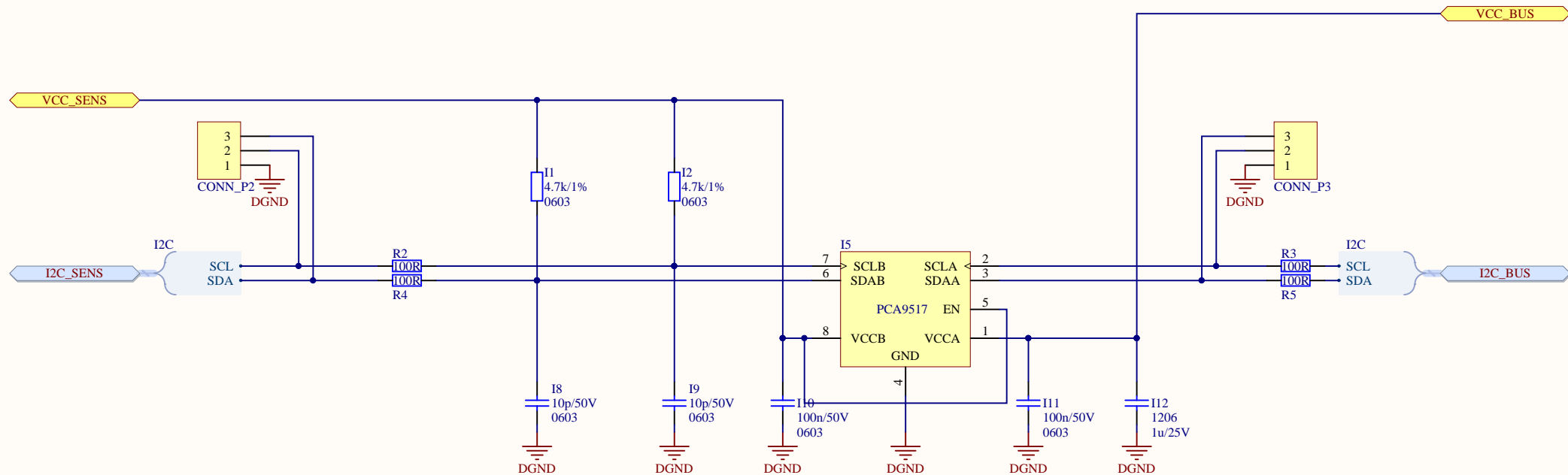
Project:		PLD / Main Board / DM1			
Title:		Main Board / Top			
Model: DM1	Document Number: PWSat2-xxx-DW-xxx	Rev: 1	Date: 2017-05-29	Sheet: 4 of 13	
Author: Michał Gumieła		Checked by: Piotr Kuligowski		Approved by: Piotr Kuligowski	
File name: PLD_Top.SchDoc					



Project: PLD / Main Board / DM1				
Title: Main Board / Gyroscopes				
Model: DM1	Document Number: PWSat2-xxx-DW-xxx	Rev: 1	Date: 2017-05-29	Sheet: 6 of 13
Author: Michał Gumiela		Checked by: Piotr Kuligowski	Approved by: Piotr Kuligowski	
File name: PLD_Gyroscopes_SchDoc				



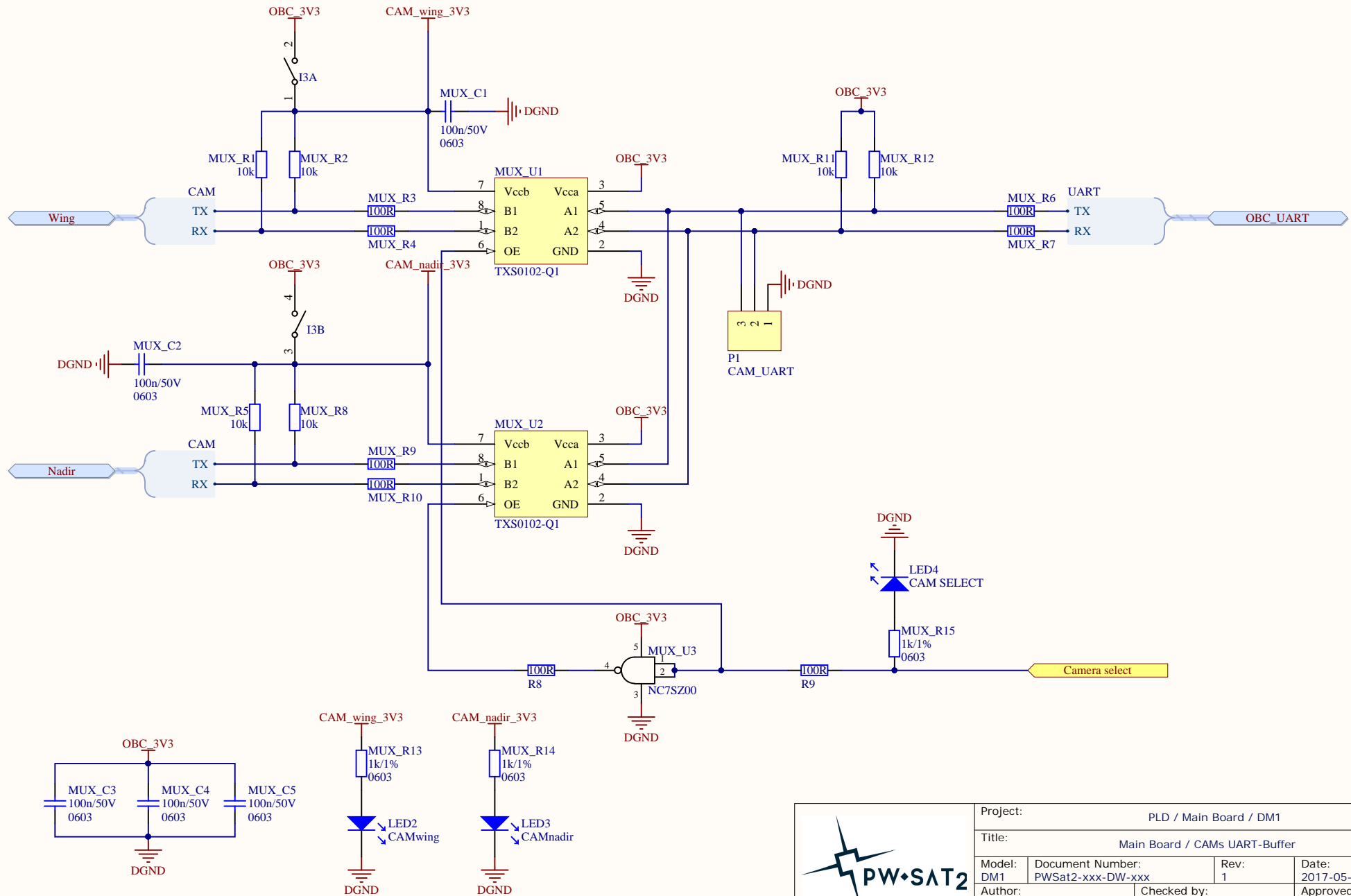
Project: PLD / Main Board / DM1				
Title: Main Board / I2C Switch				
Model: DM1	Document Number: PWSat2-xxx-DW-xxx	Rev: 1	Date: 2017-05-29	Sheet: 7 of 13
Author: Michał Gumiela		Checked by: Piotr Kuligowski	Approved by: Piotr Kuligowski	
File name: PLD_I2C_Switch.SchDoc				



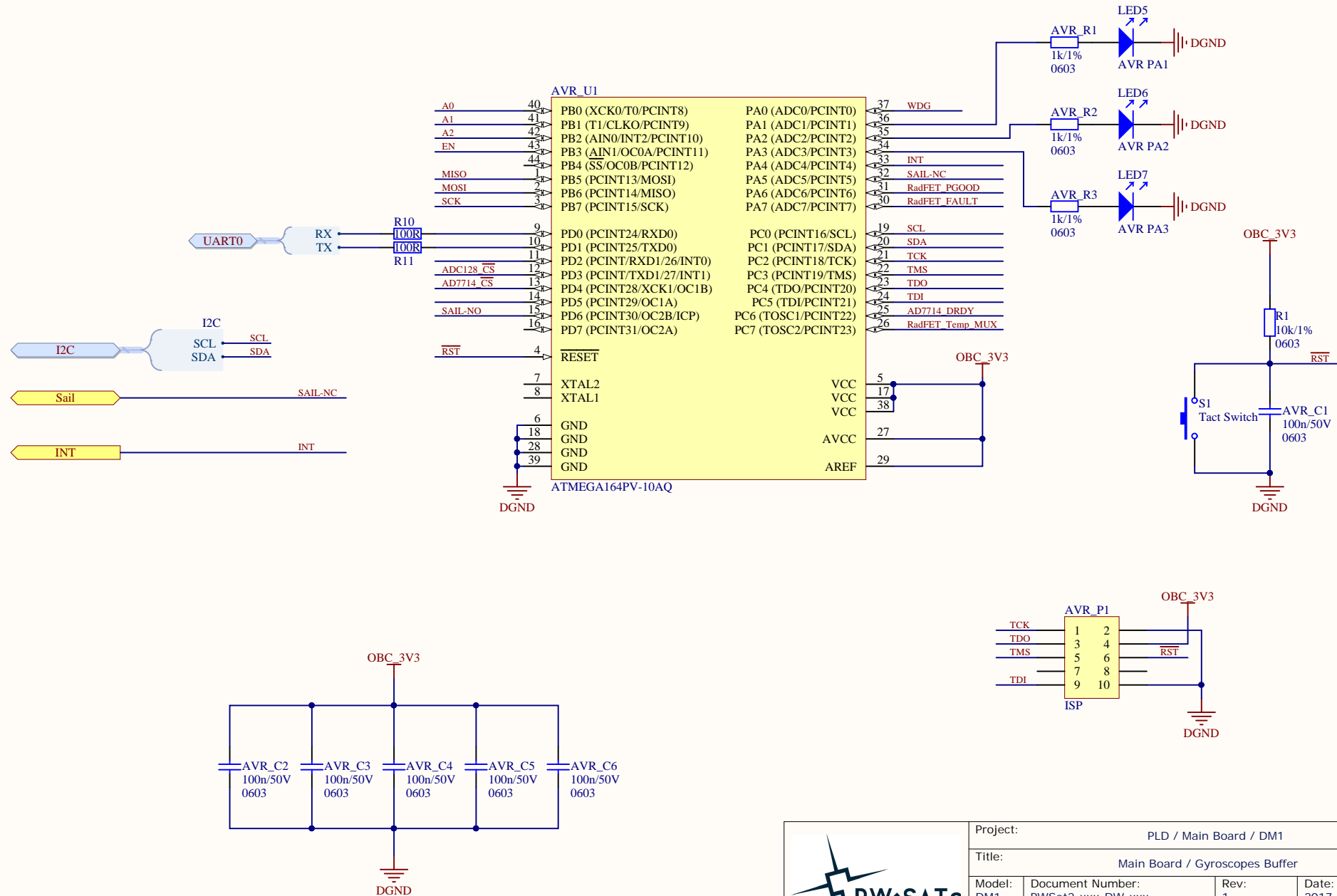
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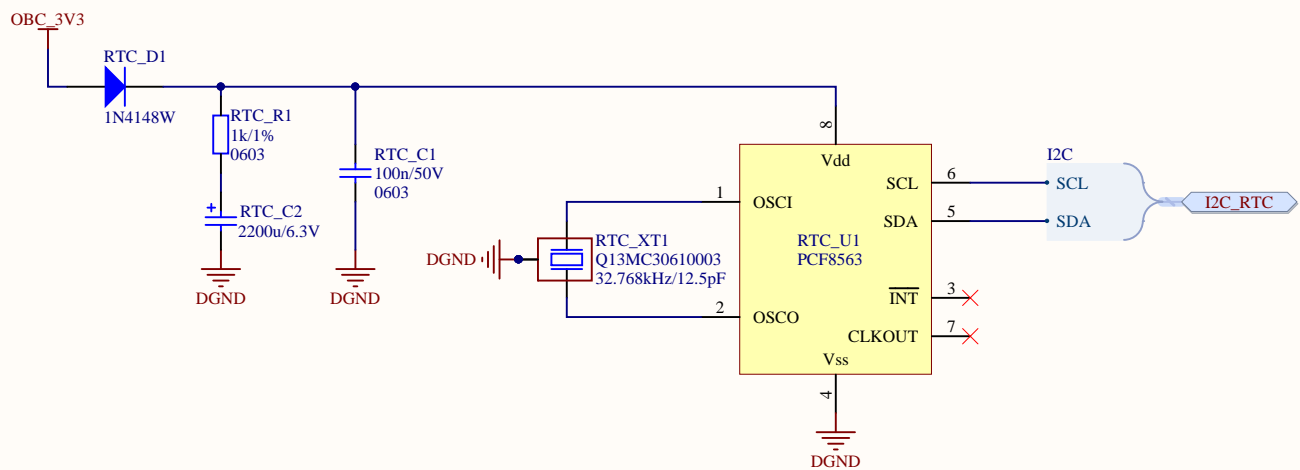
Project: PLD / Main Board / DM1				
Title: Main Board / I2C Repeater for RadFET				
Model: DM1	Document Number: PWSat2-xxx-DW-xxx	Rev: 1	Date: 2017-05-29	Sheet: 8 of 13
Author: Michał Gumiela		Checked by: Piotr Kuligowski	Approved by: Piotr Kuligowski	
File name: PLD_I2C-Repeater.SchDoc				



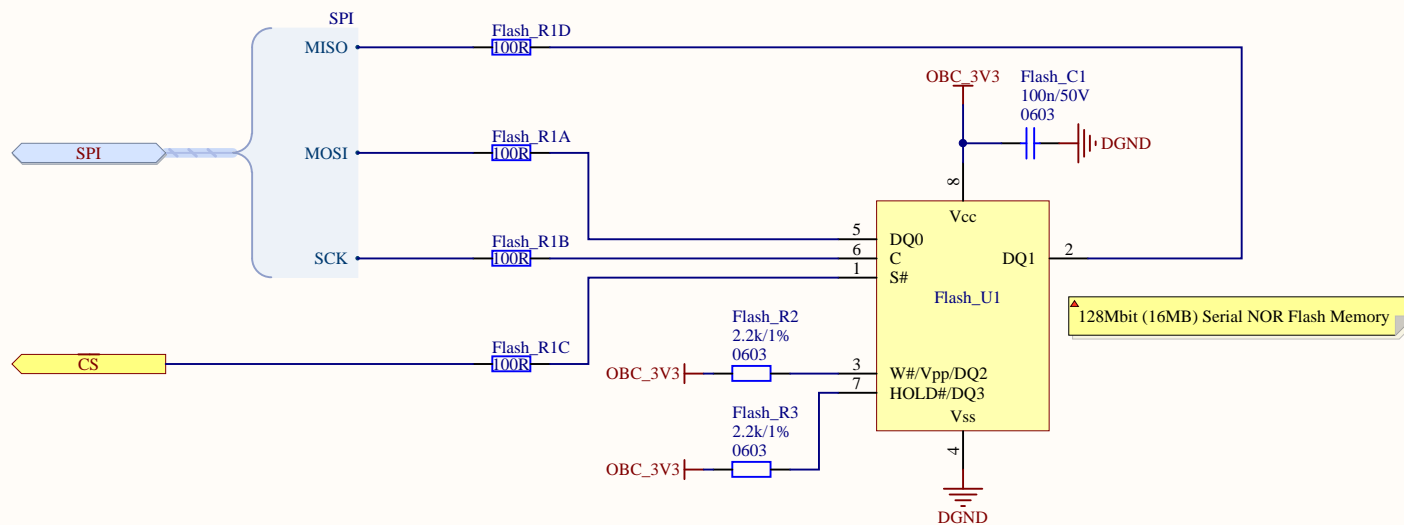
Project: PLD / Main Board / DM1				
Title: Main Board / CAMs UART-Buffer				
Model: DM1	Document Number: PWSat2-xxx-DW-xxx	Rev: 1	Date: 2017-05-29	Sheet: 9 of 13
Author: Michał Gumiela		Checked by: Piotr Kuligowski	Approved by: Piotr Kuligowski	
File name: PLD_CAM_MUX.SchDoc				



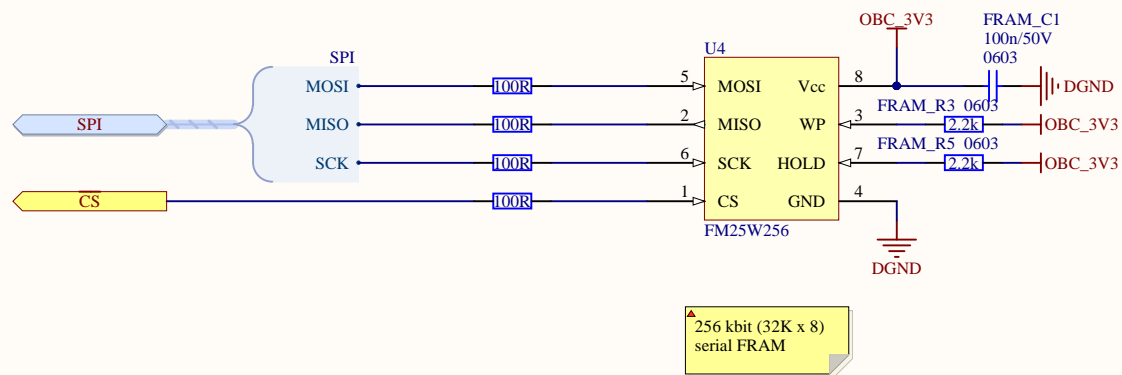
Project: PLD / Main Board / DM1				
Title: Main Board / Gyroscopes Buffer				
Model: DM1	Document Number: PWSat2-xxx-DW-xxx	Rev: 1	Date: 2017-05-29	Sheet: 10 of 13
Author: Michał Gumiela		Checked by: Piotr Kuligowski	Approved by: Piotr Kuligowski	
File name: AVR.SchDoc				



Project: PLD / Main Board / DM1				
Title: Main Board / RTC				
Model: DM1	Document Number: PWSat2-xxx-DW-xxx	Rev: 1	Date: 2017-05-29	Sheet: 11 of 13
Author: Michał Gumiela		Checked by: Piotr Kuligowski	Approved by: Piotr Kuligowski	
File name: PLD_RTC.SchDoc				



Project: PLD / Main Board / DM1				
Title: Main Board / Flash Memory				
Model: DM1	Document Number: PWSat2-xxx-DW-xxx	Rev: 1	Date: 2017-05-29	Sheet: 12 of 13
Author: Michał Gumiela		Checked by: Piotr Kuligowski	Approved by: Piotr Kuligowski	
File name: PLD_Flash-Memory.SchDoc				



Project: PLD / Main Board / DM1				
Title: Main Board / Flash Memory				
Model: DM1	Document Number: PWSat2-xxx-DW-xxx	Rev: 1	Date: 2017-05-29	Sheet: 13 of 13
Author: Michał Gumieła		Checked by: Piotr Kuligowski	Approved by: Piotr Kuligowski	
File name: PLD_FRAM-Memory.SchDoc				