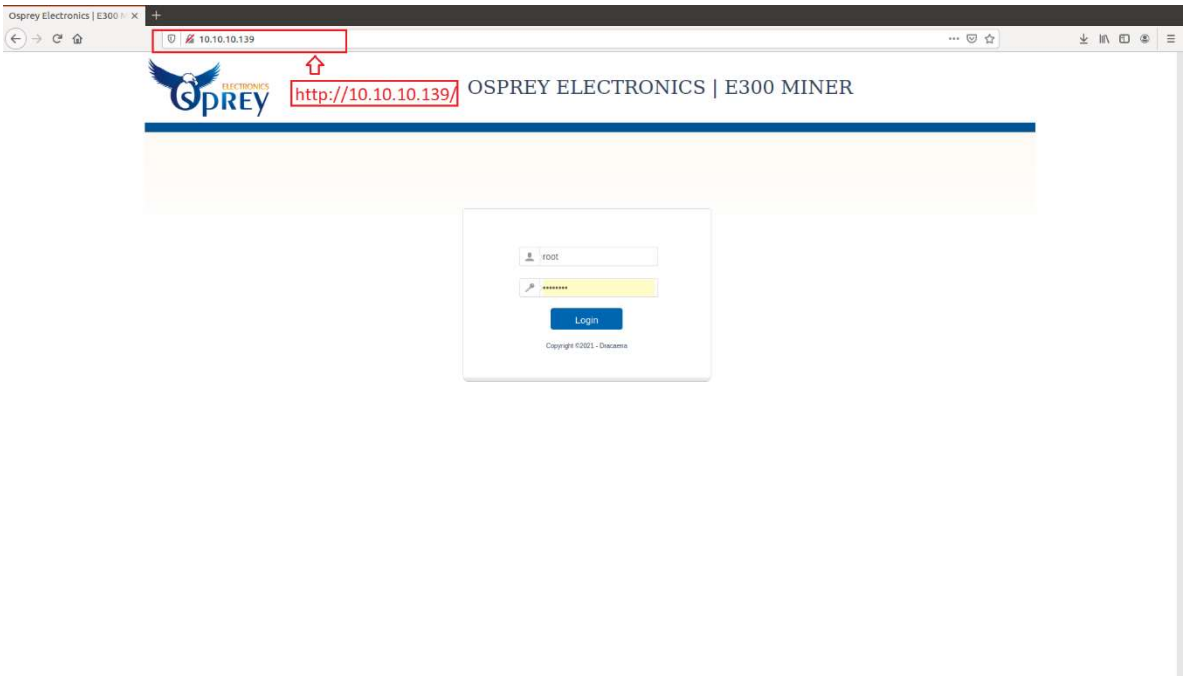
Q&A

1. How to access E300 WebUI?

   Step 1: Obtain your E300's IP address by logging into your network router and reviewing the DHCP leases for a device named ARM or installing an IP scanner tool at: (*https://www.advanced-ip-scanner.com*).

   Step 2: Once you have the E300's IP address, open a web browser (Chrome or Firefox are reommended) and insert this link in the address bar: http://E300_ip_address/ A login pop up will show up. Use the following credentials to log in:

User name: root Password: password



2. How to solve "Unrecognized comm data, skipping" issue?
   This issue comes from the vccInt value too high. Here is what you should do:

   Step 1: Stop mining tool (from "Miner" webpage)
   Step 2: Tweak vccInt value
   Please refer to the below table:

Isssue

```
#033[34;1m-------------------------------------------------------------------------------------------#033[0m
#033[34;1mMining kas with 3 FPGA workers#033[0m
#033[34;1mFPGA Board      DNA                                CoreMHz MemMHz TCore TMem VccInt VccBRAM VccMem Power#033[0m
#033[34;1m0    E335C      [redacted]                           0.0    0.0    0C   0C    0mV     0mV    0mV    0W#033[0m
#033[34;1m1    E335C      [redacted]                           0.0    0.0    0C   0C    0mV     0mV    0mV    0W#033[0m
#033[34;1m2    E335C      [redacted]                           0.0    0.0    0C   0C    0mV     0mV    0mV    0W#033[0m
#033[34;1m#033[0m
#033[34;1mStats Uptime: 0 days, 00:02:30#033[0m
#033[34;1mFPGA 0 [ 0C, fan  0%]      kas: 0.000 h/s, avg 0.000 h/s, pool 0.000 h/s a:0 r:0 er:0.00#033[0m
#033[34;1mFPGA 1 [ 0C, fan  0%]      kas: 0.000 h/s, avg 0.000 h/s, pool 0.000 h/s a:0 r:0 er:0.00#033[0m
#033[34;1mFPGA 2 [ 0C, fan  0%]      kas: 0.000 h/s, avg 0.000 h/s, pool 0.000 h/s a:0 r:0 er:0.00#033[0m
#033[34;1mTotal                      kas: 0.000 h/s, avg 0.000 h/s, pool 0.000 h/s a:0 r:0#033[0m
#033[34;1m---------------------------------------- Pool Status ----------------------------------------#033[0m
#033[34;1mpool.woolypooly.com        kas: 0.000 h/s, avg 0.000 h/s, pool 0.000 h/s a:0 r:0#033[0m
#033[34;1m-------------------------------------------------------------------------------------------#033[0m
Pool pool.woolypooly.com received new job. (job_id: 0008f560)
Pool pool.woolypooly.com received new job. (job_id: 0008f561)
FPGA 2 Bitstream loaded successfully.
FPGA 0 Bitstream loaded successfully.
FPGA 1 Bitstream loaded successfully.
Pool pool.woolypooly.com received new job. (job_id: 0008f562)
#033[33;1mFPGA 0 Received unrecognized comm data, skipping.#033[0m
#033[31;1mFPGA 2 Thread error (-401).#033[0m
#033[31;1mFPGA 0 Thread error (-901).#033[0m
Pool pool.woolypooly.com received new job. (job_id: 0008f563)
Pool pool.woolypooly.com received new job. (job_id: 0008f564)
#033[32;1mPool pool.woolypooly.com share accepted. (FPGA1) (a:1 r:0) (40 ms) (diff 105.59 GH)#033[0m
Pool pool.woolypooly.com received new job. (job_id: 0008f565)
Pool pool.woolypooly.com received new job. (job_id: 0008f566)
FPGA 2 Restarting device.
#033[31;1mFPGA 2 Failed to open device comms.  Will try again in 15s.#033[0m
FPGA 0 Restarting device.
#033[31;1mFPGA 0 Failed to open device comms.  Will try again in 15s.#033[0m
Pool pool.woolypooly.com received new job. (job_id: 0008f567)
```

ISSUE DUE TO VCCINT TOO HIGH

Solving issue

FPGA information

Index	Status	vccInt (mV)	vccHBM(mV)	Board Temperature °C)	Chip Temperature °C)	Maximum Temperature °C)
0	Power on	851	1203	23	31	95
1	Power on	850	1215	25	54	95
2	Power on	851	1225	24	31	95

Fans information

Currently level : **6**

Fans level	Capacity
10	100%
9	90%
8	80%
7	70%
6	60%
5	50%
4	40%
3	30%
2	20%
1	10%

the vccInt values too high, and it leads to the issue

Voltage settings (vccInt & vccHBM)

Select FPGA : All FPGA(s)

vccInt for FPGAs in mV (600mV - 950mV) : 635

new value

vccHBM for FPGAs in mV (1000mV - 1300mV) :

Change Voltage

click to save

Result

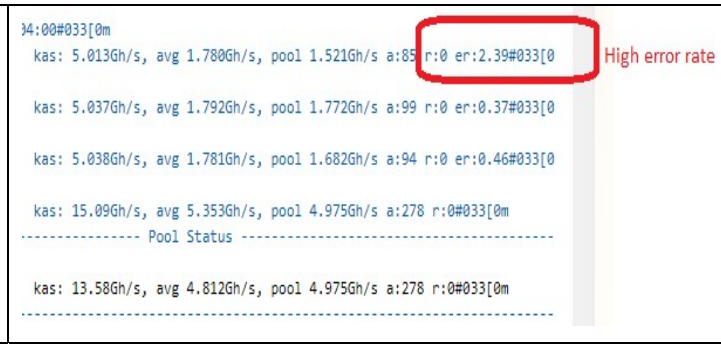
```
| #033[34;1m----------------------------------------------------------------------------------------------------#033[0m
| #033[34;1mMining kas with 3 FPGA workers#033[0m
| #033[34;1mFPGA Board     DNA                     CoreMHz MemMHz TCore TMem VccInt VccBRAM VccMem Power#033[0m
| #033[34;1m0    E335C                               620.0    0.0   51C   0C  616mV   846mV    0mV    0W#033[0m
| #033[34;1m1    E335C                               620.0    0.0   49C   0C  618mV   850mV    0mV    0W#033[0m
| #033[34;1m2    E335C                               620.0    0.0   49C   0C  614mV   850mV    0mV    0W#033[0m
| #033[34;1m#033[0m
| #033[34;1mStats Uptime: 0 days, 00:04:30#033[0m
| #033[34;1mFPGA 0 [51C, fan  0%]      kas: 4.936Gh/s, avg 2.185Gh/s, pool 1.765Gh/s a:16 r:0 er:0.00#033[0m
| #033[34;1mFPGA 1 [49C, fan  0%]      kas: 4.936Gh/s, avg 2.189Gh/s, pool 2.019Gh/s a:18 r:0 er:0.00#033[0m
| #033[34;1mFPGA 2 [49C, fan  0%]      kas: 4.935Gh/s, avg 2.186Gh/s, pool 1.415Gh/s a:13 r:0 er:0.00#033[0m
| #033[34;1mTotal                      kas: 14.81Gh/s, avg 6.561Gh/s, pool 5.200Gh/s a:47 r:0#033[0m
| #033[34;1m---------------------------------------- Pool Status ----------------------------------------#033[0m
| #033[34;1mpool.woolypooly.com        kas: 13.33Gh/s, avg 5.901Gh/s, pool 5.200Gh/s a:47 r:0#033[0m
| #033[34;1m----------------------------------------------------------------------------------------------------#033[0m
| Pool pool.woolypooly.com received new job. (job_id: 0008f9b2)
| Pool pool.woolypooly.com received new job. (job_id: 0008f9b3)
| Pool pool.woolypooly.com received new job. (job_id: 0008f9b4)
| Pool pool.woolypooly.com received new job. (job_id: 0008f9b5)
| #033[32;1mPool pool.woolypooly.com share accepted. (FPGA2) (a:48 r:0) (41 ms) (diff 719.63 GH)#033[0m
| #033[32;1mPool pool.woolypooly.com share accepted. (FPGA0) (a:49 r:0) (40 ms) (diff 145.27 GH)#033[0m
| Pool pool.woolypooly.com received new job. (job_id: 0008f9b6)
| #033[32;1mPool pool.woolypooly.com share accepted. (FPGA0) (a:50 r:0) (41 ms) (diff 46.88 GH)#033[0m
| Pool pool.woolypooly.com received new job. (job_id: 0008f9b7)
| Pool pool.woolypooly.com received new job. (job_id: 0008f9b8)
| #033[32;1mPool pool.woolypooly.com share accepted. (FPGA2) (a:51 r:0) (43 ms) (diff 55.62 GH)#033[0m
| #033[32;1mPool pool.woolypooly.com share accepted. (FPGA1) (a:52 r:0) (53 ms) (diff 95.93 GH)#033[0m
| #033[32;1mPool pool.woolypooly.com share accepted. (FPGA2) (a:53 r:0) (99 ms) (diff 164.62 GH)#033[0m
| #033[32;1mPool pool.woolypooly.com share accepted. (FPGA1) (a:54 r:0) (96 ms) (diff 233.94 GH)#033[0m
| Pool pool.woolypooly.com received new job. (job_id: 0008f9b9)
| Pool pool.woolypooly.com received new job. (job_id: 0008f9ba)
| Pool pool.woolypooly.com received new job. (job_id: 0008f9bb)
```

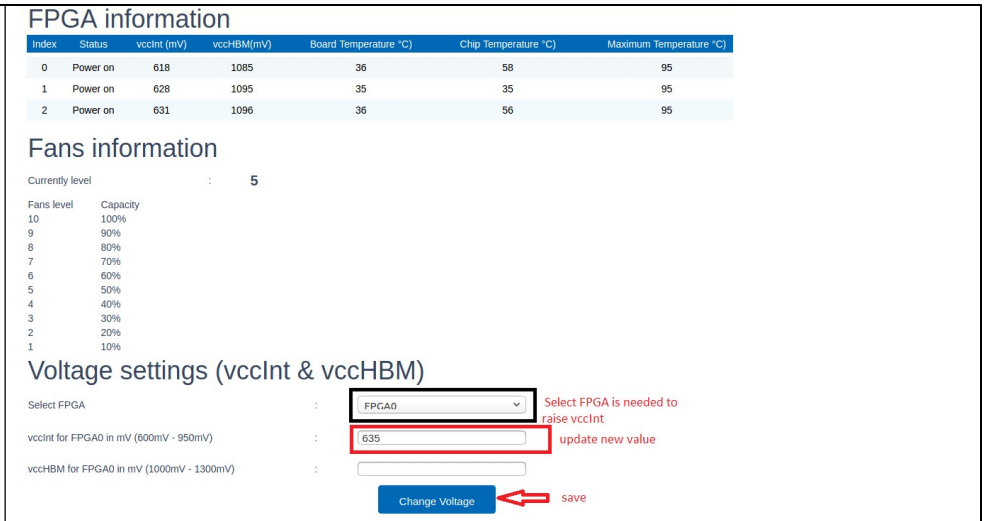
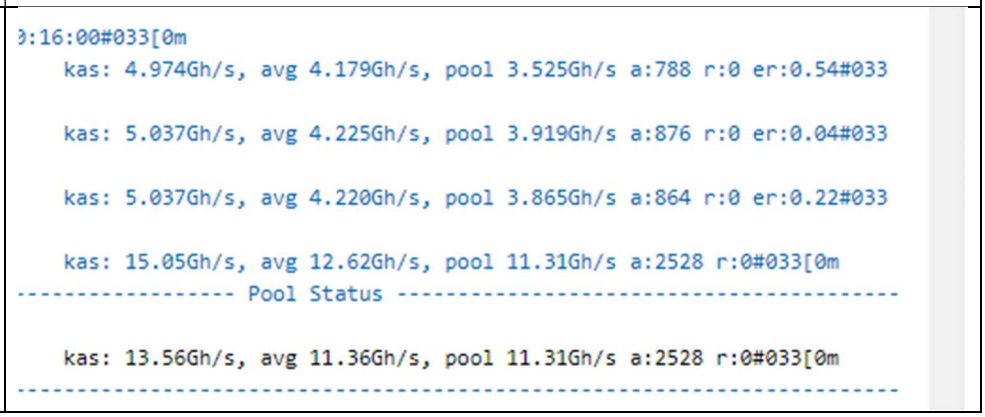
Algorithm	Clock	vccInt	vccHBM
kHeavyHash (Kaspa)	601 - 633 MHz	635 - 649 mV	Kaspa doesn't use HBM
	600 MHz	625- 635 mV	Kaspa doesn't use HBM
	550 MHz	600 mV	Kaspa doesn't use HBM

3. How to solve the high error issue?

The high error issue is relating to voltage. To solve it, we just need to increase several mV vccInt

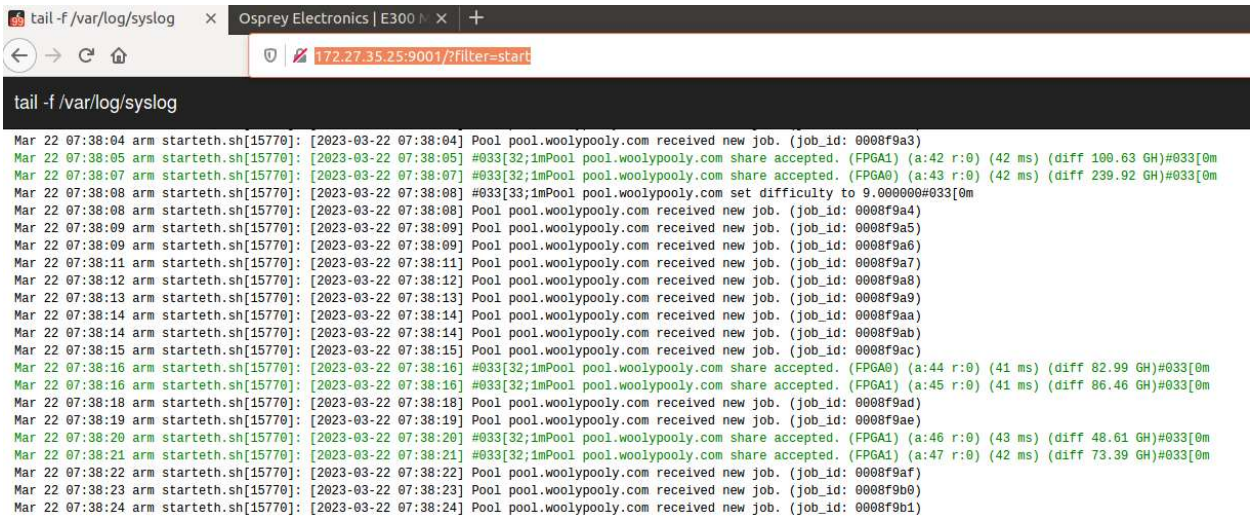
Assume that your FPGA0 get high error rate issue.



Solve by increasing vccInt of FPGA0	
Result	

4. Is there any way to monitoring streaming log without login?
   Yes, You monitor the streaming log by access.

   http://YOUR_E300_IP_ADDRESS:9001/?filter=start



5. What is the normal temperature while mining Kaspa?
   The normal chip temperature ranges from 60°C ~ 65 °C in the winter, and 60°C ~ 70°C in the summer.

6. How often should you clean the E300 miners?
   It is recommended that Ospreyers should clean hashing boards to remove dust and external objects every three (03) months. This task is not time-consuming and helps your boards work more reliable and long-lasting.

7. How can you contact us for support
   Join our discord at https://discord.gg/F86rAyYGNP