

## Q&A

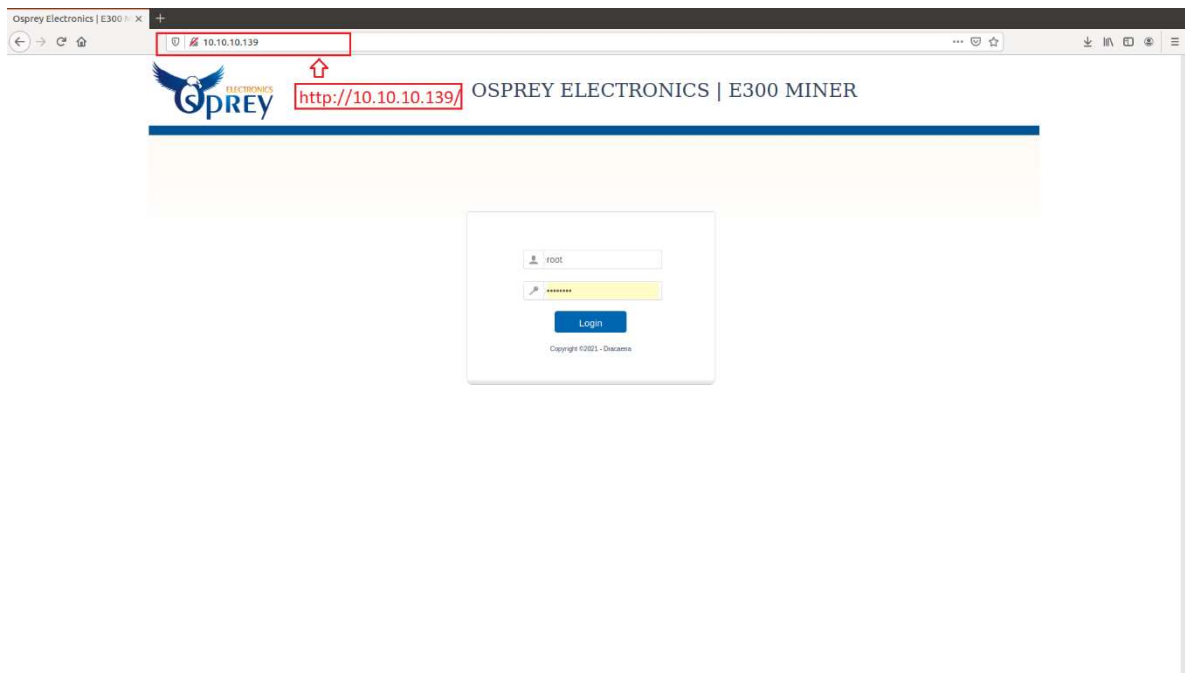
### 1. How to access E300 WebUI?

Step 1: Obtain your E300's IP address by logging into your network router and reviewing the DHCP leases for a device named ARM or installing an IP scanner tool at: (<https://www.advanced-ip-scanner.com>).

Step 2: Once you have the E300's IP address, open a web browser ( Chrome or Firefox are recommended) and insert this link in the address bar: `http://E300_ip_address/` , login window pop up will show up. Use the following credentials to log in:

User name: root

Password: password



### 2. How to mine Kaspas:

Step 1: Set vccINT to 635mv (please refer to 3 for how to change vccINT)

Step 2: Goto "Miner" webpage

Step 3: Fill in your mining pool, kaspas wallet, workerID, coreclock...

Step 4: Running miner by click the "start" button



## OSPREY ELECTRONICS | E300 MINER

FPGA status/settings

Fans speed adjustments

Miner

Update firmware

System settings

### Miner configuration

The ETC miner powered by Teamredminer team(their dev fee is 4%)

The kasper dev fee will be charged by the developers (TheAlfFather: updating..., Teamredminer: 10%)

Developer	:	Teamredminer
Miner status	:	stopped
Configuration method	:	Menu
Auto start	:	Enable
Algo	:	Kaspa
Pool	:	stratum+tcp://pool.woolpoolv.com:3112
Wallet	:	kaspa:qohhmahzmvaaw9cdf9wc920i4d569wh0fra350ax6i
WorkerID	:	E300
FPGA core clock	:	fpga0: 550 fpga1: 550 fpga2: 550
FPGA hbm clock	:	fpga0: 1100 fpga1: 1100 fpga2: 1100

valid hbm clock value from 900 to 1250; and hbm clock should double with core clock

START

**Note:** raising FPGA core clock help to increase hashrate, and need a higher vccInt value (please refer to 3 for how to change vccINT)

- How to set vccint voltage. (it may show “Unrecognized comm data, skipping” issue if vccint too high when mine kaspa)

Step 1: Stop mining tool (from “Miner” webpage)

Step 2: Tweak vccInt value

Please refer to the below table:



Result	<pre> #033[34;1m-----#033[0m #033[34;1mMining kas with 3 FPGA workers#033[0m #033[34;1mFPGA Board      DNA      CoreMHz MemMHz TCore TMem VccInt VccBRAM VccMem Power#033[0m #033[34;1m0      E335C      620.0   0.0   51C   0C   616mV  846mV   0mV   0W#033[0m #033[34;1m1      E335C      620.0   0.0   49C   0C   618mV  850mV   0mV   0W#033[0m #033[34;1m2      E335C      620.0   0.0   49C   0C   614mV  850mV   0mV   0W#033[0m #033[34;1m#033[0m #033[34;1mStats Uptime: 0 days, 00:04:30#033[0m #033[34;1mFPGA 0 [51C, fan 0%]   kas: 4.936Gh/s, avg 2.185Gh/s, pool 1.765Gh/s a:16 r:0 er:0.00#033[0m #033[34;1mFPGA 1 [49C, fan 0%]   kas: 4.936Gh/s, avg 2.189Gh/s, pool 2.019Gh/s a:18 r:0 er:0.00#033[0m #033[34;1mFPGA 2 [49C, fan 0%]   kas: 4.935Gh/s, avg 2.186Gh/s, pool 1.415Gh/s a:13 r:0 er:0.00#033[0m #033[34;1mTotal                  kas: 14.81Gh/s, avg 6.561Gh/s, pool 5.200Gh/s a:47 r:0#033[0m #033[34;1m----- Pool Status -----#033[0m #033[34;1mpool.woolypooly.com    kas: 13.33Gh/s, avg 5.901Gh/s, pool 5.200Gh/s a:47 r:0#033[0m #033[34;1m-----#033[0m Pool pool.woolypooly.com received new job. (job_id: 0008f9b2) Pool pool.woolypooly.com received new job. (job_id: 0008f9b3) Pool pool.woolypooly.com received new job. (job_id: 0008f9b4) Pool pool.woolypooly.com received new job. (job_id: 0008f9b5) #033[32;1mPool pool.woolypooly.com share accepted. (FPGA2) (a:48 r:0) (41 ms) (diff 719.63 GH)#033[0m #033[32;1mPool pool.woolypooly.com share accepted. (FPGA0) (a:49 r:0) (40 ms) (diff 145.27 GH)#033[0m Pool pool.woolypooly.com received new job. (job_id: 0008f9b6) #033[32;1mPool pool.woolypooly.com share accepted. (FPGA0) (a:50 r:0) (41 ms) (diff 46.88 GH)#033[0m Pool pool.woolypooly.com received new job. (job_id: 0008f9b7) Pool pool.woolypooly.com received new job. (job_id: 0008f9b8) #033[32;1mPool pool.woolypooly.com share accepted. (FPGA2) (a:51 r:0) (43 ms) (diff 55.62 GH)#033[0m #033[32;1mPool pool.woolypooly.com share accepted. (FPGA1) (a:52 r:0) (53 ms) (diff 95.93 GH)#033[0m #033[32;1mPool pool.woolypooly.com share accepted. (FPGA2) (a:53 r:0) (99 ms) (diff 164.62 GH)#033[0m #033[32;1mPool pool.woolypooly.com share accepted. (FPGA1) (a:54 r:0) (96 ms) (diff 233.94 GH)#033[0m Pool pool.woolypooly.com received new job. (job_id: 0008f9b9) Pool pool.woolypooly.com received new job. (job_id: 0008f9ba) Pool pool.woolypooly.com received new job. (job_id: 0008f9bb) </pre>
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Algorithm	Clock	vccInt	vccHBM
kHeavyHash (Kaspa)	601 - 633 MHz	635 - 649 mV	Kaspa doesn't use HBM
	600 MHz	625- 635 mV	Kaspa doesn't use HBM
	550 MHz	600 mV	Kaspa doesn't use HBM

#### 4. How to solve the high error issue?

The high error issue is relating to voltage. To solve it, we just need to increase several mV vccInt

Assume that your FPGA0 get high error rate issue.	<pre> #033[34;1m-----#033[0m kas: 5.013Gh/s, avg 1.780Gh/s, pool 1.521Gh/s a:85 r:0 er:2.39#033[0m kas: 5.037Gh/s, avg 1.792Gh/s, pool 1.772Gh/s a:99 r:0 er:0.37#033[0m kas: 5.038Gh/s, avg 1.781Gh/s, pool 1.682Gh/s a:94 r:0 er:0.46#033[0m kas: 15.09Gh/s, avg 5.353Gh/s, pool 4.975Gh/s a:278 r:0#033[0m ----- Pool Status ----- kas: 13.58Gh/s, avg 4.812Gh/s, pool 4.975Gh/s a:278 r:0#033[0m -----#033[0m </pre>
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**Solve by increasing vccInt of FPGA0**

### FPGA information

Index	Status	vccInt (mV)	vccHBM(mV)	Board Temperature °C	Chip Temperature °C	Maximum Temperature °C
0	Power on	618	1085	36	58	95
1	Power on	628	1095	35	35	95
2	Power on	631	1096	36	56	95

### Fans information

Currently level : 5

Fans level	Capacity
10	100%
9	90%
8	80%
7	70%
6	60%
5	50%
4	40%
3	30%
2	20%
1	10%

### Voltage settings (vccInt & vccHBM)

Select FPGA : FPGA0

vccInt for FPGA0 in mV (600mV - 950mV) : 635

vccHBM for FPGA0 in mV (1000mV - 1300mV) :

Select FPGA is needed to raise vccInt

update new value

Change Voltage
←
save

**Result**

```

3:16:00#033[0m
kas: 4.974Gh/s, avg 4.179Gh/s, pool 3.525Gh/s a:788 r:0 er:0.54#033

kas: 5.037Gh/s, avg 4.225Gh/s, pool 3.919Gh/s a:876 r:0 er:0.04#033

kas: 5.037Gh/s, avg 4.220Gh/s, pool 3.865Gh/s a:864 r:0 er:0.22#033

kas: 15.05Gh/s, avg 12.62Gh/s, pool 11.31Gh/s a:2528 r:0#033[0m
----- Pool Status -----

kas: 13.56Gh/s, avg 11.36Gh/s, pool 11.31Gh/s a:2528 r:0#033[0m
-----

```

5. Is there any way to monitor streaming log without login?

Yes, You monitor the streaming log by access.

[http://YOUR\\_E300\\_IP\\_ADDRESS:9001/?filter=start](http://YOUR_E300_IP_ADDRESS:9001/?filter=start)

tail -f /var/log/syslog
Osprey Electronics | E300 N

172.27.35.25:9001/?filter=start

tail -f /var/log/syslog

```

Mar 22 07:38:04 arm starteth.sh[15770]: [2023-03-22 07:38:04] Pool pool.woolypooly.com received new job. (job_id: 0008f9a3)
Mar 22 07:38:05 arm starteth.sh[15770]: [2023-03-22 07:38:05] #033[32;1mPool pool.woolypooly.com share accepted. (FPGA1) (a:42 r:0) (42 ms) (diff 100.63 GH)#033[0m
Mar 22 07:38:07 arm starteth.sh[15770]: [2023-03-22 07:38:07] #033[32;1mPool pool.woolypooly.com share accepted. (FPGA0) (a:43 r:0) (42 ms) (diff 239.92 GH)#033[0m
Mar 22 07:38:08 arm starteth.sh[15770]: [2023-03-22 07:38:08] #033[33;1mPool pool.woolypooly.com set difficulty to 9.000000#033[0m
Mar 22 07:38:08 arm starteth.sh[15770]: [2023-03-22 07:38:08] Pool pool.woolypooly.com received new job. (job_id: 0008f9a4)
Mar 22 07:38:09 arm starteth.sh[15770]: [2023-03-22 07:38:09] Pool pool.woolypooly.com received new job. (job_id: 0008f9a5)
Mar 22 07:38:09 arm starteth.sh[15770]: [2023-03-22 07:38:09] Pool pool.woolypooly.com received new job. (job_id: 0008f9a6)
Mar 22 07:38:11 arm starteth.sh[15770]: [2023-03-22 07:38:11] Pool pool.woolypooly.com received new job. (job_id: 0008f9a7)
Mar 22 07:38:12 arm starteth.sh[15770]: [2023-03-22 07:38:12] Pool pool.woolypooly.com received new job. (job_id: 0008f9a8)
Mar 22 07:38:13 arm starteth.sh[15770]: [2023-03-22 07:38:13] Pool pool.woolypooly.com received new job. (job_id: 0008f9a9)
Mar 22 07:38:14 arm starteth.sh[15770]: [2023-03-22 07:38:14] Pool pool.woolypooly.com received new job. (job_id: 0008f9aa)
Mar 22 07:38:14 arm starteth.sh[15770]: [2023-03-22 07:38:14] Pool pool.woolypooly.com received new job. (job_id: 0008f9ab)
Mar 22 07:38:15 arm starteth.sh[15770]: [2023-03-22 07:38:15] Pool pool.woolypooly.com received new job. (job_id: 0008f9ac)
Mar 22 07:38:16 arm starteth.sh[15770]: [2023-03-22 07:38:16] #033[32;1mPool pool.woolypooly.com share accepted. (FPGA0) (a:44 r:0) (41 ms) (diff 82.99 GH)#033[0m
Mar 22 07:38:16 arm starteth.sh[15770]: [2023-03-22 07:38:16] #033[32;1mPool pool.woolypooly.com share accepted. (FPGA1) (a:45 r:0) (41 ms) (diff 86.46 GH)#033[0m
Mar 22 07:38:18 arm starteth.sh[15770]: [2023-03-22 07:38:18] Pool pool.woolypooly.com received new job. (job_id: 0008f9ad)
Mar 22 07:38:19 arm starteth.sh[15770]: [2023-03-22 07:38:19] Pool pool.woolypooly.com received new job. (job_id: 0008f9ae)
Mar 22 07:38:20 arm starteth.sh[15770]: [2023-03-22 07:38:20] #033[32;1mPool pool.woolypooly.com share accepted. (FPGA1) (a:46 r:0) (43 ms) (diff 48.61 GH)#033[0m
Mar 22 07:38:21 arm starteth.sh[15770]: [2023-03-22 07:38:21] #033[32;1mPool pool.woolypooly.com share accepted. (FPGA1) (a:47 r:0) (42 ms) (diff 73.39 GH)#033[0m
Mar 22 07:38:22 arm starteth.sh[15770]: [2023-03-22 07:38:22] Pool pool.woolypooly.com received new job. (job_id: 0008f9af)
Mar 22 07:38:23 arm starteth.sh[15770]: [2023-03-22 07:38:23] Pool pool.woolypooly.com received new job. (job_id: 0008f9b0)
Mar 22 07:38:24 arm starteth.sh[15770]: [2023-03-22 07:38:24] Pool pool.woolypooly.com received new job. (job_id: 0008f9b1)

```

6. What is the normal temperature while mining Kaspa?

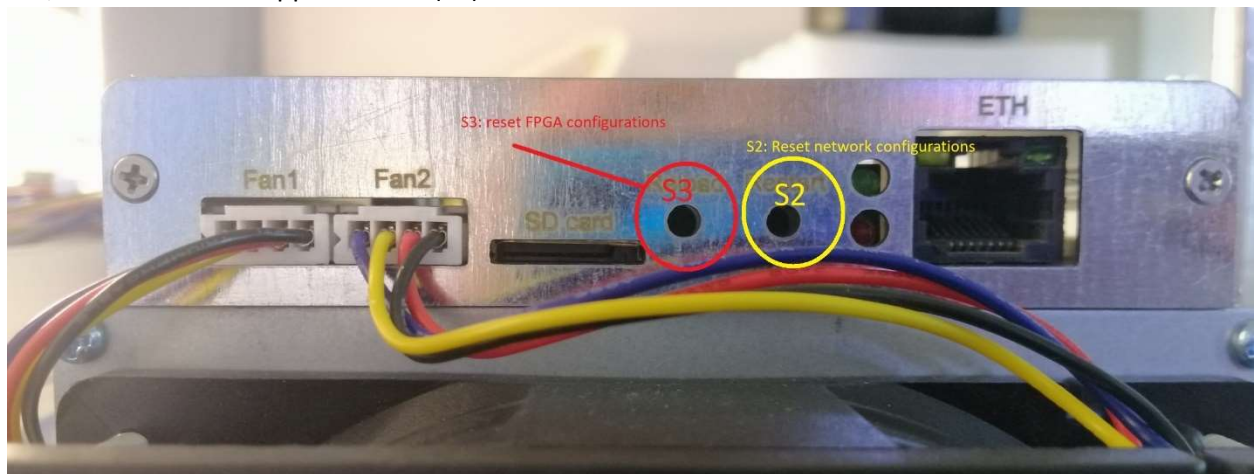
The normal chip temperature ranges from 50°C ~ 60 °C in the winter, and 60°C ~ 70°C in the summer.

7. How often should you clean the E300 miners?

It is recommended that Ospreys should clean hashing boards to remove dust and external objects every three (03) months. This task is not time-consuming and helps your boards work more reliable and long-lasting.

8. Is there any hard reset button on E300 miner?

Yes, the E300 miner supported two (02) reset button.



S2 button: Hold the S2 button for **over 5 seconds** to reset factory network configurations

S3 button: Hold the S3 button for **over 5 seconds** to reset factory FPGA (hashing boards) configurations

9. How to troubleshoot the continuous rebooting issue?

Step 1: ssh into E300 box ; user name: ubuntu ; password: temppwd

Step 2: Run two command lines below

```
sudo systemctl stop dhcp_reset  
  
sudo systemctl disable dhcp_reset
```

10. How can you contact us for support

Join our discord at <https://discord.gg/F86rAyYGNP>

